The Development Solution for Programmable Integrated Circuits

DATA I/O Corporation
The 2900 Programming System programs virtually every programmable logic and memory device on the market—even multiple package types. Its modular architecture allows you to tailor device support options for today’s budget. And the 2900 satisfies your programming requirements for many years to come with universal pin driver technology and other innovative features.

Customize your programmer with device libraries. The 2900 offers you a convenient, new approach to ongoing support: device libraries. The algorithms have been organized so that you can choose just the device support you need today. As your needs expand, you can add device libraries with simple software updates.

New technology for easy programming of surface-mount devices. The 2900 has greatly simplified handling different device packages. All DIP packages can be programmed from a single socket. PLCCs and LCCs—in all their variations—can be programmed from a single base. Using Data I/O®’s ingenious new Matchbook™ carrier technology, all package sizes are accommodated with simple, inexpensive chip carriers. This eliminates the mechanical problems and expense of sockets for surface-mount devices.

All the features you expect in a high-performance programmer, at a price you can afford.
- The universal pin driver technology eliminates pinout adapters and gives you long-term flexibility in device support.
- Floppy disks provide fast and easy device-support updates and local storage of frequently used files.
- 128K standard RAM is expandable to 2MB to accommodate future devices.
- With its flexible interfacing, the 2900 fits easily into any engineering environment.

### Device libraries

<table>
<thead>
<tr>
<th>Device type</th>
<th>Library</th>
<th>Devices included</th>
</tr>
</thead>
<tbody>
<tr>
<td>Memory</td>
<td>E/EEPROM 28</td>
<td>All EPROMs and EEPROMs up to 28 pins</td>
</tr>
<tr>
<td>Memory</td>
<td>E/EEPROM All</td>
<td>All EPROMs and EEPROMs</td>
</tr>
<tr>
<td>Memory</td>
<td>PROM All</td>
<td>All bipolar and CMOS PROMs</td>
</tr>
<tr>
<td>Microcontrollers</td>
<td>E-micros All</td>
<td>All microcontrollers with on-board EPROMs</td>
</tr>
<tr>
<td>Logic</td>
<td>PLD 24</td>
<td>All PLDs up to 24 pins</td>
</tr>
<tr>
<td>Logic</td>
<td>PLD 28</td>
<td>All PLDs up to 28 pins</td>
</tr>
<tr>
<td>Logic</td>
<td>PLD 44</td>
<td>All PLDs up to 44 pins</td>
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</tbody>
</table>

Control it from your existing terminal, or from a PC.

- High-speed I/O offers fast uploading and downloading of large data files—up to 115K baud—between the 2900 and your PC. It's up to ten times faster than conventional RS232.
- For RS232 communication, SmartPort™ automatically senses the protocol of the host, and sets up the 2900 port to match. There are no cables to rewire, no switches to set.
- Job files and macros make setups automatic for frequently used routines.
- Autobaud™ automatically senses the baud rate of the host and sets up the 2900 to send or receive at the correct rate.

Your Personal Silicon Foundry for programmable logic devices.

Data I/O offers a complete set of complementary tools for your 2900 Programming System. You can create your own Personal Silicon Foundry™; a complete solution for PLD development, from design entry and verification to programming and test.

FutureNet DASH Schematic Designer: industry-standard schematic capture for PLDs. FutureNet® DASH™ makes it easy to move directly from discrete parts to PLDs. It offers fast, smart editing of designs, and hierarchical symbol libraries for full device support. Schematics are transferred via the PLDiinx™ translator directly to ABEL™ PLD Design Software.

ABEL: industry-standard behavioral design entry and verification. Use this high-level language to describe your designs as equations, truth tables or state diagrams. It supports virtually every PLD on the market—the broadest support available.

Device-level verification and superior logic reduction help you create smaller, faster, more testable designs. And the industry-standard JEDEC files allow you to program your PLDs with the 2900.

PLDtest® Plus: for PLD testability and automatic test vector generation. Fault grading capabilities help design engineers evaluate fault coverage, pinpointing areas of the circuit that need redesign to achieve full testability.

For test engineers, PLDtest Plus™ generates a comprehensive set of PLD test vectors for both preloadable and non-preloadable PLDs. It gives you the highest fault coverage of any automatic test vector generation (ATVG) software.
A legacy of reliability. The 2900 has been designed for years of high-reliability programming. Manufacturer-approved algorithms give you confidence in the integrity of your programmed parts. High-accuracy programming ensures high yield. And high-quality construction means dependability.

Data I/O is always ready to support you. With a worldwide sales and service network, and comprehensive technical support, Data I/O offers unequaled customer service. Update service agreements assure you of prompt support for new devices and enhancements. With Data I/O, support you can depend on is always just a phone call away.
Out-of-this-world performance at a down-to-earth price.

The new 2900 Programming System brings the cost of high-performance programming down to earth. Buy only the device libraries you need today and expand capability when you need it with simple software updates.

The 2900 supports virtually every programmable logic and memory device on the market—even surface-mount packages. Its innovative technology makes programming faster, easier, and more affordable than ever before. And with Data I/O's industry-standard design and testing software, you can create a complete PLD development solution.

For more than 15 years, Data I/O has set the standard in device programming. Call today to learn how the 2900 is setting a new standard for both price and performance.

Call today for a FREE tutorial.

1-800-247-5700