NOTE: Placement of New Device Sections or Revisions on Older Sections.

- New Device Sections should be placed in the Pocket Reference Book in their entirety.

- Revisions on Older Sections will replace the existing page in the Pocket Reference Book. Only the sides which have changes show a revision date.

- For additional copies and/or suggestions for improvement, contact the Training and Documentation Department.
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Note: The table likely represents a list of hardware components or devices, with each row denoting a different piece of equipment.
6804  TEK-COM MODEM (202T)
5065  TELETYPERPTR (1450)
5066  TELETYPERPTR (2-3XX)
5061  TELETYPERPTR 64ch-37
5062  TELETYPERPTR 96ch-37
5063  TELETYPERPTR 64(210)
5064  TELETYPERPTR 96(210)
5701  TEN KEY DEVICE-BUFF
5700  TEN KEY DEVICE-UNBUFF
2904  TPA
2903  TPU CONSOLE
5067  TTY PTR (QSPI/1400)
5068  TTY PTR (QSPI/2-3XX)
6801  VADIC 1205G W/REV.
6802  VADIC 1230K WO/REV
6803  VADIC 34XX DIAL-UP
4026  VT2
4082  VT2 ON 120/210 SYS
4031  VT3 Terminal
3011  W-D DISC 3+3
3111  W-D DISC 6+6
3010  W-D DISC 3+3 2XX/3XX
3110  W-D DISC 6+6 2XX/3XX
3114  W-D S/A 6+6 2XX/3XX
3113  W-D S/A 6+6 (1400)
3112  W-D S/A 6+6 (900)
3112  W-D S/A 6+6 (900)
3481 IOU -32A/B (DMA) 5010 BULL PRINTER
3485 IOU-32A/B (NON-DMA) 5011 DIGITRONIC PRINTER
3486 IOU-42 FOR MEMOREX 5018 IOU-05, 5R
3487 IOU-42 FOR MARKSMAN 5019 IOU-27
3488 IOU-43 5021 DP 2420 PTR 64ch
4001 QCRT1 5022 DP 2420 PTR 96ch
4011 QCRT2 (27 LINE) 5023 IOU-23
4012 QCRT2 W/LINE DRIVERS 5031 DP 2440 PTR 64ch
4013 QCRT2 2400/4800 BAUD 5032 DP 2440 PTR 96ch
4026 VT2 5041 DP 2230 PTR 64ch
4031 VT3 Terminal 5042 DP 2230 PTR 96ch
4081 DTC CRT CONTR. 5046 DP 2270 VIDEO PTR-64
4082 VT2 ON 120/210 SYS 5047 DP 2230 VIDEO PTR-96
4091 DTC KEYBOARD 5049 IOU-30
4101 HAZELTINE CRT 5051 DP 2260 PTR 64ch
4201 IBM SELECTRIC/IOU-10 5052 DP 2260 PTR 96ch
4202 IBM SELECTRIC/IOU-1 I 5061 TELETYPE PTR 64ch-37
4208 IOU-01A/B 5062 TELETYPE PTR 96ch-37
4209 IOU-10 5063 TELETYPE PTR-64(210)
4301 QUME W/KEYBD 5064 TELETYPE PTR-96(210)
4302 QUME W/KEYBD (800) 5065 TELETYPE PTR (1450)
4306 QUME W/KEYBD-BMFEED 5066 TELETYPE PTR (2-3XX)
4321 QUME VIDEO PTR 5067 TTY PTR (QSP/1400)
4326 QUME VIDEO PTR-BMFEED 5068 TTY PTR (QSP/2-3XX)
4341 QUME AUX PTR 5069 IOU-37
4342 QUME AUX PTR(900SYS) 5131 QPTR 150CPS-13" W/39
4346 QUME AUX PTR-BMFEED 5132 QPTR 150CPS-13"(QSP)
4381 IOU-31 5133 QPTR 150CPS-13"(210)
4421 NEC VIDEO PTR 5136 QPTR 150CPS-13"VIDEO
4426 NEC VIDEO PTR-BMFEED 5180 IOU-39P
4431 NEC AUX PTR (QSP) 5200 PEC TAPE 800-12.5IPS
4436 NEC PTR-BMFEED (QSP) 5201 PEC TAPE 800-7"25IPS
4441 NEC AUX PTR 5202 KENNEDY TAPE 800 BPI
4446 NEC AUX PTR-BMFEED 5203 DIGI 800 BPI (1400)
4447 NEC PTR FRONT-FEED 5204 DIGI 800-CE (1400)
4451 NEC AUX PTR(120/210) 5205 DIGI 800 (2-3XX)
4456 NEC PTR-BMFEED (210) 5206 DIGI 800 (2-3XX) S/A
4501 IOU-28 A/B, A/C 5209 PEC TAPE 800-10.5"S
4502 IOU-28 24/4800 BAUD 5211 PEC TAPE 1600-7.0"S
4503 IOU-39T 5212 KENNEDY TAPE 1600
4504 IOU-39Q 5213 DIGI 1600 (1400)
4505 REMOTE DEV INTERFACE 5214 DIGI 1600-CE (1400)
4531 HUB UNIT 5215 DIGI 1600 (2-3XX)
4532 LINE DRIVER 5216 DIGI 1600 (2-3XX) S/A
4535 QSP CABLE TERMINATOR 5280 IOU-04 A/B, A/C
4921 QUME FORMS TRACTOR 5281 IOU-20A/B 25IPS
4922 QUME PINFEED PLAIN 5282 IOU-21A/B 25IPS
5001 CENTRONICS 101 PTR 5285 IOU-20A/B 85IPS
5002 CENTRONICS 702 PTR. 5286 IOU-21A/B 85IPS
5003 CENT. PTR. W/IOU-39 5300 D.P. CARD READER
5004 CENTRONICS PTR (210) 5301 P.D.I. CARD READER
5005 CENTRONICS PTR (QSP) 5308 IOU-06
5006 CENT. 702 VIDEO PTR. 5309 IOU-26
5009 IOU-17 5311 BADGE READER

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5400 ROYTRON PAPER TAPE
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5700 TEN KEY DEVICE-UNBUF
5701 TEN KEY DEVICE-BUFF
5708 IOU-09
5709 IOU-18
6000 IOU-03
6001 IOU-15A/B SYNC.
6011 DTC COM SYNC
6100 IOU-11A/C
6101 IOU-15A/B ASYNC.
6111 DTC COM ASYNC
6200 IOU-11A/B
6201 IOU-15A/C RCU MIX
6202 IOU-22
6203 IOU-40
6300 IOU-11A
6301 IOU-15A/E AUDIO
6801 VADIC 1205G W/REV.
6802 VADIC 1230K WC/REV
6803 VADIC 34XX DIAL-UP
6804 TEK-COM MODEM (202T)
6811 ASYNC MODEM ELIMINAT
6852 ASYNC-TO-SYNC CONVTR
9998 SOFTWARE
9999 SYSTEM
SELECTRIC POWER

115 VAC 60 HZ.
1.5A FOR .05 SEC.
0.7A NORMAL

PRINTER HEAT GEN.

300 BTU/HR.

WRITE CONTROL INSTRUCTIONS

\[ \text{IXYY9D} \]
\[ X = \text{DEVICE ADDRESS} \]
\[ YY = \text{CONTROL BYTE} \]

CONTROL FUNCTION BYTE

00 NULL
01 SIG 1 OFF
02 SIG 2 OFF
03 SIGS 1 & 2 OFF
05 SIG 1 ON
06 SIG 2 ON
07 SIGS 1 & 2 ON
09 SET TEST

STATUS IN INSTRUCTIONS

\[ \text{4XYY9D} \]
\[ X = \text{DEVICE ADDRESS} \]
\[ YY = \text{STATUS BYTE} \]

STATUS MEANING BYTE

01 READ BUSY
02 WRITE BUSY
04 END
08 SERVICE REQUEST
10 FLAG 1/INTERRUPT
20 FLAG 2
40 FLAG 3
80 ERROR

Revised 11/77
IOU-5 OR 5R

**MAX. PWR. REQUIREMENTS**

- $12V = 0.13A$
- $+12V = 0.13A$
- $5V = 0.30A$
- $+26V = 0.03A$

**PRINTER POWER**

115 VAC 60 HZ.
12.5A FOR 1.2 SEC.
4.0A NORMAL

**PRINTER HEAT GEN.**

550 BTU/HR.

**WRITE CONTROL INSTRUCTIONS**

$1XYY9D$  $X =$ DEVICE ADDRESS  $YY =$ CONTROL BYTE

**CONTROL BYTE**

- **30**: GO TO CHANNEL 0
- **31**: GO TO CHANNEL 1
- **32**: GO TO CHANNEL 2
- **33**: GO TO CHANNEL 3
- **34**: GO TO CHANNEL 4
- **35**: GO TO END OF FORM
- **36**: GO TO TOP OF FORM
- **37**: ADVANCE ONE LINE

**STATUS INSTRUCTIONS**

$4XYY9D$  $X =$ DEVICE ADDRESS  $YY =$ STATUS BYTE

**STATUS BYTE**

- **01**: N/A
- **02**: WRITE BUSY
- **04**: END
- **08**: SERVICE REQUEST
- **10**: N/A
- **20**: N/A
- **40**: END OF FORM
- **80**: INOPERABLE

1-2  Revised 11/77
**IOU-10 SELECTRIC QANTEL MODEL 4209**

**MAX. PWR. REQUIREMENTS**

- $12V = 0.19A$
- $+12V = 0.18A$
- $+5V = 1.35A$
- $+26V = 0.92A$

**SELECTRIC POWER**

115 VAC 60 Hz.
1.5A for .05 sec.
0.7A normal

**SELECTRIC HEAT GEN.**

300 BTU/HR.

**WRITE CONTROL INSTRUCTIONS**

1XY9YD  X = DEVICE ADDRESS  YY = CONTROL BYTE

<table>
<thead>
<tr>
<th>FUNCTION BYTE</th>
<th>01</th>
<th>02</th>
<th>03</th>
<th>05</th>
<th>06</th>
<th>07</th>
<th>08</th>
<th>09</th>
<th>10</th>
<th>14</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>RESET SIGNAL 1</td>
<td>RESET SIGNAL 2</td>
<td>RESET SIGNAL 1 AND 2</td>
<td>SET SIGNAL 1</td>
<td>SET SIGNAL 2</td>
<td>SET SIGNAL 1 AND 2</td>
<td>FILL BUFFER ONLY/READ CORR.</td>
<td>READ ASCII/PRINT CONT.</td>
<td>PRINT AND COLLECT ECHO</td>
<td>SET ALLOW TERMINATION INTERRUPT</td>
</tr>
</tbody>
</table>

**STATUS INSTRUCTIONS**

4XY9YD  X = DEVICE ADDRESS  YY = STATUS BYTE

<table>
<thead>
<tr>
<th>MEANING BYTE</th>
<th>01</th>
<th>02</th>
<th>04</th>
<th>08</th>
<th>10</th>
<th>20</th>
<th>40</th>
<th>80</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>READ BUSY</td>
<td>WRITE BUSY</td>
<td>END</td>
<td>SERVICE REQUEST</td>
<td>FLAG 1/INTERUPT</td>
<td>FLAG 2</td>
<td>FLAG 3</td>
<td>INOPERABLE</td>
</tr>
</tbody>
</table>

1-3  Revised 11/77
IOU-17
CENTRONICS PRINTERS
QANTEL MODEL 5009

IOU-17

MAX. PWR. REQUIREMENTS
+5V .32A
-12V .44A

PRINTER POWER
115 VAC 60 HZ.
2.8A MAX. FOR .1 SEC.
1.5A NORMAL

I/O CABLE LENGTH
15 FT. ONLY

60-100 LPM FOR 64 OR 96 CHAR.

WRITE CONTROL INSTRUCTIONS

1XY9D X - DEVICE ADDRESS YY - CONTROL BYTE

CONTROL FUNCTION
BYTE FUNCTION
01 VERTICAL TAB
06 FORM FEED
07 LINE FEED
0E EXPAND PRINT - USE SOE IN DATA STREAM INSTEAD
20 RESET ALLOW TERMINATION INTERRUPT
60 SET ALLOW TERMINATION INTERRUPT

STATUS IN INSTRUCTIONS

4XY9D X - DEVICE ADDRESS YY - STATUS BYTE

STATUS MEANING
BYTE MEANING
00 N/A
02 WRITE BUSY
04 END
08 SERVICE REQUEST
10 N/A
20 N/A
40 N/A
80 INOPERABLE

1-4 Revised 5/82
IOU-23
DATA PRODUCTS 2420/2440
QANTEL MODEL 5029

MAX. PWR. REQUIREMENTS

+5V .08A
+12V .67A

PRINTER POWER

115 VAC 60 Hz.
2420
15A FOR 2.5 SEC.
7.8A NORMAL

2440
25A FOR 3.0 SEC.
13A NORMAL

PRINTER HEAT GEN.

2420 = 3075 BTU/HR.
2440 = 5125 BTU/HR.

WRITE CONTROL INSTRUCTIONS

1XY9D X = DEVICE ADDRESS YY = CONTROL BYTE

CONTROL BYTE

FUNCTION

0X SLEW TO CHANNEL X (X = 1-7)
1X SLEW X LINES (X = 1-F)
20 RESET ALLOW TERMINATION INTERRUPT
60 SET ALLOW TERMINATION INTERRUPT

STATUS IN INSTRUCTIONS

4XY9D X = DEVICE ADDRESS YY = STATUS BYTE

STATUS BYTE

MEANING

01 ALWAYS ZERO
02 WRITE BUSY
04 END
08 SERVICE REQUEST
10 INTERRUPT
20 ALWAYS ZERO
40 ALWAYS ZERO
80 INOPERABLE

1-5 Revised 11/77
WRITE CONTROL INSTRUCTIONS

1XY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE
FUNCTION

CONTROL

BYTE

00  SKIP TO CHANNEL 8 - INTERFACE TOF
01  SKIP TO CHANNEL 1 - PRINTER TOF
02  SKIP TO CHANNEL 2
03  SKIP TO CHANNEL 3
04  SKIP TO CHANNEL 4
05  SKIP TO CHANNEL 5
06  SKIP TO CHANNEL 6
07  SKIP TO CHANNEL 7
11  LINE FEED
12  LINE FEED 2 LINES
20  RESET ALLOW TERMINATION INTERRUPT
30  SET ALLOW TERMINATION INTERRUPT

STATUS IN INSTRUCTIONS

4XY9D  X = DEVICE ADDRESS  YY = STATUS BYTE
MEANING

STATUS

BYTE

01  ALWAYS ZERO
02  BUSY - WRITE, PRINTER, PAPER ADVANCE
03  END
04  SERVICE REQUEST
05  INTERRUPT
06  ALWAYS ZERO
07  ALWAYS ONE
08  INOPERABLE

Revised 11/77
MAX. PWR. REQUIREMENTS

+5V = .59A

ADDRESS SWITCHES 0-F

WRITE CONTROL INSTRUCTIONS

1XY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

CONTROL FUNCTION

ON SLEW TO CHANNEL N (N = 0-3)
1N SLEW N LINES (N = 0-3)
20 RESET ALLOW TERMINATION INTERRUPT
30 SUPPRESS LINE FEED
60 SET ALLOW TERMINATION INTERRUPT

STATUS IN INSTRUCTIONS

4XY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

STATUS MEANING

BYTE

01 N/A
02 WRITE BUSY
04 END
08 SERVICE REQUEST
10 INTERRUPT
20 N/A
40 N/A
80 INOPERABLE

1-7 Revised 11/77
Rev L for Q264

IOU-30
DATA PRODUCTS 2230/2260
QANTEL MODEL 5049

ADDRESS SWITCHES 0-F

P1
30

P2
50

P3
35

P4
35

MAX. PWR. REQUIREMENTS

+5V = .71A

PRINTER POWER

115 VAC 60 HZ.
9.0A FOR 2.0 SEC.
4.6A NORMAL

PRINTER HEAT GEN.

1840 BTU/HR.

STATUS IN INSTRUCTIONS

4XYY9D X-DEVICES ADDRESS
YY=STATUS BYTE

STATUS MEANING

BYTE

01 ALWAYS ZERO
02 WRITE BUSY
04 END
08 SERVICE REQUEST
10 INTERRUPT
20 VFU LD. REQUEST
40 ALWAYS ONE
80 INOPERABLE

WRITE CONTROL INSTRUCTIONS

1XYY9D X-DEVICE ADDRESS YY=CONTROL BYTE

CONTROL FUNCTION BYTE

00 SLEW TO CHANNEL 0 (TOP OF FORM)
01 SLEW TO CHANNEL 1
02 SLEW TO CHANNEL 2
03 SLEW TO CHANNEL 3
04 SLEW TO CHANNEL 4
05 SLEW TO CHANNEL 5
06 SLEW TO CHANNEL 6
07 SLEW TO CHANNEL 7
08 SLEW TO CHANNEL 8
09 SLEW TO CHANNEL 9
0A SLEW TO CHANNEL 10
0B SLEW TO CHANNEL 11 (BOTTOM OF FORM)
0X SLEW TO LINE X
1X SLEW X LINES
20 RESET ALLOW TERMINATION INTERRUPT
60 SET ALLOW TERMINATION INTERRUPT
EE START DAVFU MEMORY LOAD
EF STOP DAVFU MEMORY LOAD

Revised 11/77
IOU-31
QUME
QANTEL MODEL 4381

**STATUS IN INSTRUCTIONS**

4XY9D X=DEV. ADDRESS
YY=STATUS BYTE

**STATUS MEANING**

<table>
<thead>
<tr>
<th>BYTE</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY</td>
</tr>
<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERV. REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>FLAG 1 (INT.)</td>
</tr>
<tr>
<td>20</td>
<td>FLAG 2</td>
</tr>
<tr>
<td>40</td>
<td>FLAG 3</td>
</tr>
<tr>
<td>80</td>
<td>INOPERABLE</td>
</tr>
</tbody>
</table>

**PRINTER POWER**

115 VAC 60 HZ,
4.0A FOR .1 SEC.
1.93A NORMAL

**WRITE CONTROL INSTRUCTIONS**

1XY9D X = DEVICE ADDRESS
YY = CONTROL BYTE

**CONTROL FUNCTION**

<table>
<thead>
<tr>
<th>BYTE</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>RESET SIG. 1</td>
</tr>
<tr>
<td>02</td>
<td>RESET SIG. 2</td>
</tr>
<tr>
<td>03</td>
<td>RESET SIG. 1 &amp; 2</td>
</tr>
<tr>
<td>04</td>
<td>SET BLIND ENTRY</td>
</tr>
<tr>
<td>05</td>
<td>SET SIG. 1</td>
</tr>
<tr>
<td>06</td>
<td>SET SIG. 2</td>
</tr>
<tr>
<td>07</td>
<td>SET SIG. 1 &amp; 2</td>
</tr>
<tr>
<td>09</td>
<td>SET BLACK RIBBON</td>
</tr>
<tr>
<td>0A</td>
<td>RESET ATI (WRITE)</td>
</tr>
<tr>
<td>0B</td>
<td>SET 8 LINES/INCH</td>
</tr>
<tr>
<td>0C</td>
<td>SET KANA MODE</td>
</tr>
<tr>
<td>0D</td>
<td>SET RED RIBBON</td>
</tr>
<tr>
<td>0E</td>
<td>SET ATI (WRITE)</td>
</tr>
<tr>
<td>0F</td>
<td>SET 6 LINES/INCH</td>
</tr>
<tr>
<td>10</td>
<td>RESET ATI (READ)</td>
</tr>
<tr>
<td>12</td>
<td>SET 12 CH./INCH</td>
</tr>
<tr>
<td>13</td>
<td>LOAD TAB REGISTER</td>
</tr>
<tr>
<td>14</td>
<td>SET ATI (READ)</td>
</tr>
<tr>
<td>15</td>
<td>SET DUAL FRAME</td>
</tr>
<tr>
<td>22</td>
<td>SET 12 CH./INCH</td>
</tr>
<tr>
<td>24</td>
<td>SET 8 CH./INCH</td>
</tr>
</tbody>
</table>

Revised 11/77
**TYPICAL POWER REQUIREMENTS**

-12V = .100A  
+12V = .150A  
+5V = 1.5A

**CONFIGURATION SWITCHES:**
Set for Printer Type and Baud Rate per the following Bit Values.

### BAUD RATE

<table>
<thead>
<tr>
<th>BIT VALUES</th>
<th>RATE</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0</td>
<td>300 BPS</td>
</tr>
<tr>
<td>0 1</td>
<td>1200 BPS</td>
</tr>
<tr>
<td>1 0</td>
<td>2400 BPS</td>
</tr>
<tr>
<td>1 1</td>
<td>9600 BPS</td>
</tr>
</tbody>
</table>

### PRINTER TYPE

<table>
<thead>
<tr>
<th>BIT VALUES</th>
<th>PRINTER TYPE</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td>NONE</td>
</tr>
<tr>
<td>0 0 1</td>
<td>CENTRONICS (Reverse Channel Mode)</td>
</tr>
<tr>
<td>0 1 0</td>
<td>CENTRONICS (REV-3; using ETX / ACK)</td>
</tr>
<tr>
<td>0 1 1</td>
<td>TELETYPE 40</td>
</tr>
<tr>
<td>1 0 0</td>
<td>NEC SPINWRITER</td>
</tr>
<tr>
<td>1 0 1</td>
<td>NONE</td>
</tr>
<tr>
<td>1 1 0</td>
<td>GAMTEL 51XX</td>
</tr>
<tr>
<td>1 1 1</td>
<td>NONE</td>
</tr>
</tbody>
</table>

### MODEM REMOTE/LOCAL

<table>
<thead>
<tr>
<th>BIT VALUES</th>
<th>REMOTE/LOCAL</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>Direct to printer or line driver</td>
</tr>
<tr>
<td>10</td>
<td>Connected to Modem</td>
</tr>
</tbody>
</table>

BIT VALUES $02$, $01$ equal DON'T CARE Values. **SET TO 0**
## WRITE CONTROL INSTRUCTIONS

1dyy90  d=Device Address  yy=Control Byte

<table>
<thead>
<tr>
<th>Write Control Byte</th>
<th>IOU39-P</th>
<th>CENTRONICS</th>
<th>TELETEYPE</th>
<th>NEC</th>
<th>51xx</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>$00</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reset Interrupt</td>
</tr>
<tr>
<td>10</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Reset Allow Termination Interrupt</td>
</tr>
<tr>
<td>14</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Set Allow Termination Interrupt</td>
</tr>
<tr>
<td>18</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Soft IPL</td>
</tr>
<tr>
<td>60</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Diagnostic Write</td>
</tr>
<tr>
<td>73</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Dump Trace Buffer</td>
</tr>
<tr>
<td>30</td>
<td>X X X X X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Line Supress</td>
</tr>
<tr>
<td>06</td>
<td>X X X X X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Top of Form</td>
</tr>
<tr>
<td>07</td>
<td>X X X X X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Line Feed</td>
</tr>
<tr>
<td>0F</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Set 6 LPI</td>
</tr>
<tr>
<td>08</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Set 8 LPI</td>
</tr>
<tr>
<td>52</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Set 8 CPI</td>
</tr>
<tr>
<td>12</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Set 10 CPI</td>
</tr>
<tr>
<td>32</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Set 12 CPI</td>
</tr>
<tr>
<td>0D</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Select Red Ribbon</td>
</tr>
<tr>
<td>49</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Select Black Ribbon</td>
</tr>
</tbody>
</table>
**STATUS IN INSTRUCTIONS**

\[ \text{4dyy9D} \quad d=\text{Device Address} \quad yy=\text{Test Bit Value} \]

**STATUS BYTE FUNCTION**

- $\text{80} -- \text{Inoperable}
- 40 -- Always 0
- 20 -- Always 0
- 10 -- Termination
- 08 -- Service Request
- 04 -- End
- 02 -- Write Busy
- 01 -- Read Busy

**READ STATUS 2**

\[ \text{xdyy86} \quad d=\text{DEVICE ADDRESS} \quad x=\text{DON'T CARE} \quad yy=\text{TEST BIT VALUE} \]

$\text{5A1} -- \text{Indicates IOU-39 is in Printer Mode of operation.}$

**SPECIAL CONTROLLED WRITE**

**NOTE:** Teletype 40 will interface with IOU-39P only through a Device 39. (Dev 39 receives its power through the Teletype 40.)
The Remote Device Interface (R.D.I.) is a single card Z-80 controlled asynchronous communication protocol converter. Its primary function is to allow a Standard RS-232 printer to be linked to the 10U 39Q daisy chain along with Video Terminals.

Note: Use of this device requires the following considerations:
1) Configuration switches of the 10U-39Q network master (See VT-3 Section pg. 4-25.)
2) Operating System configuration as determined by CFIG
3) Cables must conform to proper installation procedures (See VT-3 Section pg. 4-27.)

Service LED 5 conditions:

<table>
<thead>
<tr>
<th>PRINT</th>
<th>QSP NETWORK</th>
</tr>
</thead>
<tbody>
<tr>
<td>1) Dark</td>
<td>OK</td>
</tr>
<tr>
<td>2) Bright</td>
<td>Inoperative</td>
</tr>
<tr>
<td>3) Mostly Dark</td>
<td>OK</td>
</tr>
<tr>
<td>4) Mostly Bright</td>
<td>Inoperative</td>
</tr>
</tbody>
</table>

5) Flashing at Approximately 1 sec: RDI/NPC FAILURE:
   a) BAD FIRMWARE
   b) FIRMWARE/HARDWARE INCOMPATIBLE
   c) ILLEGAL NETWORK ADDRESS
   d) UNDEFINED PRINTER TYPE
device RDI / NPC

REMOTE DEVICE INTERFACE used as NETWORK PRINTER CONTROLLER

**Identification of Type of Attached Printer**

<table>
<thead>
<tr>
<th>Bit Values</th>
<th>Printer Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>$80 $40 $20</td>
<td>None</td>
</tr>
<tr>
<td>0 0 0</td>
<td>Centronics (Reverse Channel Mode)</td>
</tr>
<tr>
<td>0 0 1</td>
<td>Centronics (REV: 3 USING ETX ACK)</td>
</tr>
<tr>
<td>0 1 1</td>
<td>Teletype L8</td>
</tr>
<tr>
<td>0 0 0</td>
<td>NEC Spinwriter</td>
</tr>
<tr>
<td>0 0 1</td>
<td>None</td>
</tr>
<tr>
<td>0 0 0</td>
<td>TTY</td>
</tr>
</tbody>
</table>

**Power Supply**

All 3 must be on during operations

**Service LED**

**Network Device Address for the Network Printer**

Address for the network printer must be set to a unique value just as if an additional video terminal is placed on an IOU39Q

**Line Driver**

R-IR+T-Tr

**Only One Jumper on Each Channel to Select Baud Rate**

A = Network Baud Rate

B = Printer Baud Rate

For all jumpers:

1 = ON = Jumper installed

0 = OFF = No jumper present

* Self Test: Installed = Normal operation

Removed = Printer will print short test pattern at the time of R.D.I. power on. Do not leave in this position!
RDI/NPC

LOCAL or REMOTE CABLE STRING

VT3 or Dev RDI or MODEMS

NEXT DEVICE

IOU 39Q

QANTEL CURRENT LOOP

LDS Plug

NEXT DEVICE

IOU 39Q

HUB UNIT

R-RT-IT-

PRINTER

*NEXT DEVICE MAY BE: VT3 or Dev RDI or QSP Terminator

NOTE: LDS Plug is REQUIRED in CURRENT LOOP CONFIGURATIONS

LINE DRIVER SELECT (LDS) PLUG
PART NUMBER M43021-001

QSP TERMINATOR
PART NUMBER M42540-001

1-9F
**SELF TEST:** Please refer to page 1-9E

TO DUMP TRACE BUFFER:

(IPL) 0020A7 Q0020 (CR)
1d309D 0013f200408d 1d119D 0020A8
Q0040 (CR)
02nn0000000000000000000000000000 01E9F3
(28 "0"s)
ELECTRICAL ALIGNMENT

Electrical alignment must be performed when the Qume is installed, or when either board 2 or board 3 are changed. The first portion of the electrical alignment given below is for introducing new boards. If the machine has been running, and the purpose is verification or fine adjustment, the steps that are flagged by an asterisk (*) can be skipped. It is good procedure to check all of the electrical adjustments even if only one board is replaced. All adjustments require an oscilloscope, and the printer must be on line to the computer.

NOTE
Disable switches were replaced by test points on some boards. Where a function is to be disabled, short the test points together.

WARNING
The procedure listed below requires the carrier to move rapidly under full power, and with the cover removed. Keep hands and tools away from the carrier movement area except where specifically directed otherwise.

- Remove the printer top cover if it is not already removed.
- Bypass the interlock switch connection, so the printer will run with the covers off.
- Place the carrier disable switch on BD.1 in the OFF position.
- Place the printwheel disable switch on BD.1 in the OFF position.
- Place the hammer disable switch on BD.3 in the OFF position.
- Apply power to the printer.
- Set the oscilloscope as follows:
  VERT: 2v/cm (calibrated)
  HORIZ: 10ms/cm (may be changed for best display)
  TRIGGER: Automatic, Internal, Positive
- Vertically center the oscilloscope trace so that zero volts, or ground, is on the center gradicule line.
- Connect an oscilloscope probe to TP 3 on Board 2.
- Manually move the carrier, and observe the oscilloscope pattern. A sine wave should be displayed that is +6v to -6v (12 volts peak-to-peak and centered about ground - Gain and Offset)

1-10 Revised 11/77
QUME

- Adjust RV5 to center any offset, and RV3 for overall signal amplitude. See the illustration below for adjustment locations.

- Move the oscilloscope probe to TP 4 on Board 2.

- Adjust RV 4 and RV 2 for offset and gain as in the steps above.

PRINTWHEEL SERVO (PCB #3)

Revised 11/77
**QUME**

- Move the oscilloscope probe to TP 3 on Board 3.
- Adjust RV 6 and RV 4 (Board 3 shown below) to 12v peak-to-peak (gain), and centered about ground (offset) while manually moving the printwheel.
- Move the oscilloscope probe to TP 4 on Board 3.
- Adjust RV 5 and RV 3 for offset and gain as explained above.

**NOTE**
The preceding (manual) steps of this procedure can be skipped if the boards are not newly introduced into the system, and need only to be re-adjusted.

- Verify: The IOU-31 must be at or above REV L.
- Place the printwheel disable switch, and the carrier disable switch in the ON position.

**WARNING**
From this point on, keep hands and tools away from the carriage movement area.

- From the keyboard, type in the following (where x is the Qume address or the CRT address in the slave printer configuration).

**Regular Printer:**

(IPL) 000cf30100000008f30200000100a7 (XMIT)
1x159d00008f2020000b0100a7 (XMIT)
40a0769140b05091 (XMIT)

**Slave Printer (to CRT):**

(IPL) 0000f10100000100a7 (XMIT)
0007f0109b00100a7040e76091c5001 (XMIT)

The carrier should be oscillating approximately one inch. The printwheel is also moving, but is difficult to see when the carrier is moving.

- Place the oscilloscope channel #1 probe on TP 9 on Board 2.
- Place the oscilloscope channel #2 probe on TP 3 on Board 2.

Set the oscilloscope as follows:

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Vert:</td>
<td>Channel #1 5v/cm (calibrated)</td>
</tr>
<tr>
<td></td>
<td>Channel #2 2v/cm (calibrated)</td>
</tr>
<tr>
<td>Horiz:</td>
<td>10 ms/cm</td>
</tr>
<tr>
<td>Trigger:</td>
<td>Normal, Internal, Positive.</td>
</tr>
<tr>
<td>Source:</td>
<td>Channel 1</td>
</tr>
</tbody>
</table>

Display channel #2.

1-12 Revised 11/77
The waveform should be similar to the left hand illustration below. The pattern should be centered on the screen (offset) and 12 volts peak-to-peak at the minimum point (gain).

- Adjust RV 5 and RV 3 on Bd. #2 if necessary for offset and gain.
- Move the CH 2 probe to TP 4 on Board #2. The pattern should change to one similar to the right hand illustration below.

![Waveform Illustrations]

- Adjust RV 4 and RV 2 on Bd. #2 for offset and gain (similar to the steps above).

**NOTE**
Slave printers using the single buffer ROM board can not be adjusted for symmetry. They must be removed to a non-slave system configuration to be adjusted. Slave printers with the ROM R board can be adjusted in the system; The platen will advance with each back and forth movement of the carriage.

- Move the CH 2 probe to TP 8 on Board #2.
- Set the CH 2 vertical to 5v/cm.
- Rotate (uncalibrate) the HORIZONTAL control until the waveform triggers and displays alternate positive and negative signals.
- Display CH 1. It should look similar to the waveform below.

- Depress the 10X magnification button on the oscilloscope and verify that the negative going edge is still displayed.
- Adjust the horizontal position of the trace if necessary for the correct display.
- Adjust RV 1 on Board #2 (carriage symmetry) until the negative going edge is stable (see the illustration below). The adjustment is "TOUCHY" and must be done with care.
- Return the HORIZONTAL control to CALIBRATE, and release the 10X button by depressing it again.

[1-13 Revised 11/77]
Place the oscilloscope channel #1 probe on TP 9 on Board 3.

Place the channel #2 probe on TP 3 on Board 3.

Set the CH #2 gain to 2v/cm.

Set the horizontal time base to 5ms/cm.

Display channel #2, and adjust RV 6 and RV 4 on Board 3 for offset and 12v peak-to-peak. The pattern that is displayed should be similar to that for board 2.

Move the oscilloscope channel #2 probe to TP 4 on Board 3.

Adjust RV 5 and RV 3 on Board 3 for offset and gain as in the previous step above.

Set the CH #2 gain to 5v/cm.

Move the CH #2 probe to TP 8 on Board 1.

Set the trigger level to display alternate positive and negative signals (this is similar to the procedure that was performed for Board 2).

Set the oscilloscope to display channel #1.

Depress the 10X magnification button and adjust the horizontal position to display the negative going edge.

Adjust RV 2 (printwheel symmetry) until the dropoff of the pattern remains stable (the same pattern as shown for Board 2).

Release the 10X magnification button.

PRINT SPEED

Return the channel #1 oscilloscope probe to TP 9 on Board 2.

Enter the following program from device zero.

(IPL) 0084f2010000084f20100bx0006a7 (XMIT)

pppppppp...entire line (132 char.) (XMIT)

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QUME

Carefully measure the time interval between the positive going trigger, and the return to positive (see the illustration). The time should be 25ms if the printer is to operate at an average of 45 characters per second. If the time that is measured is less than 25ms, the printer will run faster than 45cps and may cause other problems. The carriage servo encoder amplifiers (TP 3 and TP 4 on board 2) can be adjusted to greater than 12 volts to increase the time (above) to 25 ms.

HAMMER INTENSITY

This adjustment is factory set, and should only be changed if the overall print quality for a particular set of forms is too heavy. First check all of the mechanical adjustments for the hammer and associated parts. Check that the proper ribbon is used for the particular job. Verify that the hammer plunger moves freely. If the print is still too light or heavy, adjust as follows.

1. Install the forms or paper that will be used.
2. Set the forms thickness lever for the forms that are being used (see the Operator Manual).
3. Type in ten to twenty upper case H's or W's.
4. Type the same number of lower case n's, a's, or o's.
5. Type in a number of commas, periods, and lower case i's.
6. Adjust RV 1 on Board #3 if necessary to lighten or darken the print.
7. Repeat the letters typed above, adjusting again if necessary until the overall print quality is the best possible. The factory setting is 3.2ms for an upper case H, and 1.7ms for a lower case g, measured at TP-11.

POWER SUPPLY

The +5 volt power supply to the printer must be maintained at full value. In a worst case, 4.7 volts can cause a power failure detection, and consequently a restore. In the table configuration the +5 volts should be adjusted to 5.00 volts (under load) at the printer end (+5v.T.P.) of the supply cable with a DVM. However, the 6.5 processors use the same supply for the processor and the printer. The 6.5 processors are model numbers 800, 900, 900, and 950. In these, the voltage should be adjusted to 5.1 volts (under load) at pin 2 of the terminal strip at the back of the power supply.
MECHANICAL ADJUSTMENTS

Mechanical adjustments for the printer require strict attention to detail, and must be performed in the order that is given. Do not make adjustments indiscriminately, or attempt any procedure that is not completely understood.

Mechanical misalignment is usually observed in the overall print quality, or the alignment of one character to the next. Once the printer has been properly adjusted, it should not need re-adjustment unless it has received violent physical abuse. However, re-adjustment is necessary when one or more of the following parts is replaced:

- Printwheel Carriage
- Printwheel Servomotor
- Printhammer Solenoid
- Ribbon Lift Solenoid
- Carriage Servomotor
- Paper Feed Stepper Motor
- Platen
- Paper Feed Rollers
- Platen Drive Gears
- Plastic Card Guide

All adjustments are not required for each of the parts that are listed; the necessary adjustments are listed with the part removal instructions.

REMOVE COVERS

The printer cover is in three sections, an upper snap-on cover, and middle and lower sections that are fastened by screws. Removing the top and middle sections exposes all of the electronics and mechanics. It will be seldom, if ever, that the bottom cover must be removed.

Top Cover

Removing the top cover is an operator function, and must be done to replace the ribbon cartridge or exchange printwheels for various type styles. The top cover snaps out with firm pressure, and is replaced in the same way. When replacing the top cover, be sure that the interlock switch is activated. The interlock switch is at the left-rear corner of the top cover, and disables the carriage and printwheel while the top is open.

Middle Section

- Remove the three 8/32 screws at the rear of the unit.
- Remove the seven 10/32 screws inside the periphery of the cover.
- Remove the platen by grasping at each end, and depressing both latch mechanisms.
QUME

- Lift the center cover section free of the printer chassis. Be careful to disconnect the associated wiring.

- If the printer is to be run with the covers off, a jumper must be placed on J4, where the interlock switch cable was removed.

PRINTHAMMER ALIGNMENT

Printhammer misadjustment usually produces characters that are too light, too dark, or uneven. Occasionally, however, a faulty adjustment will cause noisy operation or printwheel breakage. Printhammer adjustments should be made as a group, and in the given order. Check all adjustments first. Do not change hammer adjustments unless there is a known problem, or a check shows one or more dimensions out of specification.

- Remove all power from the unit.

- Remove the top cover and ribbon cartridge.

- Determine which of the three hammer types you are adjusting by referring to the drawings below. Use the designated character and dimensions for the style hammer that is being adjusted.

- Manually position the character petal at the top position.

- Displace the printhammer out until it touches or deflects the spoke as indicated in the drawing. Observe the vertical position of the hammer on the character petal.

- Adjust the height if necessary by loosening the two screws, "A" in the drawing.

- Re-tighten the two screws (A). When adjusting the hammer height, the angle must remain 7 degrees from horizontal.

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QUME

- Using a 3/16" wrench, loosen the retaining nut at the opposite end of screw C.
- Rotate the bumper beneath screw C to the maximum clearance possible between the bumper and the armature. Do not re-tighten, this adjustment will be completed later.
- Loosen the three screws, (D).
- While pinching the armature lever against the core piece, position the entire assembly so that the tip of the printhammer deflects the printwheel spoke by about the amount specified in the drawing.
- Hold assembly in the proper position, and tighten screws (D).
- Hold the armature to the core piece.
- With a screwdriver, rotate the eccentric bumper (C) to within .003" + or - .002" between the armature and the coil piece.
- While holding the adjustment with a screwdriver, tighten the nut at the opposite end of C.
- Loosen the retaining nut at the opposite end of screw (E)
- Place the proper thickness gauge between the bumper and the armature.
- With the thickness gauge in place, rotate the bumper (E) until the armature is against the polepiece (F).
- Hold the adjustment with a screwdriver, and tighten the nut at the opposite end of screw (E).

PRINTWHEEL INDEX ADJUSTMENT (HUB)

Although the printwheel index adjustment is not technically a printhammer adjustment, it should be checked when the printhammer adjustments are made. Printhammer maladjustment can produce symptoms ranging from poor print quality to physically breaking the printwheel spokes. The printwheel should not be adjusted until after all other adjustments are perfect, and then only if the misalignment is greater than 10% of the character width.

The printwheel index adjustment requires two special tools, and a 1/8" screwdriver. These tools are illustrated below. DO NOT ATTEMPT ADJUSTMENT WITHOUT THESE TOOLS.

This adjustment is made with the printer energized. Keep hands and tools clear of the carriage and printwheel, except as specifically instructed in the text.
• Remove the top cover, if it is not already off.

• Bypass the interlock switch by placing a small bit of folded paper into the opening to hold the switch lever. The interlock switch is located at the left rear of the top cover opening (as seen from the operator position).

• Disconnect the data cable from the Qume device and apply power.

• Extend the printhammer and verify that the lower case "w" is squarely in front of the printhammer. If it is aligned properly, skip the remaining procedure, otherwise continue.

• Release the carriage locking lever by depressing the "O" button on the carriage.

• Remove the printwheel by grasping firmly at the hub, and pull away from the shaft.

• Briefly activate the ribbon feed switch inside the lower right portion of the carrier travel area. The ribbon will raise and advance, then return to the lowered position. When the ribbon returns to the lowered position, the printwheel will index to a "home position", where the lower case 'w' is in the printing position.

• Using the collet adjusting tool and the concentric screwdriver, hold the shaft with the screwdriver and loosen the hub collet. The collet loosens by twisting the knurled knob of the tool counterclockwise.

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QUME

- Mount the alignment disc on the printwheel hub with the "UP" side away from the motor. The "UP" marking references the flat surface, not the rotation.

- Place a folded piece of paper between the printhammer slug and the armature, causing the printhammer to extend.

- While holding the motor shaft with the screwdriver portion of the adjustment tool, rotate the disc (but not the motor shaft) until the extended tip of the printhammer engages (or aligns with) the cut in the flat edge of the disc.

- Carefully tighten the collet hub by turning the knurled knob clockwise. DO NOT tighten with brut force. A very snug fit is sufficient.

- Remove the alignment tools, the disc, and the paper wedge in the printhammer.

- Re-mount the printwheel onto the motorshaft.

- Briefly activate the ribbon feed switch again.

- Verify that the lower case "w" is squarely in front of the printhammer. If it is not, remove the printwheel and repeat the adjustment.

- When the adjustment is complete, return the carriage to the operating position, and press the "C" button to lock it into place.

PLATEN ADJUSTMENTS

Platen misalignment usually causes variation in print quality over the width of the page, ragged lines, lines that taper, or lines that gradually fade from side to side. Difficulty in accurate overprinting may also indicate platen maladjustment. The platen should be aligned routinely when any of the following items are replaced:

Paper Feed Stepper Motor.
Paper Feed Idler Gear
Carriage
Printwheel Servo Motor.

Adjustments within this group are highly interdependent, and should be performed as a group, in the order that is given.

Platen Depth

- Remove the top and middle cover sections. Cover removal instructions are given in the first portion of this section of the manual.

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QUME

- Place the forms thickness lever in the extreme forward position. The forms thickness lever is behind the platen on the top left side. Be sure that the platen is properly seated and latched into position (the two platen releases have returned to the latch position).

- Tilt the carriage forward, and remove the printwheel.

- Return the carriage to the operating position, and lock into place by depressing the "C" button.

- Manually position the carriage to the end of travel on the side that is to be adjusted.

- Place the alignment jig (80474) on the platen, printwheel hub, and front carriage rail as shown in the following illustration.
QUME

- The figure below shows the adjustment on the carrier side plate. The adjustment locations are the same for each end, and are "mirror images" of each other. Each adjustment has a slotted locking screw, and 5/8" hex adjustment eccentric. With a screwdriver, loosen, but do not remove, the two lock screws.

- Using the hex eccentric, adjust the plate until the forward edge of the platen barely touches the flattened vertical edge of the alignment gauge.

- When the adjustment is correct, tighten the lock screws, and repeat the procedure for the opposite end of the platen.

Platen Height

- Place the alignment gauge (80474) as shown on the previous page. This is the same tool, in the same place as for the depth adjustment.

- Loosen the locking screw for the eccentric (See the illustration above).

- Adjust until the top of the platen barely touches the gauge.

- Re-tighten the locking screw, being careful not to change the adjustment.

PLATEN DRIVE GEAR ADJUSTMENT

Adjust the platen drive gear for minimum backlash in the paper feed gear train. A drive mechanism that is too tight will bind, causing poor vertical positioning. A mechanism that is too loose causes poor registration and overprinting, or uneven lines.

The gear train is adjusted at the factory, but must be checked when any of the platen adjustments are made, or when a paper feed motor is replaced. The illustration below shows the location of the adjustment screws. Access to the screw labeled A is through a hole in the large gear.

- Loosen the two retaining screws, A and B. These should be loose enough to allow adjustment, but retain the position without being held.

- Move the large gear into both of the other gears. This should gently bottom without binding.

- Re-tighten the securing screws, and check for backlash or binding. The large gear should be able to slide in and out on the shaft (without the limits of the endplay)
QUME

- but it should not have any rotational motion.

- Rotate the platen knob to verify that there is no binding or backlash at some other point in the rotation. The paper feed motor will exert some resistance, even without power.

- If the gear cannot be adjusted to eliminate backlash, the condition must be corrected by replacing worn parts. However, the quality of the printout should determine if that is necessary.

PLATEN LATCH ADJUSTMENT

- Remove the platen by pressing both releases, and lifting the platen.

- Loosen the locking screw (slot head). See the illustration for the screw location.

- Adjust the eccentric until the dimension is as shown.

- Re-tighten the locking screw, and replace the platen.

PAPER FEED ROLLER ADJUSTMENTS

Feed rollers that are not properly adjusted may cause the paper to "creep" sideways or become misaligned as the paper passes through. All of the paper feed roller adjustments are interdependent, and should be performed as a group.

Remove all power from the unit when making these adjustments.

Feed Roller Depth

- The figure below shows the mechanical details of one of the four feed roller arms. The arm is spring loaded from an adjustable pivot point. By moving the arm toward the front or rear, the roller alignment is adjusted. All four arms must be adjusted, one at a time, to assure proper tracking.

- Using two wrenches, loosen the adjustment on one arm. Loosen only enough to allow movement with finger pressure.

- Adjust the front clearance to the value shown. The front and rear clearance should be approximately equal.

- Re-tighten the adjust-
QUME

ment screws, being careful that the adjustment does not change when the screw is tightened.

• Adjust the other three arms in the same manner. Before operating the unit, manually slide the carriage over the entire travel to verify that there is no interference between the front portion of the arm and the printwheel hub.

• Remove the platen from the unit.

• Carefully lift the paper guide pan (under the platen) and allow it to lay back, out of the way. Do not stretch the springs that are attached to the pan.

• Move the paper release all the way forward; replace the platen. Place a .115" gauge between the platen and one lever arm (see the illustration).

• Adjust the arm for zero clearance by loosening the clamp, and changing the position of the lever.

• Re-tighten the clamp, making sure that the adjustment does not change when doing so.

• Adjust the other three arms in the same manner.

• Re-install the paper guide pan and the platen.

CARRIAGE CABLE TENSION ADJUSTMENT

Cable tension should be checked routinely during preventive maintenance inspections or whenever the carriage is removed and replaced. Cable tension is maintained by spring tension on one idler pulley at the right side of the printer. Adjust the cable tension as follows:

• Using a 3/16" wrench, turn the adjustment nut (item A in the illustration) until the shaft, B, is centered in the hole of the pulley mounting bracket, or 1" from the frame if the bracket does not have a hole. Either, or both ends may be
QUME

adjusted whichever is convenient, but DO NOT TWIST THE CABLE.

LEFT MARGIN PHOTO SENSOR

The optical sensor sets the beginning point for carriage travel. It is used only during the RESTORE sequence, which is issued in initialization and reset sequences.

- Initialize the printer by removing power, then returning the power. The carriage should move to the left side of the travel.
- Using a graduated rule or other suitable measuring instrument, measure the distance from the printhammer axis to the frame (see the illustration).
- If the distance must be adjusted, loosen the two mounting screws on the photo sensor, and move the assembly.
- When the adjustment has been moved, re-initialize the printer by power OFF/ON. The carriage will move to the adjusted location.

DECELERATION STOPS

- The deceleration stops are located at each end of the carrier travel. Normally these stops are not touched. However, when the stops are used (by mis-programming or malfunction) the stopping force must be as close to the cable as is practical. Adjust by loosening the screws (A) and moving the stop (B).

CARD GUIDE ADJUSTMENT

The card guide must operate with both proper clearance and alignment. The clearance is adjusted (with power off) by loosening the two mounting screws (B) that are shown in the illustration below. The card guide should lightly touch the platen along the entire working surface of the guide. Be sure
that when the adjustment is made, there is clearance between the guide and the front platen rollers (see the lower illustration).

To set the card guide height, type several lines of capital I’s at 6 lines per inch spacing. Adjust the guide by loosening the two screws (A) shown in the illustration. At 6 lines/inch, two lines should "rest" on the guide lines, while the center line is between the guide lines. The vertical mark, indicating the print position, should point directly at the center of the letter (see the illustration below).

RIBBON LIFT HEIGHT

Print a few characters, alternating between the apostrophe (') and the underscore (_). Examine the ribbon to be sure that the characters are striking the ribbon properly as shown in the illustration. If adjustment is required, loosen the mounting screws (A) for the ribbon lift solenoid and re-position the solenoid until the ribbon is struck correctly.

MOTHERBOARD ALIGNMENT

If difficulty is encountered when inserting or removing the power and data connectors at the rear of the chassis, re-aligning the motherboard may help.

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QUME

- Remove all power from the unit.
- Remove the top and middle cover sections.
- Remove all four printed circuit boards from the inside of the printer.
- Loosen, but do not remove, the eight pan-head screws that hold the motherboard.
- Insert the power and data connectors. Be sure that both connectors are well seated onto the motherboard connector flange.
- Tighten the jackscrews on the connectors.
- Tighten the eight motherboard mounting screws.
- Replace the printed circuit cards and the covers. Be sure that all printed circuit cards are securely seated, and the board switches are in the proper positions.

<table>
<thead>
<tr>
<th>ASCII EXCEPTIONS</th>
<th>SELECTRIC</th>
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<tbody>
<tr>
<td>0000 0</td>
<td>5B = +</td>
</tr>
<tr>
<td>0001 1</td>
<td>5C = c</td>
</tr>
<tr>
<td>0010 2</td>
<td>5E = \</td>
</tr>
<tr>
<td>0011 3</td>
<td>08 = BS</td>
</tr>
<tr>
<td>0100 4</td>
<td>09 = TAB</td>
</tr>
<tr>
<td>0101 5</td>
<td>0A = INDEX</td>
</tr>
<tr>
<td>0110 6</td>
<td>0D = CR</td>
</tr>
<tr>
<td>0111 7</td>
<td>1000 8</td>
</tr>
<tr>
<td>1001 9</td>
<td>TAB</td>
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<td>IDEX</td>
</tr>
<tr>
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<td>IDEX</td>
</tr>
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<td>R1D</td>
</tr>
<tr>
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<td>CR</td>
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<td>1110 E</td>
<td>R1D</td>
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<td>1111 F</td>
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IOU-37
TELETYPewriter
Qantel Model 5061, 5062

<table>
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<th>ADDRESS SWITCHES</th>
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<td>P1</td>
<td>P3</td>
</tr>
<tr>
<td>2</td>
<td></td>
</tr>
<tr>
<td>30</td>
<td>35</td>
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<tr>
<td>4</td>
<td></td>
</tr>
<tr>
<td>8</td>
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</table>

Uses Even Parity

- 300 LPM = 64 Char. Belt
- 220 LPM = 96 Char. Belt

MAX. PWR. REQUIREMENTS

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<tr>
<th>Volt</th>
<th>Amp</th>
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<tbody>
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<td>1.70</td>
</tr>
<tr>
<td>-12</td>
<td>.04</td>
</tr>
</tbody>
</table>

PRINTER POWER

- 115 VAC 60 HZ.
- 10A MAX. FOR .4 SEC.
- 1.3A NORMAL

I/O CABLE LENGTH

- Can Be Up To 2000 FT.

PRINTER HEAT GEN.

- 513 BTU/HR.

WRITE CONTROL INSTRUCTIONS

1XY9D X = DEVICE ADDRESS  YY = CONTROL BYTE

<table>
<thead>
<tr>
<th>CONTROL BYTE</th>
<th>FUNCTION</th>
</tr>
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<tbody>
<tr>
<td>06</td>
<td>FORM FEED</td>
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<tr>
<td>07</td>
<td>LINE FEED</td>
</tr>
<tr>
<td>20</td>
<td>RESET ALLOW TERMINATION INTERRUPT</td>
</tr>
<tr>
<td>60</td>
<td>SET ALLOW TERMINATION INTERRUPT</td>
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</tbody>
</table>

STATUS IN INSTRUCTIONS

4XY9D X = DEVICE ADDRESS  YY = STATUS BYTE

<table>
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<tr>
<th>STATUS BYTE</th>
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<td>01</td>
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<td>02</td>
<td>WRITE BUSY</td>
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<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>INTERRUPT FLAG</td>
</tr>
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<td>20</td>
<td>ALWAYS ZERO</td>
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<tr>
<td>40</td>
<td>ALWAYS ZERO</td>
</tr>
<tr>
<td>80</td>
<td>INOPERABLE</td>
</tr>
</tbody>
</table>

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TELETYPE
POWER SUPPLY ADJUSTMENTS (Fig. 1)

The regulator card is located in the power module assembly. Access to the -24 volt DC voltage adjustment and fuse is at the rear of the printer. The regulator card is adjustable from approximately -21 volts to -26 volts. This card has an overvoltage protection (crowbar) at approximately a -27 volt level. When an overvoltage occurs (malfunction), the output is shorted to ground and should cause fuse F1 to blow. Fuse F1 should not exceed the 2.5 amp fast blow rating.

- The LED provides a visible indication that the DC voltage is present. The LED is located next to the adjustment pot (R9).
- The DC voltage on fuse F1 is approximately -35 volts.
- Place a DVM between GND and the pin on J114 as shown in Fig. 1.
- Adjust potentiometer R9 to equal -24 ± .25 volts on the DVM.
- The 410151 Regulator Board has two option screws (A & B). Screw "A" should be on the component side, which connects the -24v common to chassis. Screw "B" should also be on the component side, which connects the -24v common to the +11.3v common.

NOTE: You must remove the power module assembly and the regulator board from the power module to reach screws "A" and "B".

BACKUP BAR ADJUSTMENT (Fig. 2, 3 & 4)

The backup bar determines the distance between the hammers and the type carrier. This distance is initially set up with 4 thicknesses of paper (or 3-part paper with carbons) which should satisfy most customers for print quality on one to six part paper. The Backup Bar adjustment should only be necessary under special circumstances with customer forms.
TELETYPE

- Remove the type carrier.
- Use a new or almost new ribbon for this adjustment.
- Insert the customer's forms in question for print quality.
- Insert 402868 gauge (Fig. 3) into carrier track and hold firmly against the backup bar at one side of the print station.
- With printer power turned on, place the printer test switch in "on" position and pass a steel object (screwdriver) over the flag sensor to initiate hammer firing. While hammers are firing, areas 2, 4, and 6 should not be visible, areas 3 and 5 should be just visible, and areas 1 and 7 should be the darkest and approximately equal (Fig. 3). This requirement must be met with the 402868 gauge at both sides of the print station.

![Diagram](Left Side View)

**FIG. 2**

Just visible printing in Areas 3 and 5

![Diagram](FIG. 3)

No visible printing in Areas 2, 4 and 6

**FIG. 3**

- If adjustment is necessary, loosen the five track assembly mounting screws friction tight.
- Turn the adjustment screw clockwise to move backup bar closer to hammers and counterclockwise to move backup bar away from hammers. Fig. 4 shows mounting and adjustment screw locations.
TELETYPE

• When the proper printout is achieved (Fig. 3) on the left and right side, place the "test" switch in the off position.

• Tighten the five track assembly mounting screws.

• Remove the 402868 gauge.

NOTE: For maximum ribbon life, the BACKUP BAR adjustment should be made to give the lightest acceptable printed copy.

NOTE: Each time the BACKUP BAR adjustment is made, the left and right CARRIER SPROCKETS, left and right RIBBON GUIDES, and the PAPER POSITIONER ROLLER adjustments must be made.

MOUNTING SCREWS

CAUTION: DO NOT LOOSEN THESE SCREWS.

FIG. 4

To perform the CARRIER SPROCKET, RIBBON GUIDE, and PAPER POSITIONER ROLLER adjustments, turn the printer A-C power off, and remove the paper and ribbon. Insert gauges 402716 and 402717 in the left and right carrier track positions respectively as shown in Figures 5 and 6.

LEFT CARRIER SPROCKET ADJUSTMENT (Fig. 5)

• Position the 402716 gauge against the backup bar.

• Position the finger lever of the sprocket hub parallel to the track assembly (Fig. 5).

• Turn the adjusting nut clockwise or counterclockwise until the rear surface of sprocket flange clears the 402716 gauge by .006" (Max.) to some (Min.) at its closest point.

RIGHT CARRIER SPROCKET ADJUSTMENT (Fig. 5)

• Position the 402717 gauge against the backup bar.

• Loosen the two hub clamp screws.

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TELETYPE

- Turn the split adjusting nut clockwise or counterclockwise until the sprocket flange clears the 402717 gauge by .005" (Max.) to some (Min.) at its closest point.

- Tighten the two hub clamp screws.

LEFT AND RIGHT RIBBON GUIDE ADJUSTMENTS (Fig. 5)

- Loosen the two left and right ribbon guide mounting screws.

- Position the 402716 and 402717 gauges against the backup bar.

- As a preliminary adjustment, position the left and right ribbon guides to clear the gauges.

- Tighten the two left and right ribbon guide mounting screws.

NOTE: The remaining ribbon guide adjustment is performed with the type carrier installed. The adjustment is a final operational check to assure proper clearance between the type pallets and ribbon guides.

- Seat all pallets of the type carrier against the rear surface of the large diameter flange of the left carrier sprocket.
TELETYPEx

• Position a .010" flat gauge against each ribbon guide and rotate the impeller shaft through one complete rotation of the carrier to check for a minimum .010" clearance between the closest pallet and ribbon guide (Refer to Fig. 6).

PAPER POSITIONER ROLLER ADJUSTMENT (preliminary) (Fig. 7)

• Position the 402716 and 402717 gauges against the backup bar.

• Loosen the two eccentric clamp screws friction tight.

• Rotate the eccentrics on both sides until the paper positioner roller just touches the gauge posts.

• Tighten the eccentric clamp screws.

• Remove the 402716 and 402717 gauges and install the type carrier, ribbon, and paper.

Further adjustment will be necessary if the two requirements below are not met. These requirements do not apply when printing within two lines of a fold on fan-fold paper.

a) The printed copy should not be illegible due to ribbon smudging after a one minute printer idle period.

b) There shall be no smudge when printing a text character (not font or identification symbol).

• If adjustment is needed, loosen the left and right eccentric clamp screws friction tight.

• Rotate eccentrics to meet the requirements above.

FIG. 7
NOTE: It is preferable to use nonmagnetic gauges to check the gaps on both magnetic sensors.

Air Gap (Fig. 8)

• Loosen sensor clamp screw.

• Insert a .015" flat gauge between the magnetic sensor pole piece and a tooth on the timing wheel and adjust the gap for a snug fit. The tolerance is minimum .008" -- maximum .020". Tighten the sensor clamp screw and remove gauge.

Under Power (Fig. 9)

• With the unit in the "test" position and printing the test character, turn the impeller sensor adjusting screw COUNTERCLOCKWISE until a column/s does not print. The impeller sensor adjusting screw can be reached through a hole in the top cover just above the screw (FIG.9).

• Slowly turn the adjusting screw CLOCKWISE until all columns print correctly for at least ten lines.

• Turn the adjusting screw CLOCKWISE an additional 2.5 turns.
TELETYPING

FLAG SENSOR ADJUSTMENT (Fig. 10 & 11)

Air Gap (Fig. 10)
- Loosen the Flag Sensor clamp screw.

- Insert a .020" flat gauge between the magnetic sensor pole piece and a flag on the font belt and adjust for a snug fit. This should meet the requirement for a minimum .010" clearance between the closest Flag and magnetic sensor pole piece and a maximum .030" clearance between any Flag and sensor pole piece.

- Tighten the Flag Sensor clamp screw.

Under Power (Fig. 11)
- Loosen the Flag Sensor bracket clamp screws friction tight.

- Turn printer "test" switch on.

- Move the sensor bracket CLOCKWISE until a different or no character prints in one or more columns. Note the position of the indicator on the range scale.

- Move the sensor bracket COUNTERCLOCKWISE until again a different or no character prints in one or more columns. Note the position of the indicator on the range scale.

- Position the Flag Sensor indicator midway between the two previously noted failure points.

- Turn the "test" switch off.

- Tighten the clamp screws.
TELETYPE

IMPELLER SHAFT TO CARRIER PHASING  (Fig.12)

• Enter a program to print a full line of capital H's.

• Print several lines, place printer off-line, and wait for motor to stop.

• Inspect the characters to see if the right or left portion of the characters are being clipped.

• To make adjustment, push the impeller shaft gear (which has a strong spring tension) away from the knurled adjusting collar and turn the collar:
  a) CLOCKWISE if right portion of character is clipped.
  b) COUNTERCLOCKWISE if left portion of character is clipped.

• Place the printer back on-line and repeat above steps until proper printing of complete character is achieved.

NOTE: When multicopies are used, phasing should be adjusted to minimize clipping on the last copy with no clipping on the original copy.

TRACtOR SIDE PLATE UPPER MOUNTING SCREW

TRACTOR PHASING  (Fig.13)

• This adjustment is done by the factory and should not be needed unless tractors are removed or replaced.

• The phasing marks (white dot) on the left and right tractors shall be on the same spline shaft groove.

• To adjust, remove the left tractor from the splined shaft, turn the tractor, and reassemble on the correct spline shaft groove.
TELETYPE

RIBBON MECHANISM DRAG  (Fig.14)

• Place a spring scale over the ribbon disc drive pin.

• A force of 8 to 10 ounces should start the free wheeling ribbon drive disc moving when pulling tangentially on its drive pin.

• To adjust, rotate the adjusting nut CLOCKWISE to increase drag and COUNTERCLOCKWISE to decrease drag to meet the requirement above.

• Reverse the ribbon mechanism by pulling up on the reversing lever adjacent to the free wheeling drive disc.

• Manually rotate the impeller shaft clockwise as viewed from the right side of the printer until ribbon reversal occurs.

• The other ribbon drive disc is now the free wheeling disc and may be checked and adjusted as above.

NOTE: Before checking the drag, rotate the free wheeling drive disc at least one-half revolution in the direction to be pulled in order to take up all play.

![Diagram of RIBBON MECHANISM](FIG.14)

RIBBON MECHANISM DRIVE BELT TENSION  (Fig.15)

• Loosen the two ribbon mechanism mounting screws.

• Place a spring scale gauge around the bottom of the belt.

• Position the ribbon mechanism left or right to obtain a minimum 11/32" -- maximum 1/2" between the outside surfaces of the drive belt when a force of 8 ounces is applied adjacent to the ribbon mechanism frame.

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IMPELLER SHAFT DRIVE BELT TENSION (Fig. 16)

- Loosen the four motor assembly mounting screws friction tight.
- Place a spring scale gauge perpendicular to the drive belt approximately midway along its free length.
- Using one of the pry points, adjust the belt to deflect 1/8" to 3/16" with a force of 5 ounces applied from the spring scale gauge.
- Tighten the four motor assembly mounting screws.

LINE FEED CLUTCH DRIVE BELT TENSION (Fig. 17)

- Loosen the three line feed assembly mounting screws.
- Place a spring scale gauge perpendicular to the drive belt approximately midway along its free length.
- Position the line feed assembly so the belt deflects 1/16" to 5/32" with a force of 5 ounces applied from the spring scale gauge.
- Tighten the three line feed assembly mounting screws with the tension applied.

FEED BAR DRIVE BELT TENSION (Fig. 18)

- Loosen nut mounting the feed bar shaft to the mounting plate.
- Place a spring scale gauge perpendicular to the feed bar drive belt approximately midway along its free length.
- Move feed bar shaft left or right so belt deflects 1/16" to 1/8" with a force of 5 ounces applied by spring scale gauge.
- Tighten the feed bar shaft nut.
PAPER-OUT SWITCH ADJUSTMENT (Fig. 19)

- Loosen the two switch clamp screws friction tight.

- Place a .075" gauge between end of paper-out switch arm and rear paper guide. The switch should trip before arm contacts .075" gauge.

- Place a .095" gauge between end of paper-out switch arm and rear paper guide. The switch should not trip when lightly held against the .095" gauge.

- Tighten the clamp screws while meeting the requirements.

TYPE CARRIER PALLET ALIGNMENT (Fig. 20)

- The pallet location in the carrier should be a minimum .070" from the stem end to the rear surface of the carrier.

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TELETYPE

CLUTCH STOP LEVER  (Fig.21)

• Loosen the two Magnet Assembly Mounting nuts and the Armature Stop Mounting screw (back of Magnet Mounting nut) friction tight.

• Position Armature Stop so that Clutch Stop lever is a minimum flush -- maximum .005" overflush with the top surface of the Shoe Release Arm.

• Tighten the Armature Stop screw and Magnet Mounting nuts.

LINE FEED ARMATURE GAP  (Fig.22)

• Loosen the two Magnet Assembly Mounting nuts and the Magnet Assembly Mounting Post (in front of right Magnet Assembly Mounting nut) friction tight.

• Rotate the Magnet Assembly about mounting post so that a .025" gauge will enter between the armature and magnet pole piece but will not pass beyond the midpoint of the pole piece.

• Tighten the Magnet Mounting nuts and the Magnet Mounting Post.

IOU 37 adjustments are in Tech Memo #25
TELETYPEx

FORM-OUT CONTACT GAP

1. Check that when the form-out contacts are completely open, the gap between the contacts is between 0.015 inches and 0.025 inches.

2. Adjust the contact gap if necessary by turning the eccentric bushing. This adjustment will be finalized in the Form-out Contact to Belt Spacing Section on the next page.

FORM-OUT CONTACT ASSEMBLY LATERAL ADJUSTMENT

1. Check this adjustment by setting the form-out mechanism to Position 4.

2. Advance the belt manually until one of the short lobes on the belt rests against the end of the form-out contact assembly. Push the line-feed pawls away from the line-feed gear to allow the belt pulleys to turn freely.

3. Center the tip of the contact assembly on the lobe, if necessary. Loosen the contact assembly mounting screws to allow the contact assembly to be moved laterally. The entire form selector assembly will have to be removed on early models.

4. Retighten the contact assembly mounting screws.
TELETYPE

FORM-OUT GEAR BACKLASH

1. Check the backlash (free play) of the form-out gear. Try to turn the form-out gear while holding the line feed gear stationary. A slight amount of backlash should be detectible as the form-out gear is twisted back and fourth.

2. If an adjustment is necessary, loosen the three clamp nuts and move the form-out mechanism forward or backward to allow a slight (and only a slight) amount of backlash between the gear teeth.

3. Retighten the clamp nuts.

FORM-OUT CONTACT TO BELT SPACING

1. Lift the line feed pawls off the line feed gear and advance the form-out belt until a cam lobe just touches the tip of the form-out contact assembly.

2. Allow the line feed pawls to engage with the line feed gear as the form-out belt is moved slightly back from the form-out contact assembly. The line feed gear should click into position.

3. The clearance between the front of the cam lobe and the tip of the form-out contact assembly should be some to 0.010 inches.

4. The tip of the form-out contact assembly should clear the surface of the belt by some to 0.015 inches.

5. Slightly loosen the screw holding the eccentric bushing and the nut holding the eccentric post. The eccentric bushing primarily adjusts the form-out contact in the forward and reverse directions. The eccentric post primarily adjusts the form-out contact in the up and down positions. However, one adjustment affects the other and they must alternately be turned until they are both correctly adjusted.
6. If there is not enough adjustment range to meet the above requirements, the form-out gear will have to be disengaged and moved one tooth in either direction. In this case, go back to the Form-Out Gear Backlash Adjustment.
TELETYPEx

INTERPOSER BACKSTOP ADJUSTMENT

THIS ADJUSTMENT SHOULD ONLY BE DONE IF THE CHARACTERS IN ONE OR MORE COLUMNS ARE NOT PRINTING AND IF ALL OTHER ADJUSTMENTS HAVE BEEN MADE.

In some cases, a weak interposer spring may cause a misalignment of the bottom of an interposer with the tip of its armature. When this happens, the armature is unable to raise the interposer sufficiently to engage the teeth of the impeller. This prevents the interposer from being driven into the hammer, and thus prevents a character from being printed in the hammer's column.

This problem will usually appear as an intermittent absence of a character from a particular column, but in an extreme case, a column may be consistently blank. If a column is consistently blank, it could also be caused by a bad winding in an armature magnet, or by a broken interposer spring. In such a case:

1. Turn off the printer and remove the logic PWA from the underside of the printer.
2. Examine the armatures and the interposers for missing or broken springs.
3. Check the resistance of any suspected coil units. It should measure 50 ohms + 2 ohms from the common terminal to the terminal of the coil unit being measured.

When the printer is tilted up exposing the terminals, the upper row of terminals go to the armatures that operate the odd numbered hammers, and the lower row of terminals go to the armatures that operate the even numbered hammers.

![Diagram of the interposer backstop adjustment](image-url)
TELETYPE

A cross section of the print head assembly is shown below to illustrate what needs to be adjusted and how all the parts function together:

Follow this procedure for the Interposer Back Stop Adjustment:

1. Disconnect the Molex Connectors at the mechanical VFU assembly.
2. Remove the four screws at the top of the paper handling assembly. Then lift the paper handling assembly by the handle.

3. The impeller will be exposed and the interposer back stop will be visible behind it. Loosen the screw at each end of the interposer back stop only enough to allow the back stop to be moved with a screwdriver blade.

4. Move the interposer back stop about 1/32-inch towards the the impeller and tighten the back stop screws.

The clearance between the edge of the back stop and the ridge of the printhead casting will vary from one printhead to another. Therefore, no specific gap setting can be given. The edge of the backstop may or may not be parallel to the ridge of the printhead casting.

5. Carefully replace the paper handling assembly. Make sure the line feed pawls are properly aligned with the line feed gear, and make sure the metal paper guide is behind the plastic guides. Screw down the paper handling assembly with at least two screws.

6. Reconnect the Molex Connectors at the mechanical VFU assembly.

7. Run the self test to see if the problem is corrected or improved.

8. If further correction is necessary, all the above steps may be repeated.

IT IS EXTREMELY IMPORTANT NOT TO OVERCORRECT WHEN DOING THE INTERPOSER BACKSTOP ADJUSTMENT. If there is any doubt about this procedure, turn off the printer power and remove the Logic PWA from the underside of the printer. This will expose the bottom of the armature assembly. The interposers can be seen at either end of the assembly with the aid of a mirror.

If an interposer at either end of the assembly is not aligned with its respective armature tip, the interposer back stop is definitely adjusted incorrectly. A penlight may be used in addition to the mirror to observe if any of the other interposers are badly out of line.

Finish the adjustment by readjusting the impeller sensor positioning. Refer to Figure 9 on page 1-35:

1. Turn on the power and turn on the TEST Switch.

2. With the printer printing the test character, turn the impeller sensor adjusting screw COUNTERCLOCKWISE until the printer starts missing in one column or more. The printer will begin to sound differently when this happens.

3. Slowly turn the adjusting screw CLOCKWISE until all columns print correctly for at least ten lines.
TELETYPE

4. Turn the adjusting screw CLOCKWISE another 2-1/2 turns. This will usually place the impeller sensor in the optimum position, but this adjustment sometimes varies with some printers.
TELETYPEx
OPTION SWITCHES FOR 410072 LOGIC BOARD (Fig.21)

Printer Circuit Card Viewed From Beneath Printer - Access to Switches is Through a Cutout in Bottom Pan of Printer.

* Indicates switch in ON position.
0 Indicates switch in OFF position.
- Position of switch does not affect option.

1. PRINTER RIGHT MARGIN AND FORM WIDTH

<table>
<thead>
<tr>
<th>Last Char. Printed</th>
<th>D8</th>
<th>D9</th>
<th>D10</th>
</tr>
</thead>
<tbody>
<tr>
<td>in Column Number</td>
<td>123456789</td>
<td>12345678</td>
<td>12345678</td>
</tr>
<tr>
<td>132</td>
<td>--------**-</td>
<td>--------**-</td>
<td>*0-00---</td>
</tr>
<tr>
<td>121 109 97 85 73</td>
<td>--------**-</td>
<td>--------**-</td>
<td>**-00---</td>
</tr>
<tr>
<td>122 110 98 86 74</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>123 111 99 87 75</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>124 112 100 88 76</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>125 113 101 89 77</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>126 114 102 90 78</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>127 115 103 91 79</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>128 116 104 92 80</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>129 117 105 93 81</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>130 118 106 94 82</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>131 119 107 95 83</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
<tr>
<td>120 108 96 84</td>
<td>--------**-</td>
<td>--------**-</td>
<td>0*-00---</td>
</tr>
</tbody>
</table>

To obtain counts:
121 through 132 program as shown.
109 through 120 program as shown, then operate D9 position 7 OFF.
97 through 108 program as shown, then operate D9 position 8 OFF.
85 through 96 program as shown, then operate D8 position 7 OFF.
73 through 94 program as shown, then operate D8 position 8 OFF.

2. CHARACTER SET

a) Printers With 96-Character Set
   D8 123456789
b) Printers With 56-Character Set
   0*------
c) Printers With Extended ASCII Character Set
   00------
d) Printers With Longest Character Set Having Less Than 64 Characters
   00------

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11/77
TELETEYPE

OPTION SETTING FOR THE 418729 LOGIC CARD (2 SWITCH PADS)

- Set the 64 character printer switch as shown in Figure 1.
- Set the 96 character printer switch as shown in Figure 2.

NOTE: This logic board can only be used with the IOU-37.

![Figure 1](image1.png)

![Figure 2](image2.png)
TELETYPE

OPTION SETTINGS FOR THE 410072 LOGIC BOARD (4 SWITCH PADS)

IOU-37 OPTION REQUIREMENTS

- Set the 64 character switches (shown in black) as in Figure 1.
- The 96 character switch variations (compared to the 64) are indicated with an X.
- The following switches must be changed: SW D9-1, SW D8-1, and SW D8-2.

![Figure 1 Diagram]

IOU-39 AND DTC COM OPTION REQUIREMENTS

- Set the 64 character switches (shown in black) as in Figure 2.
- The 96 character switch variations on SW D9-1, SW D8-1, and SW D8-2, are indicated with an X.

![Figure 2 Diagram]
3. PRINTER LEFT MARGIN AND FORM WIDTH

a) First Printed Column -- Column 1
b) First Printed Column -- Column 2
c) First Printed Column -- Column 3
d) First Printed Column -- Column 4
e) First Printed Column -- Column 5
f) First Printed Column -- Column 6
g) First Printed Column -- Column 7
h) First Printed Column -- Column 8
i) First Printed Column -- Column 9
j) First Printed Column -- Column 10
k) First Printed Column -- Column 11
l) First Printed Column -- Column 12
m) First Printed Column -- Column 13

4. SSI/OEM INTERFACE

a) SSI
b) OEM

5. FOLDOVER ON PRINTERS WITH 96-CHARACTER SET

a) Lower Case and Upper Case Print
b) Lower Case Prints as Upper Case

6. FOLDOVER ON PRINTERS WITH 64-CHARACTER SET

a) Lower Case Prints as Error Symbol
b) Lower Case Prints as Upper Case

7. PRINTER PAPER FEED OUT

a) No Paper Feed Out
b) Paper Feed Out on DSR or RM Loss -- 16 Lines or One Form.
c) Paper Feed Out on DSR or LRM Loss or ETX -- 16 Lines or One Form.

8. INCOMPLETE FORM SUPPRESSES PAPER ALARM

a) No (Paper Out Not Gated With Form Out)
b) Yes (Paper Out Gated With Form Out)

9. AUX. ALARM

a) Enable
b) Disable

10. IDLE LINE MOTOR CONTROL

a) Disabled - Motor Held ON Indefinitely During Idle Line.
b) Enabled - Motor Turned Off After 40-second Idle Line.

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TELETYPE

11. SI/SO DETECTION
   a) SI/SO Detection Not Used
      --0-----
   b) SI/SO Detection Enables Printing Additional Char.--*-----

12. PRINTER ERRORED CHARACTER SYMBOL
   a) Printed on Even Parity Error
      -----*0-
   b) Printed on Odd Parity Error
      -----0*-
   c) Not Printed on Parity Error
      -----**-
   d) Prints Extended ASCII Characters
      (No Parity Check)
      ------00-

13. PRINTING OF ESCAPE SEQUENCES SUPPRESSED
   a) Character After ESC Printed as Received
      ------0-
   b) Printing of Character After ESC Suppressed
      ------*

14. SPEED SELECTION (applies only if option 4b is selected)
   a) 75 baud
      00000000
   b) 150 baud
      00000000
   c) 300 baud
      00000000
   d) 600 baud
      00000000
   e) 1200 baud
      00000000
   f) 2400 baud
      00000000
   g) 4800 baud
      00000000
   h) 9600 baud
      00000

OPTION SWITCHES FOR 410729 LOGIC BOARD (Fig. 22)

1. INCOMPLETE FORM SUPPRESSES PAPER ALARM
   a) No (Paper Out Not Gated With Form Out)
      ------*
   b) Yes (Paper Out Gated With Form Out)
      ------*

2. PRINTER ERRORED CHARACTER SYMBOL
   a) Printed on Even Parity Error
      --#0---
   b) Printed on Odd Parity Error
      --0*----
   c) Not Printed on Parity Error
      --**---
   d) Prints Extended ASCII Characters- No Parity Chk.
      ------00---
3. **TELETEYPE**

3. **PRINTER PAPER FEED OUT**

   a) No Paper Feed Out  
   b) Paper Feed Out on DSR Loss - 16 lines  
   c) Paper Feed Out on DSR Loss or ETX

4. **CHARACTER SET**

   a) Printers With 96 Character Set  
   b) Printers With 64 Character Set  
   c) Printers With Extended ASCII Character Set  
   d) Printers With Longest Character Set  
      Having Less than 64 Characters

5. **FOLDOVER ON UP-LOW PRINTER**

   a) Lower Case and Upper Case Print  
   b) Lower Case Prints as Upper Case

6. **FOLDOVER ON MONOCASE PRINTER**

   a) Lower Case Prints as Error Symbol  
   b) Lower Case Prints as Upper Case

7. **PRINTER MARGIN AND FORM WIDTH**

   a) Last Character on Column 132  
   b) Last Character on Column 131  
   c) Last Character on Column 130  
   d) Last Character on Column 129  
   e) Last Character on Column 128  
   f) Last Character on Column 127  
   g) Last Character on Column 126  
   h) Last Character on Column 125  
   i) Last Character on Column 124  
   j) Last Character on Column 123  
   k) Last Character on Column 122  
   l) Last Character on Column 121
LUBRICATION

General

The printer can be lubricated by positioning it in the ribbon changing (maintenance) position.

Lubricate printer just prior to placing in service or before putting it in storage. The printer should be re-lubricated after it has been in service a few weeks. Thereafter, lubricate mechanisms every 2000 hours of running time.

Apply lubricant to points as indicated.

On small parts, a minimum amount of lubricant should be applied so that the lubricant remains on the parts and does not run off.

Excessive lubricant should be removed with a dry, lint-free cloth.

The following areas must be kept dry, free of all lubricant:

- All electrical components, including terminals.
- All parts normally touched by the operator, including exposed surfaces in ribbon, paper handling areas, and all large flat areas.

The following symbols indicate the quantity of lubricant to be used in a specified area: Symbols 01, 02, 03, etc., refer to 1, 2, 3, etc., drops of oil. The following list of symbols applies to the lubrication instructions and the type of lubricant to be used:

- **O** 88970 oil.
- **G** Apply thin film of 145867 grease.
- **S** Saturate felt oilers, washers, and wicks with oil.
- **K** Keep dry, no lubricant permitted.

NOTE: All ball bearings are packed with grease and normally need no re-lubrication. However, if lubrication is required (Beacon 325) grease should be used. Grease is to be applied 1/64 inch maximum thickness.
TELETYPE

LUBRICATION

Front and Top

01 Springs (Each End, 2 Springs)

01 Pivot (Each Side, 5 Places)

NOTE: The type carrier must be removed to gain access to the pad.

01 Motor Shaft Bearings (Both Ends)

02 Ribbon Mechanism

01 Ribbon Guide Rollers (4 Places)

01 Ribbon Guide Rollers (4 Places)

S Felt Wick Under Type Carrier

G Front Flange of Left Idler Sprocket (Rear Surface)

IDLER

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TELETYPE

LUBRICATION (Cont)

Left Side

-01 Left Ribbon Guide
Bracket Pivot

02 Sleeve End
of Shaft

G Cam Surfaces (6 Places)

01 Hooks

01 Paper Feed Clutch
S Felt Wick

(Top View)

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TELETYPEN

Left Side

LUBRICATION (Cont)

01 Detent
01 Eccentric Post
01 Pulley Slot
01 Gear Shaft

01 Eccentric Bushing

01 Form-Out Gear
G

01 Form-Out Gear Shaft
01 Guide

01 Hooks (Each End of Spring)

04 Eccentrics (Approximately)

01 Feed Bar Shaft (Each End)
02 Roller Feed Shaft
02 Roller

03 Sleeve Oil Hole
01 Pivot (Each End)

G Gear Teeth

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TELETYPE

LUBRICATION (Cont)

Left Side

G Stop Lever
Engaging Surface

O1 Hooks (Each End of Spring)

Right Side

O1 Phasing Mechanism Hooks (Each End)

G Impeller Driven Gear Teeth

G Shaft Bearings (Pack Bearings, Each End)

S Carrier Lubricating Pad O1 Right Ribbon Guide Bracket
### BASIC CODE SET FOR 64 CHARACTER PRINTERS

**NOTES:**

**HEX DECIMAL = ASCII COLUMN, ASCII ROW.**

IOU's FOR 64 CHARACTER PRINTERS IGNORE BIT 6 ($2^5$), AND REPLACE IT WITH THE INVERTED BIT 7 ($2^6$). THIS PRINTS COLUMNS 0 and 1 AS CHARACTERS IN COLUMNS 2 AND 3, AND COLUMNS 6 AND 7 AS CHARACTERS IN COLUMNS 5 AND 6.

ALL BITS ARE ENABLED FOR CONTROL CODES (SEE TABLE).

EXACT CHARACTERS IN POSITIONS 6, $\varnothing$ AND 7B THRU 7F MAY VARY SLIGHTLY BETWEEN PRINTERS. THESE POSITIONS ARE NOT AVAILABLE ON THE SELECTRIC.

---

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<th>3</th>
<th>4</th>
<th>5</th>
<th>6</th>
<th>7</th>
</tr>
</thead>
<tbody>
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<td>SP</td>
<td>0</td>
<td>@</td>
<td>P</td>
<td>@</td>
<td>P</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0 0 0 1 1</td>
<td>!</td>
<td>1</td>
<td>A</td>
<td>Q</td>
<td>A</td>
<td>O</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0 0 1 0 2</td>
<td>&quot;</td>
<td>2</td>
<td>B</td>
<td>R</td>
<td>B</td>
<td>R</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>#</td>
<td>3</td>
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<td>S</td>
<td>C</td>
<td>S</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>D</td>
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<td></td>
</tr>
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<td>U</td>
<td>E</td>
<td>U</td>
<td></td>
<td></td>
</tr>
<tr>
<td>0 1 1 0 6</td>
<td>&amp;</td>
<td>6</td>
<td>F</td>
<td>V</td>
<td>F</td>
<td>V</td>
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<td></td>
</tr>
<tr>
<td>0 1 1 1 7</td>
<td>'</td>
<td>7</td>
<td>G</td>
<td>W</td>
<td>G</td>
<td>W</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0 0 0 8</td>
<td>(</td>
<td>8</td>
<td>H</td>
<td>X</td>
<td>H</td>
<td>X</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0 0 1 9</td>
<td>)</td>
<td>9</td>
<td>I</td>
<td>Y</td>
<td>I</td>
<td>Y</td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0 1 0 A</td>
<td>:</td>
<td>J</td>
<td>Z</td>
<td>J</td>
<td>Z</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0 1 1 B</td>
<td>+</td>
<td>K</td>
<td>C</td>
<td>K</td>
<td>C</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1 0 0 C</td>
<td>,</td>
<td>L</td>
<td>\</td>
<td>L</td>
<td>\</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1 0 1 D</td>
<td>-</td>
<td>M</td>
<td>J</td>
<td>M</td>
<td>J</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1 1 0 E</td>
<td>&gt;</td>
<td>N</td>
<td>^</td>
<td>N</td>
<td>^</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1 1 1 F</td>
<td>/</td>
<td>?</td>
<td>O</td>
<td>-</td>
<td>O</td>
<td>-</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
### BASIC CODE SET FOR 96 CHARACTER PRINTERS

**NOTES:**

- **HEXDECIMAL = ASCII COLUMN, ASCII ROW.**
- **IOU's FOR 96 CHARACTER PRINTERS FORCE BIT 6 TO A 1 WHEN BIT 7 (2^6) IS 0. THIS PRINTS COLUMNS Ø AND 1 AS CHARACTERS IN COLUMNS 2 AND 3.**
- **ALL BITS ARE ENABLED FOR CONTROL CODES (SEE TABLE).**
- **EXACT CHARACTERS IN POSITIONS 6,Ø AND 7B THRU 7F MAY VARY SLIGHTLY BETWEEN PRINTERS. THESE POSITIONS ARE NOT AVAILABLE ON THE SELECTRIC.**

---

**1-52**
DATA PRODUCTS

ADJUSTMENT PROCEDURES FOR DP-2230 SERVO PCB

These adjustments will help to alleviate problems of overprinting with possible hammer faults and/or format errors. Also, print speed can be increased as much as 30%. All adjustments are on the Servo PCB and should be performed anytime Servo PCB is replaced or problems in Servo systems come up.

The adjustments are in order as follows:

6/8 LPI Adjustment
- Scope Settings
  CH1 - 0.5 VCM, TP J11-2
  CH2 - 0.5 VCM, TP J11-3
  Time - 10 MS Div.
  Trig - EXT (-), TP J21-1
  Vert. Mode - Chop

- 6 LPI Adjustment
  Place a loop of single part 11 X 14 paper around drum gate for this adjustment. Establish APT "Print" test, manual section, print E's. Adjust R218 for minimum overshoot and undershoot of both signals. (Refer to Figure 1 below.)

![Figure 1](image)

FIGURE 1
6 LPI & 8 LPI PAPER STEP

- 8 LPI Adjustment
  Leave all scope settings and TP's as established in 6 LPI adjustment. Place the 6/8 LPI switch on operator panel to the 8 LPI position and start "Print" test. Adjust R204 for minimum overshoot and undershoot of both signals.
DATA PRODUCTS

ADJUSTMENT PROCEDURES FOR 'DP-2230 SERVO PCB. Continued

Slew Speed Adjustment

Scope Settings
- CH1: 2 VCM, TP J20-4
- Time: 1 MS Div.
- Trig: INT (-) CH1
- Vert. Mode: CH1

Adjustment Procedure
1. Tape down paper out switches (no paper in printer).
2. Place 6/6 LPI switch in 6 LPI position.
3. Load following test program:
   [IPL] 1d0090 4d4490 0000A3 0003A7 [XMIT]
   (d - To be replaced by Controller Address.)
4. On Terminate, the tractors will rotate then stop as system checks status, and then rotate again, etc. As tractors are rotating, adjust R206 for 8.1 ms (±0.2 ms) from trailing edge to trailing edge as indicated in Figure 2 below.

Verification Procedure for 6/8 LPI and Slew Speed Adjustment

Scope Settings
- CH1: 10 VCM, TP J21-1
- CH2: 2 VCM, TP J20-4
- Time: 5 MS Div.
- Trig: INT (-) CH1
- Vert. Mode: Chop

Figure 2
DATA PRODUCTS

ADJUSTMENT PROCEDURES FOR DP-2230 SERVO PCB Continued

Slew Speed Adjustment For 6/8 LPI Continued

Test Procedure

1. Remove tape from paper out switches and load paper into printer.
2. Establish ATP "Print", manual section, print E's.
3. Look at CH2 and verify that it is 50 ms or less from trailing edge to trailing edge. There are no adjustments to this procedure.
4. If the 6/8 LPI and slew speed are correct, then verification will be correct. Refer to Figure 3 for correct adjustment.

\[ \text{J21-1 (PFPM)} \]
\[ \text{J20-4 (PFCK)} \]

\[ \text{\textbf{FIGURE 3}} \]

\[ \text{PAPER FEED TIMING VERIFICATION} \]

NOTE: If time delay is greater than 50 ms, the paper feed servo needs repair. No further adjustments should be attempted. Replace paper feed servo board and make previous adjustments again.

Move Hammer Bank Left/Right Servo Adjustment

Scope Settings

CH1 - 0.5 VCM, TP J3-5
CH2 - 0.5 VCM, TP J3-3
Trig - EXT (-), TP J23-2 (Trailing Edge, HI to LO)
Time - 20 ms Div. (10 ms after signal is established)

NOTE: Signal may be easier to catch if scope is in NORM mode instead of Auto mode.
DATA PRODUCTS

ADJUSTMENT PROCEDURES FOR DP-2230 SERVO PCB CONTINUED

Move Hammer Bank Left/Right Servo Adjustment

• Test Procedure
  The test can be done using the ATP test or with the test program written at the end of this procedure. If ATP test is used, a paper loop should be used in the printer. With the test program, no paper is used. (Paper is loaded for Paper Out Switch.)

• ATP Test Procedure
  Start ATP Print Test, manual section, print E's. As printer is printing, adjust R145 to resemble Figure 4. Minimize the over/under shoot of signal.

• Test Program Procedure
  Load program at Device "Zero". Make sure Paper Out Switches are down, paper or taped. Follow adjustment procedure as above using Figure 4.

![FIGURE 4](image)

J3-5 (HBPOS)

J3-3 (HBTS)

Final Hammer Bank Servo Adjustment

• Same as previous adjustment except for trigger.
  Trig = EXT (+), LO to HI

• Establish test procedure as in previous adjustment and adjust R127 to resemble Figure 5, again minimizing the over/under shoot of the signal.

1-56
DATA PRODUCTS

ADJUSTMENT PROCEDURES FOR DP-2230 SERVO PCB CONTINUED

Hammer Bank Servo Adjustment

[Diagram showing Undershoot and Overshoot]

NOTE: Test Program for Hammer Bank Servo Adjustment:

[IPL] 008BF2200000 00000A [XMIT]
Enter 2 1/2 CRT Lines of Spaces [XMIT].

[IPL] 1d009D 008BF220008d 0000A7 [XMIT]
\(d\) = Printer Device Number

Verification Test of Hammer Bank Servo Adjustments

• Scope Settings

CH1  - 2 VCM, TP J22-3
CH2  - 5 VCM, TP J23-1
Time  - 5 ms Div.
Trig.  - INT (+) CH1
Vert. Mode - Chop (Place Trigger Mode to NORM)

• Test Procedure

Establish 4TP Test "Print", manual section, print E's. The signal must be less than 50 ms from leading edge of HBMC to the trailing edge of HBDOS, and should fall between 37-50 ms (refer to Figure 6).

[Diagram showing timing verification]

NOTE: If the verification test does not meet these requirements, stop all other tests or adjustments until problem in Hammer Bank Servo System is resolved. Problem is either in previous adjustment, Servo PCB, or Hammer Bank System.

1-57
5100 SERIES PRINTER

UNPACKING

Stand the printer carton on its end, open and remove the foam end piece. The printer may now be lifted from the carton. Next clip the cable tie used to hold the head securely during shipping. Save all packing material for possible return shipment.

CABLING AND SWITCH SETTINGS

The printer can be directly driven by the IOU-39P, or may serve as a slave printer for the CRT2 or the 210 system. The 232/V24 cable is used in all three configurations. This cable is available in four lengths:

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Length</th>
</tr>
</thead>
<tbody>
<tr>
<td>42055-001</td>
<td>6 ft.</td>
</tr>
<tr>
<td>42055-002</td>
<td>12 ft.</td>
</tr>
<tr>
<td>42055-003</td>
<td>25 ft.</td>
</tr>
<tr>
<td>42055-004</td>
<td>50 ft.</td>
</tr>
</tbody>
</table>

Note: Greater lengths will require line drivers.

* The 5100 printer can also be cabled to an R.D.I. Box. Refer to pages 1-90 to 1-9G for this configuration.

IOU-39P Interface System Printer -

**CONFIGURATION #1**

LOCAL (LESS THAN OR EQUAL TO 50 FT)

- A Maximum length of 50 ft. of 232/V24 cable can be used to connect the Printer and IOU-39.
5100 SERIES PRINTER
CONFIGURATION #2 LOCAL (Greater than 50 ft.)

- Two line driver units needed: One connects to IOU-39 through 232V24 cable, the other connects to printer through 232V24 cable.
- Note: the P3 connector of the IOU-39 is used in this case.
- Detail on the line driver unit connection is shown below.
5100 SERIES PRINTER

- Line driver assembly, box and P.C.B. . . . . Part number - M42118-001

- The 4 conductor unshield cable used between line drivers is available in five different lengths:

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Length</th>
</tr>
</thead>
<tbody>
<tr>
<td>M42851-001</td>
<td>100 ft.</td>
</tr>
<tr>
<td>M42851-002</td>
<td>500 ft.</td>
</tr>
<tr>
<td>M42851-003</td>
<td>1000 ft.</td>
</tr>
<tr>
<td>M42851-004</td>
<td>5000 ft.</td>
</tr>
<tr>
<td>M42851-005</td>
<td>10,000 ft.</td>
</tr>
</tbody>
</table>

CONFIGURATION #3 MODEMS REMOTE

- If printer to system distance exceeds 2 miles modems are required. Modem communications take place over leased or dial-up lines using asynchronous modems at 1200 BPS. Ordering and installation of modems and phone lines is the responsibility of the customer. Controller and printer are connected to the modems using 232V24 cables.

A) Leased Lines

The suggested modem for leased lines is the Bell 202T with the options listed on the following page.
5100 SERIES PRINTER

Circuit 4-wire, multipoint, private line
Received data squelch 9 msec
Clear-to-send delay 8 msec
Fast carrier detection IN
Soft carrier turn-off 8 msec
Receive data clamp IN
Grounding AB connected to AA
Alternate voice Optional
Dial backup Optional
Carrier detector reset OUT
Continuous carrier IN at master site, OUT at all slave stations.

B) Dial-up Lines

A pair of dial-up full duplex 2-wire line modems may be used between an IOU-39 controller and a remote site. The suggested modem is the Bell 212A data set with the following options:

CC indication for AL ON
Speed control By HS button
Interface controlled Busy OUT
Transmitter timing Internal
1200 bps operation Asynchronous
Character length 10 bit
Receiver responds to DL OUT
Interface controlled DL OUT
Loss of carrier disconnect OUT
Receive space disconnect OUT
CB/CF indications Separate
Send space disconnect OUT
Auto Answer IN
Answer mode indication-CE OFF
Speed mode HIGH
Interface speed indication OUT
CN and TM assignments CN25, TM not connected
Grounding AB connected to AA

1-64
# 5100 SERIES PRINTER

## SWITCH SETTINGS - IOU39P

**Configuration Switches Setting for IOU-39P Boards:**
IOU39 P and 5100 ROMS must be Rev L or higher.

<table>
<thead>
<tr>
<th>Printer Type</th>
<th>IOU-39 Configuration Switches</th>
</tr>
</thead>
<tbody>
<tr>
<td>5131 Printer</td>
<td>8 1 1 0 (1)* 0 0 0</td>
</tr>
<tr>
<td>1200 BPS</td>
<td>0 1 0 0 0 0 0</td>
</tr>
<tr>
<td>9600 BPS</td>
<td>1 1 0 0 0 0</td>
</tr>
</tbody>
</table>

**NOTE:** Set switch 4 to 1 for remote and 0 for local.
5100 SERIES PRINTER

210 System Interface Printer

- OTC-COM S (preferred) or OTC-COM R (alternate) is used in any empty slot in the card cage. Place the connector for the board as shown below. (This connector is part of the cable harness inside the 210). Connect 232/V24 extension cable as shown.

RPTR-11C or later ROMS must be used on the COM S or COM R boards.

Switch Settings OTC-COM S, OTC-COM R

<table>
<thead>
<tr>
<th>Switch</th>
<th>Setting</th>
</tr>
</thead>
<tbody>
<tr>
<td>8Ω</td>
<td>ON</td>
</tr>
<tr>
<td>4Ω</td>
<td>ON</td>
</tr>
<tr>
<td>2Ω</td>
<td>OFF</td>
</tr>
<tr>
<td>1Ω</td>
<td>ON</td>
</tr>
<tr>
<td>0Ω</td>
<td>ON</td>
</tr>
<tr>
<td>0Ω</td>
<td>COM S = ON, COM R = OFF</td>
</tr>
<tr>
<td>0Ω</td>
<td>OFF</td>
</tr>
<tr>
<td>0Ω</td>
<td>OFF</td>
</tr>
</tbody>
</table>

1-66
5100 SERIES PRINTER

SWITCH SETTINGS - PRINTER

• Two DIP switch packs are located on the printer board (See Interconnection Diagram, Figure F). The 8 switch pack at 3N establishes UART controls as follows:

0=OFF 1=ON

<table>
<thead>
<tr>
<th>BPS</th>
<th>SW1</th>
<th>SW2</th>
<th>SW3</th>
<th>SW4</th>
</tr>
</thead>
<tbody>
<tr>
<td>50</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>75</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>110</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>134.5</td>
<td>0</td>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>150</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>300</td>
<td>0</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>600</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>1200</td>
<td>0</td>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1800</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>2000</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>2400</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>3600</td>
<td>1</td>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>4800</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>7200</td>
<td>1</td>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>9600</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>19.2K</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

SW5
ON = PARITY
OFF = NO PARITY

SW6
ON = 2 STOP BITS
OFF = 1 STOP BIT

SW7
ON = 8 DATA BITS
OFF = 7 DATA BITS

SW8
ON = ODD PARITY
OFF = EVEN PARITY

1-68
The 10 switch DIP pack located at 4N on the Printer logic board (See Interconnection Diagram, Figure F).

MODEL 5131 5133 5136

1-69
5100 SERIES PRINTER

POWER OPTIONS

- The following power options exist:

<table>
<thead>
<tr>
<th>Voltage</th>
<th>Frequency</th>
</tr>
</thead>
<tbody>
<tr>
<td>100 VAC</td>
<td>50/60 Hz</td>
</tr>
<tr>
<td>115 VAC</td>
<td>50/60 Hz</td>
</tr>
<tr>
<td>220 VAC</td>
<td>50/60 Hz</td>
</tr>
<tr>
<td>240 VAC</td>
<td>50/60 Hz</td>
</tr>
</tbody>
</table>

- Printers shipped to international sites may require modification of the line cord wall plug. It is the responsibility of the distributor to install the line cord plug common to their locale.

- Place the proper fuse in the line fuse receptacle located below the power On/Off switch on the back panel.

<table>
<thead>
<tr>
<th>Voltage</th>
<th>Amps</th>
<th>Type</th>
<th>Voltage</th>
</tr>
</thead>
<tbody>
<tr>
<td>110 VAC</td>
<td>2A</td>
<td>SLO-BLO</td>
<td>125 V</td>
</tr>
<tr>
<td>115 VAC</td>
<td>2A</td>
<td>SLO-BLO</td>
<td>125 V</td>
</tr>
<tr>
<td>220 VAC</td>
<td>1A</td>
<td>SLO-BLO</td>
<td>250 V</td>
</tr>
<tr>
<td>240 VAC</td>
<td>1A</td>
<td>SLO-BLO</td>
<td>250 V</td>
</tr>
</tbody>
</table>

- The following four illustrations show the power option strapping configurations on the terminal barrier strip which is adjacent to the power transformer on the rear panel assembly.

- Two styles of barrier strip jumpers are used for the power option configurations:

<table>
<thead>
<tr>
<th>Jumper</th>
<th>Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>2 position barrier strip jumpers</td>
<td>A11669-001</td>
</tr>
<tr>
<td>3 position barrier strip jumpers</td>
<td>A11669-002</td>
</tr>
</tbody>
</table>
5100 SERIES PRINTER

POWER OPTIONS

100 VAC
50/60 Hz

Install a jumper wire as shown in detail "A" from pin #1 to pin #4

Install barrier strip jumpers as shown

Install 2A, SLO-BLO, 125V Fuse in fuse holder on rear panel

Detail "A"

115 VAC
50/60 Hz

Install barrier strip jumpers as shown

Install 2A, SLO-BLO, 125V Fuse in fuse holder on rear panel
5100 SERIES PRINTER

POWER OPTIONS

220 VAC
50/60 Hz

Install a jumper wire as shown in detail "A" from pin #3 to pin #7

Install barrier strip jumpers as shown

Install a 1A, SLO-BLO, 250 V Fuse in the fuse holder

240 VAC
50/60 Hz

Install a jumper wire as shown in detail "A" from pin #1 to #6

Install a jumper wire as shown in detail "A" from pin #3 to pin #7

Install barrier strip jumpers as shown

Install a 1A, SLO-BLO, 250 V Fuse in the fuse holder
PLATEN ADJUSTMENT

- With the printer assembly removed from the clamshell enclosure, remove the ribbon cartridge from the print head. Rotate the form thickness knob clockwise situating the print head in its closest position relative to the platen bar. Do not change the position of the form thickness knob throughout this adjustment procedure.

![Diagram](image-url)

**Figure G**

- Loosen slightly the platen retainer screws (2 located on each end of the platen bar) enabling the platen bar to be moved, yet still held in position. (See Figure G). Manually position the print head carriage as far toward the left margin as possible. Using a feeler gauge, align...
the platen bar to be 0.010 inches from the nozzle of the print head (see Figure H).

- Manually position the print head carriage as far toward the right margin as possible and likewise align the platen bar to a 0.010 inch clearance from the nozzle of the print head using a feeler gauge. The platen retainer screws on the right margin end of the platen bar are now tightened.

Figure H
ATP TEST

• Use the ATP program PRINT or QAN 39 for testing.

OPERATING SYSTEM DIAGNOSTIC UTILITIES

• The PRINT program is also available as one of the Operating System Diagnostic Utility programs under the name *PRINT.

SELF TEST

• Depressing the SELF TEST switch while in the OFFLINE mode causes the printer to print the complete character set in bidirectional mode. Momentarily tapping the SELF TEST switch at the end of a print line will cause printing to stop. Depressing the ONLINE switch will also stop the test printing.
5100 SERIES PRINTER

PRINT HEAD TEST PROCEDURES

Shorted coils in the print head can produce the following symptoms:
the carriage moves back and forth normally, but the print-wire
solenoids don't fire. This is usually accompanied by the failure
of fuse F1. The logic board will probably be damaged. In the
event of such a failure, both the print head and the logic board
should be checked.

To check the print head:
1. Unplug the AC line cord.
2. Unplug the head cable from connector J8.
3. Verify the coil resistances via the head cable P8
   according to the following table:

<table>
<thead>
<tr>
<th>Pin Numbers</th>
<th>Resistance</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 to 2</td>
<td>Ribbon Motor 20-30 ohms</td>
</tr>
<tr>
<td>3 to 4</td>
<td>Solenoid #1 3-4 ohms</td>
</tr>
<tr>
<td>5 to 6</td>
<td>Solenoid #3 3-4 ohms</td>
</tr>
<tr>
<td>7 to 8</td>
<td>Solenoid #5 3-4 ohms</td>
</tr>
<tr>
<td>9 to 10</td>
<td>Solenoid #7 3-4 ohms</td>
</tr>
<tr>
<td>11 to 12</td>
<td>Solenoid #2 3-4 ohms</td>
</tr>
<tr>
<td>13 to 14</td>
<td>Solenoid #4 3-4 ohms</td>
</tr>
<tr>
<td>15 to 16</td>
<td>Solenoid #6 3-4 ohms</td>
</tr>
<tr>
<td>17 to 18</td>
<td>Solenoid #8 3-4 ohms</td>
</tr>
<tr>
<td>19 to 20</td>
<td>Solenoid #9 3-4 ohms</td>
</tr>
</tbody>
</table>

Note: Some method must be used to access the pins on J8,
such as a short piece of wire, or sharply pointed
ohm meter probes used at the end of the ribbon cable.

1-75
To check the print-wire driver transistors on the logic board:

1. Unplug the AC line cord.
2. Unplug the head cable from connector J8.
3. Test for damaged driver transistors by measuring the resistance from the mounting tabs to logic board ground on transistor Q24 through Q32. This resistance should measure infinity on the 200K ohm scale.

2. CABLE ORIENTATION

1. Pin 1 of the head cable P8, should be indicated by a triangular symbol on the connector, and should mate with pin 1 of J8 which is labelled on the PC board. Since some cables may have P8 installed backwards a resistance check should be made using the table on the previous page.

2. J1, J2, J6, J7 and J10 are keyed and bear a ridge on one side of the molex connector.

Figure 1
1-77
5100 SERIES PRINTER

3. **PC BOARD MOUNTING**

To prevent shorts be sure the fibre washers have been installed under the mounting screws when the Logic Board is replaced. Also check to be sure that nylon caps are in place on the standoffs and that the ground lead is not shorting.

4. **POWER TRANSFORMER SECONDARY CABLES**

1. The cable of yellow wires from the power transformer secondary is routed to connector J5 on all logic boards.

2. The cable of blue wires from the power transformer secondary is routed to the logic board only if a J11 connector exists on the board. On some boards J11 will reside at the end of a short pigtail of wires. Boards not having J11 do not use the blue cable from the power transformer secondary, yet are still totally compatible.
REMOVAL/REPLACEMENT PROCEDURES

TOOLS REQUIRED: None

- Remove the ribbon from the print head ribbon guides.
- Remove the fingerboard P037 on the print head cable from connector J037 on the video amplifier.
- On the left side of carriage assembly release clamping spring tension by pulling spring up and over spring retainer.
- Remove print head by sliding off carriage towards front of printer.

Note: Releasing tension on clamping spring will enable print head to be removed. When installing print head assembly, ensure the clamping spring catches the lip underneath the head, then pull spring up and over spring retainer.
Print Head Penetration

- Loosen penetration control knob and hardware securing head adjusting indicator.
- Move penetration control knob away from platen and as close as possible, but not contacting, the print head.
- Lightly tighten indicator hardware.
- Insert a 0.177mm (0.007 in.) feeler gauge between the print head and platen. Move penetration control knob towards the platen until 0.177mm (0.007 in.) gap is met. Tighten penetration control knob.
- Holding penetration control knob, loosen hardware securing head adjusting indicator and position indicator against penetration control knob.
- Tighten head adjusting indicator hardware.
- Loosen penetration control knob and move back and forth.
- Retighten control knob against indicator and recheck for 0.177mm (0.007 in.) gap.

Figure 2

Penetration Control Adjustment Print Head Assembly
REMOVAL/REPLACEMENT PROCEDURES

TOOLS REQUIRED: Phillips Head Screwdriver.

- Remove print head cable fingerboard P037 from connector J037 on video amplifier.
- Remove connector J036, on cable of video amplifier, from connector P036 on logic board.
- Remove the four carriage solenoid wires attached to the bottom of the video amplifier.
- Remove the two screws (2), lockwashers (3) and flatwashers (4) and remove video amplifier (1) and attaching cable from printer.

ADJUSTMENTS

NOTE: The video amplifier contains an optical pickup assembly which must be adjusted. Steps 1 and 2 detail the mechanical alignment of the pickup assembly, while step 3 outlines...
ADJUSTMENTS (cont.)

the electrical adjustments.

Optical Pickup Alignment With Respect To Timing Fence.

- Loosen the hardware supporting the video amplifier to the carriage.
- Ensure the timing fence is centered in the slot of the optical pickup assembly. If required, loosen optical pickup mounting hardware and position pickup assembly so that timing fence is centered in slot and tighten hardware.
- Ensure the bottom of the optical pickup assembly is parallel with bottom of timing fence, once positioned.
- When above conditions have been met, tighten video amplifier mounting hardware.

Figure 4

OPTICAL PICKUP ASSEMBLY ADJUSTMENT WITH RESPECT TO TIMING FENCE

1-82
VERTICAL AND HORIZONTAL ALIGNMENT OF OPTICAL PICKUP ASSEMBLY

• Sight right edge of optical pickup assembly and ensure parallel alignment with respect to timing fence encoder lines. If not parallel, loosen right side hardware mounting the video amplifier to carriage and adjust video amplifier until optical pickup assembly is parallel with encoder lines.

• Sight bottom edge of optical pickup assembly and ensure parallelism with bottom edge of timing fence.

Figure 5

VERTICAL AND HORIZONTAL ALIGNMENT OF OPTICAL PICKUP ASSEMBLY
ELECTRICAL ADJUSTMENTS OF VIDEO AMPLIFIER

NOTE: The main logic board determines the forward and reverse direction of the print head by comparing the leading and trailing edges of one video channel pulse with the other video channel pulse. The outputs from both channels are adjusted for a 50% duty cycle and a constant 90° phase shift between channels. Both the duty cycle adjustment and the phase adjustment are performed separately as follows.

DUTY CYCLE ADJUSTMENT

- Ensure that the optics block on the underside of the video amplifier board is centered over the timing fence, so that the block does not rub the timing fence as the print head is manually moved right and left.

- Monitor video output V1 at test point E1 on the power driver board while the print head is moving at a constant speed.
  
  Note: The print head can be moved at a constant speed by depressing the OVERRIDE switch while the printer is deselected. This places the printer in the self-test mode.

- Check the duty cycle of the V1 waveform and, if it is not 50% as shown in Figure 6 proceed to next step.

- Stop carriage motion by deactivating self-test mode, turn adjusting pot R4 slightly, reactivate self-test mode, and again recheck for a 50% duty cycle. Repeat until 50% duty cycle is achieved for V1.
  
  Note: If the 50% duty cycle cannot be achieved, check the pulse width and make sure it is constant over the complete travel of the print head. If not, the timing fence should be adjusted so that it is parallel with the guide bars. If the timing fence is adjusted, recheck the duty cycle for the previously set-up video channel.

- Repeat Steps 2 through 4 above while monitoring V2 at E2 and, if necessary, adjust R10 to obtain a 50% duty cycle for V2. Refer to Figure 6.
PHASE ADJUSTMENT OF VIDEO AMPLIFIER

Monitor V1 and V2 simultaneously at E1 and E2 on the power driver board while the print head is moving at a constant speed (see figure 20).

Check the phase relationship between V1 and V2 as the head moves in both the forward and reverse directions. As shown in Figure 6, the phase relationship between V1 and V2 should be a constant 90° phase shift over the complete travel of the print head (i.e., width C equals 90° when it equals 1/4 width D). If phase adjustment is required, proceed to next step.

Stop carriage by deactivating the self-test mode, and turn printer power off. Manually move carriage all the way right to the EOP switch.
PHASE SHIFT ADJUSTMENT (cont.)

- Loosen (approximately one turn) the left side mounting screw which attaches the video amplifier circuit board to the carriage.
- Move left side of the circuit board all the way up until the bottom of the slotted mounting hole touches the mounting screw.
- Turn printer power on, activate self-test mode, and check phase adjustment again. Refer to Figure 6.
- If further adjustment is required, turn printer power off, move left side of board slightly down in the slotted mounting hole and check phase again as in the previous step.
- Repeat the two previous steps until phase adjustment of V1 and V2 is correct.
- When phase is correct, tighten left side mounting screw on video amplifier.

REMOVAL/REPLACEMENT PROCEDURE - Timing Fence Assembly

TOOLS REQUIRED: 3mm Hex Key.

- Loosen the four screws from left and right timing fence mounting brackets and remove fence.

Adjustments

- Loosen four screws mounting timing fence and adjust fence so that the first window is located 106.6mm (4.2 in.) from left frame.

Note: This adjustment allows for the left hand print margin to align with the number 1 on the column scale assembly.

Figure 7 - Timing Fence Assembly Adjustment
Preventive Maintenance

- Using soft, clean cloth wipe both sides of timing fence, if necessary use liquid freon.

  CAUTION: NEVER USE AN ORGANIC SOLVENT AS THIS WILL DAMAGE THE TIMING FENCE.

- Inspect for scratches in the encoder lines and proper mechanical alignment on the timing fence.

CARRIAGE ASSEMBLY

Removal/Replacement Procedure

Tools Required: Slotted and Phillips Head Screwdriver Snap Ring Tool.

WARNING: TURN POWER OFF BEFORE REMOVING CARRIAGE ASSEMBLY

- Move the carriage assembly to the center of the printer.
- Remove the ribbon cartridge assembly.
- Remove the print head and video amplifier assemblies per pages 1-79 and 1-81.
- Remove the power driver board assembly per page 1-89.
- Loosen the carriage drive belt by turning, counter-clockwise, the two adjusting nuts on the idler pulley assembly. NOTE: The idle pulley assembly is located on the left side of the printer.
- Remove the retaining ring on the left end of the upper guide bar.
- Remove the two screws mounting the upper guide bar to the right frame.
- Loosen the locknut and screw attaching the bottom of carriage to the bottom of the guide bar.
- Remove the upper and lower ribbon drive monofilament from the ribbon drive shaft.
CARRIAGE ASSEMBLY (cont.)
Removal/Replacement Procedure

• Remove the two screws and lockwashers clamping the carriage drive belt to the carriage assembly.

• While supporting the carriage assembly, slide the upper guide bar to the right through the carriage assembly.
  
  NOTE: Retain the rubber O ring removed from the left end of the upper guide bar.

• Remove the carriage assembly from the printer.

Figure 8
Carriage Assembly Removal/Replacement
1-88
CARRIAGE ASSEMBLY (cont.)

Adjustments

- Adjust ready to print (RTP) actuator arm parallel to and 59mm (2.3 inches) from the upper guide bar.
- Adjust end on print (EOP) actuator arm parallel to and 55mm (2.1 inches) from the upper guide bar.
- Once the actuator arms have been adjusted slowly move the carriage assembly from left to right ensuring the RTP and EOP actuator arms do not contact the RTP and EOP switches.

Preventive Maintenance

- Wipe the upper and lower guide bars clean using a soft clean cloth.

STEPPER MOTOR ASSEMBLY, CARRIAGE DRIVE

REMOVAL/REPLACEMENT PROCEDURE

Tools Required: M5 Nut Driver, Phillips Head Screwdriver.

- Disconnect connector P028 on stepper drive motor from connector J028 on power driver board assembly.
CENTRONICS 702

STEPPER MOTOR ASSEMBLY, CARRIAGE DRIVE
Removal/Replacement Procedure (cont.)

- Loosen tension on main drive belt (#14 in Figure 10) by turning two adjusting screws (8) counter-clockwise.
- Remove the stepper drive motor (1) by removing four screws (2) lockwashers (3) and nuts (4).
- Separate main drive belt (14) from the carriage assembly by removing the main drive belt holder (15) and two screws (16) and lockwashers (17).
- Remove main drive belt and idler pulley (7) by removing two snap rings (12), adjusting screws (8) and slide idler pulley shaft (11) through idler pulley.

Figure 10  STEPPER MOTOR ASSEMBLY, CARRIAGE DRIVE
1-90
STEPPER MOTOR ASSEMBLY, CARRIAGE DRIVE

Adjustments

- With the carriage assembly in the left most position adjust main drive belt to deflect 8 to 9mm (.314 to .354 in.) when a 300 gram (.66 lbs.) load is applied at the center of the belt.

Note: It is best to use a wide load distributed 63.5mm to 76.2mm (2-1/2 in. to 3 in.) over the belt.

STEPPER MOTOR ASSEMBLY, PAPER DRIVE

Removal/Replacement Procedures

Tools Required: M5 Nut Driver

- Disconnect two pin connector P028 of stepper motor assembly from connector J028. (See following page, Figure 12).
CENTRONICS 702

STEPPER MOTOR ASSEMBLY, PAPER DRIVE
Removal/Replacement Procedures (cont.)

Figure 12
STEPPER MOTOR ASSEMBLY, PAPER DRIVE

- Remove line feed pulley (7) by removing set-screw (6) and sliding pulley off stepper motor shaft.
- Remove four screws (2,3) lockwashers (4) and nuts (5) and remove stepper motor assembly from printer.

Adjustments
- At a point equidistant from the line feed pulley and paper feed pulley, adjust tension of the belt to deflect 4.5mm (.18 inch) with a 300 gram (10.6 ounces) load applied.

Note: The belt tension is adjusted by the tension roller on the right frame.

4.5mm (.18 inches)
300 grams (10.6 ounces)

FIGURE 13
PAPER FEED TIMING BELT ADJUSTMENT
PLATEN ASSEMBLY

Removal/Replacement Procedure

Tools Required: M4 Hex Key, Flat Blade Screwdriver

- Remove the paper feed knob from the right side of the platen assembly.
- Loosen tension of paper feed timing belt and remove timing belt from pulley.
- Remove the two stop collar springs (3) on ends of platen assembly.
- Remove pin tractor drive pulley (5) and timing belt from left end of platen.
- Slide the two brass bushing (2,4) towards the center of the printer and lift platen assembly out of the printer.
PLATEN ASSEMBLY

Adjustments

The platen assembly must be parallel to the travel of the carriage, if not adjust as follows:

- Operate printer and ensure print quality is uniform for the full 132 character printed.
- If print quality is not uniform, loosen bolt and rotate adjusting plate until platen assembly is parallel to the travel of carriage assembly.
- Once adjusted tighten mounting hardware.

Preventive Maintenance

Clean platen assembly using a mild detergent.

TRACTOR DRIVE AND PIN FEED ASSEMBLY

Removal/Replacement Procedure

Tools Required: Phillips Head Screwdriver

Tractor Drive Assembly

- Disconnect connector P044 of harness assembly (9) from connector J044.
Removal/Replacement Procedure (cont.)

- Remove left (2) and right (3) stud screws, two flatwashers (5) and nuts (4) supporting the sub-tractor drive assembly.
- Remove the two screws (6) lockwashers (7) and flatwashers (8) and remove sub-tractor drive assembly from the printer.

Tractor Units, Left and Right

- Remove the sub-tractor drive assembly per paragraph above.
- Loosen set-screw mounting drive pulley on left side of tractor drive assembly and remove pulley and side plate.
- Loosen set-screw mounting hardware bushing to the left support assembly and remove bushing and support plate.
- Slide the left and right tractor units off drive shaft and support bar of tractor drive assembly.
TRACTOR DRIVE AND PIN FEED ASSEMBLY

Adjustments

Tractor Drive Belt Tension

- Adjust tension of belt such that when a load of 156-249 grams (5-8 ounces) is applied at the center of the belt, belt deflection will be 6.35mm (0.25 in.).

![Tractor Drive Belt Tension](image)

6.35mm (0.25 inches)
156-249 grams (5-8 ounces)

Paper Travel

- Using the eccentric knob on the right hand side of the printer, adjust tractor units so that plane of paper travel (top side of tractor unit) is tangent to the platen.

![Paper Travel Adjustment](image)

Figure 17 TRACTOR DRIVE TIMING BELT TENSION

Figure 18 PAPER TRAVEL ADJUSTMENT TRACTOR DRIVE ASSEMBLY
Removal/Replacement Procedure
Tools Required: Phillips Head Screwdriver

- Disconnect all connectors plugged into logic board.
- Remove the three screws (2,5) lockwashers (3,6) and flatwashers, (4,7) and remove the logic board from the printer.

Adjustments

The following electrical adjustments may be required on the main logic board.

<table>
<thead>
<tr>
<th>Function</th>
<th>Signal Name</th>
<th>Element/Pin</th>
<th>Adjustment Resistor</th>
<th>Pulse Width</th>
</tr>
</thead>
<tbody>
<tr>
<td>+5 Volts</td>
<td>+5V out</td>
<td>Test Point-TP4</td>
<td>R4</td>
<td>Adjust to +5V Level</td>
</tr>
<tr>
<td>Print head speeds</td>
<td>-</td>
<td>ME89, pin 10</td>
<td>R40</td>
<td>650-660 usec.</td>
</tr>
<tr>
<td>Printer Strobe</td>
<td>PRTSTB</td>
<td>ME73, pin 6</td>
<td>R50</td>
<td>425-450 usec.</td>
</tr>
</tbody>
</table>

Figure 19

CENTRONICS 702
LOGIC BOARD ASSEMBLY
Removal/Replacement Procedure

Tools Required: Phillips Head Screwdriver

- Remove the following connectors from one another:

<table>
<thead>
<tr>
<th>POWER DRIVER BOARD CONNECTOR</th>
<th>DISCONNECT FROM:</th>
</tr>
</thead>
<tbody>
<tr>
<td>J 28</td>
<td>P028 of Carriage Drive Stepper Motor</td>
</tr>
<tr>
<td>J 29</td>
<td>P029 of Paper Drive Stepper Motor</td>
</tr>
<tr>
<td>J 30</td>
<td>P030 of Video Amplifier Cable Assy.</td>
</tr>
<tr>
<td>J 31</td>
<td>P031 of Main Wire Harness (63703112-4001)</td>
</tr>
<tr>
<td>J 32</td>
<td>P032 of Control Panel Harness</td>
</tr>
</tbody>
</table>

- Remove the six screws (2) and flatwashers (3) and remove the power driver board from the printer.
Line Feed on CR
FOR PARALLEL PRINTER
not used

NO Line Feed on CR
FOR SERIAL PRINTER

DIP Switch
IOU 13
CENTURY 111
Quantel Model 3000

Max. PWR. Requirements
-12v = 0.35A
+12v = 0.10A
+5v = 2.19

Disc Unit PWR
20A for 10 sec.
8A normal

Write Control Instructions
1XYY9D  X = Device Address
         YY = Control Byte

Control Function
01. Set inhibit tr. verify
02. Set inhibit read
     after write check

Status In Instructions
4XYY9D  X = Device Address
         YY = Status Byte

Status Meaning
01. Read busy
02. Write busy
04. End
08. Service request
10. Error
20. Marked sector
40. Invalid seek
80. Inop.
### WRITE CONTROL INSTRUCTIONS

1XYY9D  
X = DEVICE ADDRESS  
YY = CONTROL BYTE

<table>
<thead>
<tr>
<th>CONTROL BYTE</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>INHIBIT TRACK VERIFY</td>
</tr>
<tr>
<td>02</td>
<td>INHIBIT READ AFTER WRITE CHECK</td>
</tr>
<tr>
<td>10</td>
<td>Res. Allow Term Int. (resets Fl)</td>
</tr>
<tr>
<td>14</td>
<td>Set Termination Interrupt</td>
</tr>
</tbody>
</table>

### STATUS IN INSTRUCTIONS

4XYY9D  
X = DEVICE ADDRESS  
YY = STATUS BYTE

<table>
<thead>
<tr>
<th>STATUS BYTE</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>READ BUSY--------</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY---------- ABSENCE OF ALL 3 = HEAD IN MOTION</td>
</tr>
<tr>
<td>04</td>
<td>END-------------------</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>SEEK OR WRITE TERMINATE</td>
</tr>
<tr>
<td>20</td>
<td>---SEE BELOW</td>
</tr>
<tr>
<td>40</td>
<td>INOP. / PACK OFF</td>
</tr>
</tbody>
</table>

### Error Codes

- **0 - 0**: O.K.  
- **0 - 1**: ERROR  
- **1 - 0**: MRKD SCTR-  
- **1 - 1**: INV.SEEK  

---

Revised 3/10/76
IOU-32A/B

MEMOREX DISC 25 M/BYTE
MEMOREX DISC 75 M/BYTE

TYPICAL POWER REQUIREMENTS

-12V = .02A
+12V = .26A
+5V = 7.6A

DISC UNIT POWER

START UP: Not to exceed 30A for 18 seconds
NORMAL: 8.0A nominal @ 117 VAC

IOU-32A
Must be set to Q7.5
IOU-32 works only with
AS/BS CPU (DMA)
Q29B CPU (Non-DMA)

IOU-32B

IOU-32 works only with
AS/BS CPU (DMA)
Q29B CPU (Non-DMA)

Rom labels lower than Rev. H indicate a DMA IOU-32. To update these controllers for non-DMA operation factory modification is required.

Rom labels Rev. H or higher may be modified from DMA to non-DMA or the reverse by simply changing the ROM's.

ROM NUMBERS ROM LABEL
M1H - M12H = DMA IOU-32
M1H M12H = NON-DMA
### WRITE CONTROL INSTRUCTIONS

<table>
<thead>
<tr>
<th>Idyy9D</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>$01</td>
<td>Inhibit Track Verify</td>
</tr>
<tr>
<td>$02</td>
<td>Inhibit Read-After-Write Check</td>
</tr>
<tr>
<td>$03</td>
<td>Set Memory I/O Mode</td>
</tr>
<tr>
<td>$04</td>
<td>Reset Memory I/O Mode</td>
</tr>
<tr>
<td>$05</td>
<td>Branch to Local Memory</td>
</tr>
<tr>
<td>$n6</td>
<td>Select Drive n</td>
</tr>
<tr>
<td>$07</td>
<td>Set Scanning Mode</td>
</tr>
<tr>
<td>$10</td>
<td>Disable Termination Interrupt</td>
</tr>
<tr>
<td>$14</td>
<td>Enable Termination Interrupt</td>
</tr>
<tr>
<td>$40</td>
<td>Disable Sector Pooling</td>
</tr>
<tr>
<td>$50</td>
<td>Enable Sector Pooling</td>
</tr>
<tr>
<td>$60</td>
<td>Disable Alternate Sectoring</td>
</tr>
<tr>
<td>$70</td>
<td>Enable Alternate Sectoring</td>
</tr>
<tr>
<td>$80</td>
<td>Bypass Sector Pool</td>
</tr>
<tr>
<td>$90</td>
<td>Incremental Seek</td>
</tr>
</tbody>
</table>

After RIO or IPL the controller will have terminal interrupt inhibited, scanning mode enabled, sector pooling enabled, alternate sectoring enabled and read after write check and track verify enabled.
The $10$ weight status byte is set whenever a Seek or Write Sequence terminates for any reason. It is reset by a Read, Write, Seek and Fill Buffer the same unit, Write Control Requesting Selected Unit Number or a Write Control Disabling Termination Interrupt.

- The $20$ and $40$ weight status bits are valid only if the $10$ weight status bit is set.

- The $80$ weight status bit is always valid and signals a disc or controller fault requiring operator attention.

**READ STATUS 2**

$$xdyy86 \quad d = \text{Device Address} \quad yy = \text{Test Bit Value} \quad x = \text{Don't Care}$$

$\$FD$ is returned by the IOU-32 A/B

2-2C
INTRODUCTION

Invalid seek (IVS) and temporary read errors (TMP RD ERR) that are intermittent are frequently caused by electro-mechanical problems rather than electronic failure. Some steps to eliminate this problem are given below.

No attempt should be made to evaluate, test, or adjust a disc unit until it stabilizes with temperature. The pack must run for a minimum of 15 minutes, or more if it was taken from another temperature environment.

TESTING

Intermittent errors should be tested by running the Acceptance Test. The test must run error free overnight to be reasonably sure the problem is fixed.

GENERAL PRECAUTIONS

The following is a list of procedures that will help to minimise disc difficulties.

- The disc pack must be in the same temperature environment for at least one hour, and allowed to spin 15 minutes before use.
- Before writing records or generating disc copies, allow the disc to be 'ready' for at least 5 min.
- When the drive is not in use (disc not spinning), the power switch should be "OFF".
- The disc should be powered from the unswitched outlet to protect against power loss when the disc is running and the system is shut down.

Moving The Drive

The heads must be taped in the retracted position when ever the disc drive is moved. Without power, the heads are extremely free to move and would be damaged if the drive is tilted or moved suddenly.

Protecting The Fixed Disc

The diagnostic tests write on most or all disc areas. The customer information on the fixed disc must be copied (use PILFIL) before running disc tests.

Protecting The C.E. Pack

When the C.E. pack is used for head alignment, the pack must be protected from being written upon. To disable the write circuits, install a jumper between pins 1 and 2 (the two top pins) on plug B1 of the drive control PCB, AO. With this jumper installed, an attempt to write causes a loss of "READY".

Disable Restore by unsoldering the jumper from U7-6 on the motherboard and connecting it to U7-7 (ground).

Spindle Binding must be guarded against by ensuring that the pack is evenly seated on the base. If the motor is started when the spindle is binding, the motor burns up.
TROUBLE SHOOTING CHART

The chart below lists things that should be tried before swapping cards. Be aware that the disc may need realignment after some of the mounting screws are tightened.

<table>
<thead>
<tr>
<th>POSSIBLE CAUSE</th>
<th>CHECK FOR</th>
<th>REFERENCE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Head Misalignment</td>
<td>- Loose base plate screws</td>
<td>F.A.#33</td>
</tr>
<tr>
<td></td>
<td>- Loose opt transdcr or shutter</td>
<td>Below</td>
</tr>
<tr>
<td></td>
<td>- Align with C.E.pk</td>
<td></td>
</tr>
<tr>
<td>Servo misadjusted</td>
<td>- Use Iomec procedure</td>
<td>Manual</td>
</tr>
<tr>
<td>Faulty heads</td>
<td>- Dirty heads</td>
<td>*</td>
</tr>
<tr>
<td></td>
<td>- Damaged heads</td>
<td></td>
</tr>
<tr>
<td>R/W card ground</td>
<td>- Gr. strap loose at base plate</td>
<td>*</td>
</tr>
<tr>
<td>Head leads binding</td>
<td>- Properly dressed leads</td>
<td>F.A.#31</td>
</tr>
<tr>
<td>VCO temp. drift</td>
<td>- Install temp. comp. speed sel. module</td>
<td>*</td>
</tr>
<tr>
<td></td>
<td>- Adjust VCO</td>
<td></td>
</tr>
<tr>
<td>Servo Actuator side play</td>
<td>- Loose mtg. screws</td>
<td>*</td>
</tr>
<tr>
<td>Missing sector pulses</td>
<td>- Install FCO#15</td>
<td></td>
</tr>
<tr>
<td>Sector burst off</td>
<td>- Loose sector transdcr.</td>
<td>*</td>
</tr>
<tr>
<td></td>
<td>- Adjust with C.E pk.</td>
<td></td>
</tr>
</tbody>
</table>
-5v supply               |                                               |           |

*See later text.

---

*OPTICAL TRANSDUCER MOUNTING SCREW*

*FRONT ADJUST REAR MOUNTING*
DIRTY OR DAMAGED HEADS

The illustrations below show some examples of head dirt buildup and damage. Clean heads with 91% isopropyl alcohol, and wipe clean. Do not allow alcohol to dry on the surface.

A. Slight oxide buildup. Head should be cleaned and used.

B. Alcohol residue. Head must be cleaned.

C. Oxide buildup in pole piece. Head must be replaced.

D. Slight scratches. No oxide buildup. Head is usable.

E. Oxide buildup due to scratches. Head must be replaced.

F. Crushed (usually burned). Head must be replaced.
IOMEC

GROUND STRAP ON R/W CARDS

Some versions of the R/W card have a ground strap to the base plate. If the screw at the base plate is loose, intermittent errors can occur. Check that the screw is tight whenever there is doubt.

IOU-24C VCO ADJUSTMENT

Adjust the frequency and phase pots (R15 and R1) fully counterclockwise.

Connect a DVM probe to Test Point 1.

Adjust R15 (frequency) clockwise until the voltage at TP1 is .5v-.5v (0v to 1v).

Connect Channel 1 of the oscilloscope to 5C, pin 12. Connect channel 2 to 5C pin 11. Trigger on ch 1, neg. at 50 nsec/cm.

Adjust R1 (phase) until the crossover of the signals is as shown below.

SERVO ACTUATOR SIDE PLAY

The servo actuator mechanism is mounted to the base plate by two screws (see the illustration below). If these screws become loose, there can be intermittent seek and read errors.

![Diagram of actuator mountings screws]
MISSING SECTOR PULSES

If the sector transducer is loose or not properly adjusted, one or more sector pulses can be missed, causing seek or read errors. Check that the sector transducer is secure and properly adjusted (see the illustration below). FCO #15 must also be installed in all IOMEC drives.

---

**Diagram**: Illustration of the IOMEC drive with labeled parts:
- Sector Transducer Screws
- Door Latch Screws
1. Slots on the fixed disc sector plate get dirty and miss sector pulses. Revision D to the interface board makes a modification that unloads the heads when a sector count is missed. This means that if the heads seem to unload for no reason, the sector plate on the fixed disc needs cleaning.

2. The fixed disc may become loose and slip on the spindle. The symptom for this is that the disc needs re-initializing every time it is fired up. Change the fixed disc.

3. R10 on the interface card has been removed to increase the amplitude of the fixed disc sector pulses.

4. Sticking or burned brushes in the servo motor cause failures in the harmonic test. This condition gets worse until the disc fails all of the time. Remove the cap over the brushes, and clean the armature. Sticking brushes are caused by an allen screw that is too long, and presses against the brush holder deforming it.

5. Be sure to check the ECN’s for compatibility of the motherboard and the interface card.

6. The new style motor with thermal protection has one less black wire. This motor needs a 40 mfd. capacitor between the red motor lead and pin 2 of the starting relay.

7. R86 on the power supply board (25ohm 10w) has been removed, and a jumper put in its place. The resistor was added externally to allow heat dissipation. However, with this modification there must also be the fan modification. If there is not, the resistor will become hot enough to unsolder itself.

8. Poor lead dressing on the head wires will cause errors in the harmonic test.

9. Many read/write cards do not have shielding on them. These may work well until something on the card is disturbed (such as replacing a component). Symptoms may then appear that give temporary read errors. This one is very subtle, and is hard to believe when it is encountered.

10. On the old style drives, the head carriage can come off the track in a sideways direction. This happens during service procedures. Check that the carriage is on track before starting the drive.

11. Loose lugs on the filter capacitors may cause ripple. Some screws were too long, leaving the lugs loose when the screw was tight (bottomed).
12. Rattling during start up is caused by a bad motor start relay.

13. Loosing "Ready" can be caused by one of the following:
   - Cover micro switch
   - Loose fuse in power supply
   - Motherboard
   - Missing sector pulse
   - Power supply failure (low voltage)
   - Unsafe - selecting more than one head

14. Cables can get pinched in the front bezel, or rub against the drive belt. This causes just about any symptom that the machine will ever exhibit.

15. The mask portion of the optics (cylinder seek count) may become loose, or even fall off. This can cause seek errors at a minimum, or complete failure.

16. Servo power can be lost from one of the following causes:
   - No 70% speed signal from the interface card
   - Bad servo 1 or 2 card
   - Q12 and Q13 in the power supply (usually shorted)
   - Relay K1 on the power supply may have dirty or misadjusted contacts.
<table>
<thead>
<tr>
<th>PROCEDURE</th>
<th>REMARKS</th>
</tr>
</thead>
</table>
| 1. Remove user cartridge, remove power, and extend drive to obtain access to back of drive. | a. Set CARTRIDGE switch to UNLOCK.  
b. When UNLOCK lamp lights, remove cartridge.  
c. Set POWER switch to POWER OFF.  
d. Set ac switch on drive rear panel to OFF. |
| 2. Remove drive component cover. | a. Connect CHANNEL 1 to pin 10 of the A2 PC card by attaching a mini test clip to U3, pin 3.  
b. Connect CHANNEL 2 to pin 11 of the A2 card by attaching a mini test clip to U3, pin 4.  
c. Attach external sync lead (CRAN A) to TPI on the A5 card by attaching a 16 pin extender to U12; pin 13 is TPI.  
d. Set VERTICAL gain to 50 mv/cm (both channels).  
e. Set TIME BASE to 2 ms/cm.  
f. Set SYNC to EXT (+), CH A.  
g. Set MODE to ADD.  
h. INVERT channel 2. |
| 3. Connect and set oscilloscope to observe the read test points.  
**NOTE**  
Drives below serial number 30400 that do not have F.A. #16 installed, must have U7, pin 6 on the motherboard grounded to prevent restores. | a. Power up drive, install alignment pack.  
b. Set ac power switch (rear) to ON.  
c. Set power switch to POWER ON.  
d. Install alignment pack.  
e. Set cartridge switch to LOCK.  
f. Attach a 14 pin extender to A5, U24. When the disc is spinning at full speed, attach a ground to pin 11 (pin 7 is ground).  
g. Enter the following on the console device:  
(IPL) 0006F30400000000AS (CR)  
10303293330 (CR)  
(IPL) 0006F30500000000A8 (CR)  
10303293330 (CR)  
**NOTE:** The D in the first byte is the disc address. If disc is not at this address, change to suit. |
| 4. Power up drive, install alignment pack. | a. Set ac power switch (rear) to ON.  
b. Set power switch to POWER ON.  
c. Install alignment pack.  
d. Set cartridge switch to LOCK.  
e. Attach a 14 pin extender to A5, U24. When the disc is spinning at full speed, attach a ground to pin 11 (pin 7 is ground).  
g. Enter the following on the console:  
(IPL) 0400S00000AS (CR)  
| 5. Insert SEEK parameters into memory. | a. Allow ample time for the C.E. pack to equalize in temperature with the drive (minimum of 30 minutes).  
a. Length of time required for thermal expansion to equalize depends upon how great the change was in environment from storage to installing.  
b. Enter the following on the console:  
(IPL) 0400S00000AS (CR)  
| 6. Allow ample time for the C.E. pack to equalize in temperature with the drive (minimum of 30 minutes). | a. Enter the following on the console device:  
(IPL) 0006F30400000000AS (CR)  
10303293330 (CR)  
(IPL) 0006F30500000000A8 (CR)  
10303293330 (CR)  
**NOTE:** The D in the first byte is the disc address. If disc is not at this address, change to suit. |
| 7. Position the heads to cylinder 146, head #2. | a. Enter the following on the console:  
(IPL) 0400S00000AS (CR)  
|
IOMEC
Removable Disc Head Adjustment

### PROCEDURE

8. Display the "cats eyes".

**NOTE**
The object of this adjustment is to position the alignment mark at the crossover point. Centering the oscilloscope display is not necessary. (Diablo C.E. pack shown, make loops equal length for other type C.E. packs)

9. Slightly loosen the clamping screw on head 12. Turn the adjusting screw as required until the alignment mark is positioned as in Figure. Move the adjusting screw counter-clockwise to move the crossover to the left.

### REMARKS

<table>
<thead>
<tr>
<th>Head Too Far In</th>
<th>Correct Head Alignment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Head Too Far Out</td>
<td></td>
</tr>
</tbody>
</table>

| a. Move head #2 adjusting screw clockwise to move crossover to the right. |
| b. Move the adjusting screw counter-clockwise to move the crossover to the left. |
### IOMEC Removable Disc Head Adjustment

<table>
<thead>
<tr>
<th>PROCEDURE</th>
<th>REMARKS</th>
</tr>
</thead>
<tbody>
<tr>
<td>10. Re-position heads. Check alignment (snug head clamp to prevent movement when the head is seeking)</td>
<td>a. See step 7a. b. If display does not conform, repeat steps 8 and 9.</td>
</tr>
<tr>
<td>11. Tighten the head #2 clamping screw. (8 inch/lbs, or firm, but not extra tight)</td>
<td>a. Be sure that tightening the clamp does not change the adjustment. b. When clamp screw is tight, back off the adjusting screw to release pressure on the R/W head.</td>
</tr>
<tr>
<td>12. Position the heads to cylinder 146, head #3.</td>
<td>a. Enter the following on the console: (IPL) 0500000000A8 (CR)</td>
</tr>
<tr>
<td>13. Compare the display with Figure (opposite step 8). If necessary, adjust head #3, using the same technique as steps 8 thru 11.</td>
<td>a. Enter program from step 7a (hd. #2). b. Enter program from step 12a (hd. #3).</td>
</tr>
<tr>
<td>14. Check both heads.</td>
<td>a. TIME BASE - 5 usec/cm (CALIBRATED). b. Vertical - .1 v/cm (both chan.)</td>
</tr>
<tr>
<td>15. Change scope settings to check Index-to-burst.</td>
<td>a. (IPL) 000EF300000000A8 (CR) 1D30323130 (CR) (IPL) 000EF3070000000A8 (CR) 1D30323130 (CR)</td>
</tr>
<tr>
<td>16. Load seek parameters.</td>
<td>a. (IPL) 000000000A8 (CR)</td>
</tr>
<tr>
<td>17. Seek to cylinder 10, head #2.</td>
<td>a. CH 1 - U12, pin 13, A5 card. b. CH 2 - U3, pin 4</td>
</tr>
<tr>
<td>18. Verify that the timing pulse is delayed 30 ± 5 usec from the start of the sweep (Index)</td>
<td>c. Time Base - 5 usec/cm (Calibrated) d. Vert - CH 1 - .2v/cm, CH 2 - 50mv/cm e. Mode - ADD f. Trig - CH 1 (+) g. Slope - (+) h. Source - INT i. Reset - Invert Switch</td>
</tr>
</tbody>
</table>

**NOTE**
This adjustment is measured from the leading edge of INDEX to the null point between the positive and negative transitions of the timing pulse.

![Timing Pulse Diagram](image-url)
**PROCEDURE**

19. Seek to cylinder 10, head 3.

20. If required, adjust the delay.

21. Power down, and remove all external connections.

22. Mount a "scratch" cartridge, and allow a minimum 5 minute warm up.

23. Initialize both the removable and fixed platters with DISCINT.

24. Exercise the unit with test DISC6.

25. Load the customer's fixed data on to the fixed platter using FILFIL.

**REMARKS**

a. Enter the following on the console: (IPL) 0700000000 (CR)


   b. If unable to reach acceptable delay, adjust the sector pickup in the removable section, as outlined in the following pages.
The first four steps of this adjustment must be made if the delay adjustment (step 20 of the Removable Disc Head Adjustment) can not be brought to within specifications.

1. Cycle the disc drive down and dismount the pack.

2. Loosen the sector transducer screws (see Figure on pg.2-7) Position the transducer to a point that allows R3 on the Interface card, A5, to be adjusted within specifications, when the pack is mounted and the disc drive is cycled up.

3. Re-tighten the sector transducer screws.

4. Check the adjustment again.

5. Set the oscilloscope to check transducer gain.

6. Check display for 1.2 v (±0.1) peak-to-peak.

7. Re-check index-to-burst delay, by repeating steps 15 thru 20 of Removable Disc Head Adjustment.

**Remarks**

- **a.** If the display resembles below, move the transducer to the left.

- **b.** If the display resembles below, move transducer to the right.

---

**PROCEDURE**

<table>
<thead>
<tr>
<th>The first four steps of this adjustment must be made if the delay adjustment (step 20 of the Removable Disc Head Adjustment) can not be brought to within specifications.</th>
</tr>
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<tr>
<td>1. Cycle the disc drive down and dismount the pack.</td>
</tr>
<tr>
<td>2. Loosen the sector transducer screws (see Figure on pg.2-7) Position the transducer to a point that allows R3 on the Interface card, A5, to be adjusted within specifications, when the pack is mounted and the disc drive is cycled up.</td>
</tr>
<tr>
<td>3. Re-tighten the sector transducer screws.</td>
</tr>
<tr>
<td>4. Check the adjustment again.</td>
</tr>
<tr>
<td>5. Set the oscilloscope to check, transducer gain.</td>
</tr>
<tr>
<td>6. Check display for 1.2 v (±0.1) peak-to-peak.</td>
</tr>
<tr>
<td>7. Re-check index-to-burst delay, by repeating steps 15 thru 20 of Removable Disc Head Adjustment.</td>
</tr>
</tbody>
</table>

**REMARKS**

- **a.** Connect CHAN 1 to pin 5 of pc card A5.
- **b.** VERT GAIN - 20mv/cm.
- **c.** Time base - minimum.
- **d.** Mode - CH 1.

- **a.** To adjust, loosen Allen lock screw.
- **b.** Increase amplitude by moving toward spindle; decrease by moving away.
IOMEC

ACCESS TIME ADJUSTMENT

• Connect the Channel 1 oscilloscope probe to TP4 (POS DR) on card A4.
• Connect the channel 2 probe to pin E (ACCESS RDY) on card A5.
• Connect the EXT SYNC lead to pin P (SET CYLINDER) on the A5 card.
• Set up the oscilloscope as follows:
  CH 1 2v/cm
  CH 2 2v/cm
  HORIZ 10ms/cm
  SYNC = EXT (-)
  Mode = Chop
• Power up the system, including the disc drive.
• Enter the following instruction:
  IPL 000980000f800000a71d3030303030ld3032363830 (TR)
  (If the drive is 100 tr/in modify the last 12 digits to read as follows) 1d3031333430 (TR)

  The disc will seek continuously between track zero (sector 0) and track 134 (sector 2680). If the
drive is a 100 tr/in, it will seek between 0 and 67 (sector 1340).

  The average access time should be 55ms, or 38ms for units with the high speed actuator. If the access
time is not acceptable, the adjustment below must be made.
  If the access time is acceptable, the following adjustment need not be made.
• Move the SYNC lead to pin 4 (LINEAR ZONE ENABLE) on the A3 card. Set the HORIZ time to 5ms/cm.
• Adjust R77 on card A3 to get a waveform similar to those shown below.
IOMEC
OPTICAL TRANSDUCER/SHUTTER ALIGNMENT

- Disconnect leads E 13 and E 14 from the power supply (see the figure below.

- Connect the CH 1 oscilloscope probe to TP6 on the A4 card.

- Connect the EXT (HORIZ) INPUT from the oscilloscope to TP7 on the A4 card.

- Set the oscilloscope as follows:
  
  Set ground trace to top graticle of the scope.
  Input Selector = dc
  CH 1 = .5v/cm
  Horiz. = .5v/cm
  Time Base = EXT
  Coupling = dc
  Slope = +
  Mode = AUTO

- Move the carriage back and forth between the crash stop and the point just short of the head load position. A lissajous pattern will appear on the scope. The samples below show the relationship of the pattern to position.

  ![Diagram showing the relationship of pattern to position](image.png)

  **GROUND**
  
  -0.5V or less
  **SAFE WAVEFORM**
  Too much space

  -1.5V to -1.9V
  **CORRECT**
  (space is 0.005 inches nominal)

  **GROUND**
  **GROUND**

  **DANGER**
  Too little space

2-16
IOMEC

- Adjust for the correct pattern by loosening the front screw (see illustration below) and moving the shutter slightly.

- Position the carriage to cylinder 100, then move the carriage between cylinders 100 and 200. Check the lissajous pattern to be sure that the shutter is not too close. If it is too close, loosen the rear adjusting screw on the shutter and move it away from the actuator motor.

- Position the carriage at cylinder 300, and move between cylinders 300 and 400.

- Adjust the rear of the shutter until the correct pattern appears.

- Move the carriage between cylinders 000 and 400 to verify that the lissajous pattern is correct through the entire travel. Adjust the shutter slightly if necessary until the pattern is correct throughout the full travel.

NOTE: It is possible to obtain a pattern that is similar to the correct pattern, but is the wrong amplitude. Ignore these positions, and adjust to the right amplitude and shape.

- Connect the CH 1 oscilloscope probe to TP1 on A4 (HOME POSITION).

- Adjust the oscilloscope to detect zero and +5v levels.

- Move the carriage against the crash stop. The HOME signal should go low approximately .020" to .040" before the carriage reaches the stop (each division of the scale is .020").

If necessary, adjust by loosening the transducer mounting screw slightly, and sliding the transducer.

- Connect the CH 1 probe to pin E (TRACK 000) of A4, and set the sensitivity to 5v/cm.

- Connect the CH 2 probe to TP2 (ARRIVAL) on A4, and set the sensitivity to 2v/cm. Trigger from channel 1, slope minus.
Manually move the carriage toward the spindle, observing the waveform on the oscilloscope as the carriage moves past track 000. The correct waveform is shown below.

**TRACK 000**

**ARRIVAL**

**CORRECT WAVEFORM**

If the waveform is not correct, loosen the transducer mounting screw slightly so that it can be tilted. If the waveform resembles figure A below, lift the rear of the block; if it resembles B, lift the front of the block.

**TRACK 000**

**ARRIVAL**

A  B

Connect the CH 1 probe to TP6 on A4, and CH 2 to TP7; set the oscilloscope as follows:

- Set the ground traces of CH 1 and CH2 to match.
- Sensitivity (both channels) = .5v/cm
- Selector = dc
- Mode = AUTO
- Slope = (+)

Select CH 1, and move the carriage between cylinders 000 and 400. The waveform should be as shown below, with an amplitude between 1.5 and 2.0 volts. If the pattern is not correct, reset the lissajous pattern as given on the previous pages.

**GROUND**

1.5 to 2.0 volts peak to peak

Select CH 2, moving the carriage the same as before. The pattern should be as shown above, with an amplitude of 1.5v to 2.0v.

Select both CH 1 and CH 2 (CHOP) moving the carriage as above. The scope pattern should look similar to that to the right. If the difference between the peak amplitudes is greater than 200mv, the lissajous pattern must be adjusted as was described earlier.
IOMEC

• Connect the channel 1 probe to pin S on A4.

• Move the carriage slowly back and forth through cylinder 000. Verify that the voltage at pin S goes to -2.0 volts as the carriage is moved through cylinder 000.

• Check that the transducer block mounting screw is tight (8-inch/lbs).

• Check head alignment with a C.E. pack.
The following paragraphs are a discussion and instructions concerning the Western Dynex top-load disc unit as configured to the Qantel system. The marketing model number remains the same as for the disc that it replaces: 3001 for the 3+3, and 3101 for the 6+6.

Operation

The operating sequence is interlocked electronically to prevent damage to the unit and disc pack. The following is a reproduction of the general operating instructions that are included with each unit. Failure to follow these instructions can cause an interlock dropout that may appear as a disc failure.

DISC OPERATING INSTRUCTIONS

START/LOAD

- Depress the top portion of the DISC POWER switch. This is a rocker type switch, and is illuminated when it is in the ON position.
- Wait approximately ten seconds for the SAFE indicator to light.
- Insert the disc pack onto the spindle, and place the protective cover from the pack on top of it. The pack protective cover must be turned bottom side up.
- Depress the top portion of the DISC DRIVE switch. The switch should light, and the motor and fan should start.
- Wait until the READY indicator is lit before attempting to read or write. The delay is approximately 90 seconds. During the delay, the speed will increase, then drop to the running level for a time before READY comes ON.

UNLOAD/STOP

- Wait until the disc is off line, i.e. not reading or writing.
- Depress the lower portion of the DISC DRIVE switch. The lamp inside the switch will go OFF and the rotation speed will begin to drop.
- Wait approximately 90 seconds. The SAFE indicator will light, and the mechanical interlock on the protective cover (on top of the disc pack) will release.
- Change or remove the disc pack, returning the pack to its protective cover. A disc pack should remain in the drive at all times when the system is powered down as a protection against dust on the internal working surfaces of the drive.
- To resume operation, START as listed above.
- If the system is to be powered down, depress the lower portion of the DISC POWER switch. The lamp inside the switch will go OFF, and the fan will stop.

NOTE: If an attempt is made to start the drive without the pack protective cover in place, allow disc to stop, turn power OFF, then restart.

2-21
GENERAL DESCRIPTION

The Western Dynex drive uses a linear motor (voice coil) to position the head carriage. Physical head carriage position is monitored by a transducer that consists of a glass reticule that is marked with the exact track spacing, and a tungsten filament bulb/photo diode arrangement to read the position marks.

Velocity measurement for the head carriage is accomplished by a coil plunger combination that essentially generates voltage with movement. The faster it is moved, the higher voltage is generated. Both position (relative to destination) and velocity are fed to the servo loop for motion control.

The drive, as configured for Qantel, has a spindle speed of 2400 rpm. It uses a 20 sector disc that is electrically counted to ten sectors of data. Conversion to a Western dynex drive is a field change, but requires heavier slide rails, and in some main frames, extensions to the framing itself. Installation is discussed in a later section.

ADJUSTMENTS

Unlike some units, the Western Dynex has demonstrated through experience that once properly adjusted, the drive seldom if ever needs readjusting. The obvious exception to this is when a part needs replacing or repair. All drives are thoroughly checked and adjusted at the factory, and are shipped in top working condition. Installation failures are not likely to be adjustments; the usual installation failure is lack of operating knowledge (causing the interlock to drop out) or a printed circuit board that has become unseated in shipment.

If adjustments are to be made to the drive, follow the instructions exactly, checking the values first, then adjusting only if necessary. Some adjustments affect others, and must be followed by checks in other areas. These will be pointed out in the text where they are needed. Adjust the IOU-24 vco as given on page 2-6.

+5 Volt Check

Before any adjustments are made to other areas, check the plus five volts. The collector (case) of the regulator, Q1, can be used as a test point for the +5v. If necessary, adjust as shown in the illustration to +5.05 Vdc.
The sector/index transducers are both coil type devices incased in plastic. These seldom if ever need adjustment unless they are physically disturbed by part replacement. The initial mechanical settings are .002" for the fixed disc, and .027" for the removable disc. Final adjustment is made by voltage measurement as outlined below.

- Place a disc pack or dummy sector ring onto the spindle of the drive.
- Connect an oscilloscope probe to TP 1 on board A1 (see the illustration below).

• With the power off, remove connector J2 from the motherboard. This disables the linear motor power.
- Jumper pin 15 on A3 to ground (see the illustration for pin sequence). This disables the emergency retract circuits. Pin 9 of A2 is the same electrical point, and can be used if it is easier to reach with the jumper.

* Note: It may be easier to jumper TP3 to TP4 on A2 (control card).
WESTERN DYNEX

- Insert spacers between each set of heads to prevent them from loading onto the disc surface. Be careful during all of the following adjustments and checks to avoid touching the disc with the spacers.

- Power up the drive.

- Enable the spindle motor by pushing the positioner forward about \( \frac{1}{4} \) inch (no more). Do not push heads off the loading ramps.

- Observe the pattern on the oscilloscope. The voltage should be \( 1 \text{VP-P} \pm 350 \text{mv peak-to-peak} \). The signal polarity must be positive going first, if not, reverse the transducer connector (on the removeable disc).

- Change the oscilloscope probe to TP 2, A1. The signal must be positive going first, and 1.5 to 3.8 volts peak-to-peak. If the signal is negative going first, reverse the transducer leads on the fixed disc.

- If both of the above checks are within limits, retract the positioner, and power down the unit. Replace J2.

- If one or both of the voltages are not within specifications, continue with the steps below.

- Retract the positioner and allow the spindle to come to a stop.

- Loosen the set screw on the transducer that is to be adjusted, and move inward for more voltage, and outward for less voltage.

- Check the voltage by advancing the positioner slightly to activate the spindle motor.

- When the proper setting is achieved, gently tighten the setscrew. Be careful, the transducer case is plastic, and can be broken by overtightening the setscrew. Power down the unit, and replace J2.

SERVO ADJUSTMENTS

The servo adjustments must be checked before head alignment is attempted. If a C.E. check is made, and the heads appear to be misaligned, adjust the servo first, then check the heads again. The servo circuits are analog, and can drift slightly as they age etc. The heads are a mechanical adjustment, and on this drive, they have little tendency to become misadjusted. The servo checks and adjustments are as follows:

- Disable the linear motor by disconnecting J2 from the motherboard.

- Disable the emergency retract by placing a jumper wire from pin 15, A3 or pin 9, A2 to ground.

2-24 Revised 5/82
Index Balance Check/Adjustment

- Connect ch 1 probe to TP 5 on board A3 (see illustration).
- Connect ch 2 to TP 6 (A3).
- Set oscilloscope as follows:
  - CH 1: 5v/division
  - CH 2: 5v/division
  - Horiz: 1ms/cps
  - Auto sync
  - Chopped
- Power up the drive.
- Manually move the carriage about the index area (the area that would be track -1.5).
- The output of channel 1 (INDEX) should have at least five volts change from the high point to the low point. The 5v includes any crosstalk on the signal and must be centered about ground.
- Adjust R30 if necessary to make the signal equal above and below ground. Clockwise adjusts the offset positive, and counterclockwise adjusts negative.
- Verify that the signal on channel 2 (the digital result of the same signal) has an output that transitions from zero to +5v, and changes only once for each direction of movement at track -1.5. The read/write heads must be past the plastic loading ramp when the high to low transition occurs. If not, adjust the position lamp/head as described in a later paragraph.

X + 0° Amplitude and Balance

- Connect the channel 1 oscilloscope probe to TP 1 on A3.
- Connect the channel 2 probe to TP 2 (on A3). Refer to the illustration above for test points and adjustment locations.
- Manually move the carriage while observing the signal on the oscilloscope. The signal should be 9v (+ 5v) centered about ground. Adjust R37 (on A3) for amplitude if the 9v peak-to-peak is not within specifications.
- Adjust R21 (on A3) until the sinewave is within .25v of being symmetrical about ground. Clockwise adjustment offsets the signal positive, counterclockwise offsets the signal negative.
- Observe the signal from TP 2 (ch2). It should be a digital (0 to 5v) signal that approximately corresponds to the crossover of the analog signal from TP 1 (ch 1).
X + 90° Amplitude and Balance

- Connect the channel 1 oscilloscope probe to TP 3 on A3.
- Connect the channel 2 probe to TP 4 on the same board.
- Manually move the carriage back and forth.
- Observe the signal on the oscilloscope. The signal from TP 3 (ch 1) should be from 5v to 25v peak-to-peak, and symmetrical about ground.
- If necessary, adjust R 160 (on A3) until the signal is symmetrical about ground, and within 30% of the peak-to-peak amplitude.
- The signal from TP 4 (ch 2) should be a digital signal (0 to 5v) that approximately corresponds to the crossover of the analog signal.

X + 0° and X + 90° Phasing

This adjustment is only necessary when the photodiode assembly (the position read head) has been physically moved or replaced. The diode head and lamp replacement is described in a later paragraph.

- Connect the channel 1 oscilloscope probe to TP 3 on A3.
- Connect the channel 2 probe to TP 2 (on A3).
- Set the oscilloscope as follows:
  CH 1 0.2v/cm (10x probe)
  CH 2 1.0v/cm (10x probe)
  Horiz 0.2ms/cm
  Sync on CH 1 pos
  Add (CH 1 + CH 2)
- Slowly move the carriage in the direction of the spindle.
- Observe the oscilloscope pattern. The digital signal (added to the analog signal) should occur within ±15° of the positive peak of the analog signal.
- When the carriage is moved in the direction away from the spindle, the digital signal should occur within ±15° of the negative peak of the analog signal.

2-26 Revised 3/77
Connect an oscilloscope probe to TP 11 on A3.

- Set the scope to INTernal trigger, negative.
- Manually move the carriage toward the spindle. The scope should display a series of negative going pulses when the carriage is moved toward the spindle.
- Move the oscilloscope probe to TP 12 (just below TP 11).
- Manually move the carriage back and forth. A string of pulses should be generated as the carriage is moving away from the spindle, and no pulses as it is moved toward the spindle.

**Velocity Reference Adjustment**

- Manually retract the carriage, if it is not already retracted.
- Remove the head spacers that were placed there for the previous tests and adjustments.
- With the power removed, reconnect J2 to the motherboard. The location of J2 can be seen in the illustration at the beginning of this section.
- Install a jumper from pin 15, board A3, to ground, or from pin 9, board A2 to ground, or from TP 3 on the Control board to ground.
WESTERN DYNEX

Velocity Reference Adjustment (Continued)

• Connect the I/O cable to either I/O connector, A4 or A5.

• Connect the channel 1 oscilloscope probe to pin 1, A2 (on the motherboard). The motherboard connections as seen from the rear, are odd numbers to the right, even numbers to the left (see below).

• Connect the channel 2 probe to TP 7 on the A3 board (see the illustration on the previous page for TP 3 location).

• Set the oscilloscope as follows:
  CH 1 2v/division
  CH 2 .5/division
  Horiz 10ms/cm

• Mount an Acceptance Test Pack on the drive. Or alternately, mount a scratch pack on the drive if the ATPs are to be read from a tape.

• Start the disc drive, and allow it to become ready.

• Initialize the DISC 6 test to the point where the program says: Start test disc x or man section disc x? When using an ATP pack, it is not necessary (during this test) to replace it with a scratch pack.

• On the IPL device (device zero), enter (IPL) 1000A7 (CR). This is an unconditional branch to location 1000 (hex) in memory. At this address in memory the DISC 6 program has placed the Fast Realization of Disc Operation (FRODO) program. The program will respond with "INSTRUCTIONS" and the ENTER lamp will light on the IPL device.

• Enter the following:
  ES1 ES2 BL SK1 CS$4 SK2 CS$4 EL (Depress FLAG 2, then CR)

Note: spaces can be used between instructions (as shown above), or they can be omitted as desired.

2-28 Revised 5/82
Reference Adjustment (Continued)

The entries that were made (on the previous page) translate as follows:

- **ES1** = Enter seek parameter 1
- **ES2** = Enter seek parameter 2
- **BL** = Begin loop
- **SK1** = Seek to parameter 1
- **CS04** = Check for status 04 (04 is end, allowing the seek to complete before starting the next seek)
- **SK2** = Seek to parameter 2
- **CS04** = Wait for end again
- **EL** = End loop (All instructions between BL and EL will be repeated)

Depressing Flag 2 before transmit causes the FRODO instructions to loop repeatedly until halted by the operator.

Enter the two seek parameters:

- R00000 (CR)
- R02560 (CR)

The disc should seek continuously between track 0 and track 128.

Observe the oscilloscope (channel 1), and record the seek time (see the illustration below).

![Oscilloscope illustration]

IPL, and enter the following:

1000A7 (CR)

The program branches back to FRODO, and prints out "INSTRUCTIONS".

Enter:

- ES1 ES2 BL SK1 CS04 SK2 CS04 EL (FLAG 2) (CR)
- R02560 (CR)
- R05120 (CR)

This is the same program as before, but with new seek parameters (track 128 to track 256).

Record the seek time as shown above.

IPL, and enter the same FRODO program as above, but with the following seek parameters:

- R05120 (CR) ! Track 256
- R07680 (CR) ! Track 384
Velocity Reference Adjustment (Continued)

• Record the seek time as before.

• Determine which of the seek times is the shortest time. The shortest seek time should be 34 ms or slightly longer. If it is not, adjust as follows:
  • Perform the seek that had the shortest time.
  • Adjust R63 on board A3 until the seek time is 34 ms. Clockwise is slower, counterclockwise is faster.
  • Recheck the other two seek times to verify that the adjustment has not made one of them too short.
  • IPL, and branch to FRODO again (100A7)

• Enter:
  ESI ES2 BL SK1 CSG4 SK2 CSG4 EL (FLAG 2) (CR)
  R63 (CR)
  R63 (CR)

• Verify that there is no overshoot on the full seek (see the illustration below). Look for this pattern on CH 2.

  Overshoot

• If there is overshoot, adjust R63 (on A3) clockwise until the overshoot disappears.

• Move the channel 2 oscilloscope probe to TP 5 on A3.

• Verify that the INDEX signal does not become more positive than minus 1.5v. If it does, replace the servo board, A3.

• Remove the jumper from pin 15, A3, and restore the drive to normal operating condition.

POSITION READ HEAD/LAMP ADJUST CHECK-- DON'T ADJUST

This adjustment is only necessary if the position read transducer head or lamp has been physically moved or replaced. When replacing these elements, the lamp filament should be as nearly vertical as possible. The photodiode assembly should also be vertical, with the large dark segment to the bottom (see below).

• Adjust as follows:
WESTERN DYNEX

POSITION READ HEAD/LAMP ADJUST (Continued)

- Disable the linear motor by disconnecting J2 from the motherboard. The location of J2 is shown in the illustration at the beginning of this section.

- Disable the emergency retract by placing a jumper from A3, pin 15 to ground, or from A2, pin 9 to ground.

- Insert spacers between each set of heads to prevent them from loading onto the disc surface. Be careful during the following tests and adjustments that the spacers do not touch the disc when the head carriage is moved manually.

- Connect an oscilloscope probe to TP 3 on board A3. The location of A3 test points is shown on a previous page.

Lamp Adjustment

- Loosen the lamp setscrew so the lamp can be rotated.

- Manually move the carriage back and forth, observing the voltage pattern on the oscilloscope.

- Rotate the lamp slightly while still moving the carriage. Find the point where the maximum deflection is made. This must be in excess of 5v peak-to-peak (2.5v each way from ground).

- Re-tighten the setscrew, and make the following checks:
  - $X + 0^\circ$ Amplitude and Balance
  - $X + 90^\circ$ Amplitude and Balance
  - $X + 0^\circ$ and $X + 90^\circ$ Phasing
  - Up Clock Down Clock

- If any of the above tests are not within specification, re-position the lamp to another maximum position and repeat the procedure.

Transducer (Diode) Head Adjustment

- Loosen the head setscrew so the head can be rotated.

- While moving the carriage, slightly rotate the head for maximum deflection (the same as in the lamp check above).

- Re-tighten the setscrew, and make all of the checks that are listed in the lamp adjustment above. In difficult cases, the head and lamp may require adjusting in conjunction.

2-31
**Index to Clock Check**

- Check or adjust the velocity reference as given previously.

- Connect the channel 1 oscilloscope probe to TP 6 on A3.

- Connect the channel 2 probe to A3, pin 2.

- Set the oscilloscope to trigger on Channel 1 positive.

- If the DISC 6 test is still in memory, (i.e. the processor has not been powered down since the velocity reference was made) branch to FRODO by entering (IPL) 1000A7.

- If DISC 6 is not in memory, load it from tape or disc, and initialize the test to where it prints "LOAD SCRATCH PACK". If the DISC 6 program was loaded from a disc pack, the pack need not be removed for this test. Instead, IPL, and enter 1000A7 (GR).

- When the FRODO program responds with "INSTRUCTIONS" enter:
  
  BL RES CS@4 EL

- Observe the oscilloscope pattern. The clock signal (ch 2) must be more than 500 us from the negative (falling) edge of the INDEX signal on TP 6 (ch 1). If it is less than 500 us, adjust R 30 on A3 (400 to 500 is O.K.)

- If R 30 is adjusted, repeat the INDEX BALANCE check (the first servo adjustment) to verify that the index balance is still between +2v and -3v. If the index to clock cannot be adjusted without moving the index balance out of limits, the Position Read Head must be rotated slightly (see Position Read Head/Lamp Adjustment).

**X + 0° Cell Balance Check**

- Check or adjust all other servo settings.

- Connect an oscilloscope probe to TP 1 on board A3.

- With the carriage retracted, the voltage level on TP 1 should be between + 2.5v and - 2.5v. If it is not, the position read lamp must be adjusted (see Position Read Head/Lamp Adjustment).
WESTERN DYNEX

READ/WRITE HEAD ALIGNMENT

NOTE: a jumper is required on the IOU 24C for head alignment.

To ensure that data that is put onto a removable pack by one disc drive is readable by any other drive of the same type requires that the read/write heads are positioned accurately. The fixed disc heads, of course, do not require positional alignment because the platters are not exchanged between units. Whenever a fixed disc or one of the fixed disc heads is changed, initialization is all that is required.

Heads 0 and 1 (top, or removable, platter) must be checked or aligned whenever any mechanical change occurs to the heads, the head carriage, or the position read mechanism. However, before head alignment is started, all servo checks and adjustments must be made.

Head alignment is very sensitive to temperature and environment. The entire disc drive must be stabilized at room temperature (72°F to 82°F) for at least three hours before the alignment is started. This allows all portions of the drive to reach expansion equilibrium.

It is also very important that during the head alignment procedure, the front panel must not support any weight. The unit should be supported by the side rails only.

The procedure below covers situations where unknown problems may exist in the drive, and could damage a C.E. pack. Checks are made to verify that the drive is working well enough to install the C.E. pack. If a drive is known to be working, and only needs checking or alignment, the steps that are preceded by an asterisk (*) may be skipped. However, if the drive is at all questionable, the small investment in extra checks will be rewarded by assured protection of the C.E. pack.

• Remove the outer top cover from the drive.
• Remove the smaller plastic cover from over the head carriage region.
• (*) Mount a spare (scratch) pack on the drive.
• (*) Place both protect switches in the PROTECT position.
• (*) Start the disc spinning and wait for 30 minutes for the pack to stabilize.
• (*) Measure the voltage across R22 on A4 (see the illustration). If there is any voltage across R22, the erase circuit is ON, and the C.E. pack must not be mounted until this condition is corrected.
• (*) If there is no voltage across R22, power down the drive, and remove the spare pack.
Mount a C.E. pack on the disc drive, and power up the unit. See note with the illustration, below.

Allow the C.E. pack to spin for at least 30 minutes before continuing. If the C.E. pack was carried from a different temperature environment, allow one hour. Leave the two plastic covers off during this time.

Place a short jumper wire across the two points of jumper position I on board A3 (servo). These two points are labeled C and 2 (see the illustration). The jumper point 2 is not test point 2 that was shown in the earlier illustrations. The jumper introduces an offset in the

NOTE: If an unsectoring C.E. pack is used, emergency unload must be disabled by jumpering A2, pin 9 to ground, or A3, pin 15 to ground.

If the IOU-24C is at revision II or higher, ground 4D13.

Connect the channel 1 oscilloscope probe to test point 12 on board A1 (see the illustration above).

Set the oscilloscope to measure:

CH 1 2v/division AC coupled
CH 2 .5v/cm
HORIZ 2ms/division uncal.
Sync INT (CH 1) POS

2-34 Revised 5/82
Connect the channel 2 oscilloscope probe to TP 3 or 4 on board A4 (see the illustration below).

NOTE:
TP3 resides higher on late model P.C. boards.

Seek to the C.E. track and select the lower head by entering the following program. (IPL)

0006800000A8lx3032393230

Device number, usually D

Observe the pattern on the oscilloscope. Adjust the time base on the oscilloscope to center the "cat's eyes" on the screen. Both loops must be equal when the head is properly aligned. The illustrations below show examples of the patterns that can be expected.

If head adjustment is necessary, first disable the emergency retract by jumpering either A2, pin 9 to ground, or jumpering A3, pin 15 to ground.
• When the lower head is properly aligned, switch to the upper head by entering the following program.

   (IPL) 0006800000A81x3032393330

   Device no., usually D

• The pattern should be the same as it was for the lower head. Be sure the emergency retract is disabled before attempting head adjustment.

• When both heads are properly aligned, the index to burst (circumferential alignment) must be checked as follows.

• Move the oscilloscope probe to TP 3 on board A4 (the location of TP 3 is near TP 1, and is shown on the previous page).

• Set the oscilloscope as follows:

   Trigger INT (CH 1) POS
   CH 2  20mv/cm (with 10x probe)
   HORIZ  5us/cm

• Enter the following program:

   (IPL) 0006800000A81x3030323030

   Device number, usually D

• This program seeks to the burst track and selects the lower head.

• The oscilloscope pattern should be similar to the illustration below. The delay should be 19us ± 4us. Make a notation of the delay time. For single sector packs

   Time= 19us + 19 (\(\frac{x}{25ms}\) -1) where x= measured rotation time.

   ![Oscilloscope Pattern Illustration](image)

   **NOTE:** Burst may appear as reversed polarity.

• Select the upper head by entering the following program.

   (IPL) 0006800000A81x3030323130

   Device number, usually D
Compare the delay of the upper head to that of the lower head (from the previous seek). If the difference is greater than 7us, one or both heads are improperly seated against the side guide block. In that case, the mechanical correction must be made, and the entire alignment check repeated.

If both heads have a delay that is within 7us of each other, but the delay is less than or greater than 19us (+4us), adjust R 84 on board A1 (see the illustration below for the location of R 84). If the range of R 84 is insufficient, do the following:

- Power down the disc, and remove the pack.
- Loosen, but do not remove, the five Allen-head screws that hold the fixed disc cover. Do not loosen the three round head Allen screws that are in the same vicinity.
- Rotate the fixed disc cover (within the allowance of the screw holes) and re-tighten the screws. Clockwise increases the time; counterclockwise decreases the time.
- Re-insert the disc pack and power up and ready the drive.
- Seek to the burst track, as given above, and try again to adjust R 84.

When the above checks and adjustments are correct, return the disc drive to normal operating status by removing any disabling jumpers, and replacing the covers.
FRODO

FRODO (Fast Realization of Disc Operations) is loaded into memory, and can be used by the operator, any time after START TEST DISC X or MAN SECTION DISC X ? is printed.

FRODO is initialized after an error halt in DISC6 by turning ON Flags 2 and 3 and pressing the ST-SP button. The operator can also enter FRODO by branching to location 1000 in memory at any time.

The program will print "INSTRUCTIONS:" and the ENTER lamp will light. The user program consists of a series of two or three character symbolic codes. Each code causes a specific function. Some codes require following digits as noted in the code description. All codes may be (but need not be) separated by spaces.

Program entry is terminated by turning ON Flag 2 and/or Flag 3 and pressing TERM or TRANSMIT. Flag 2, alone, causes the program to be executed repeatedly at machine speed until stopped by the user. Flag 3, alone, causes the program to halt after each instruction is executed, and repeat until stopped by the user. Flag 2 and Flag 3 cause the program to execute only once at machine speed.

If a code that is entered cannot be recognized, the program will print "illegal entry" and return to the program entry phase.

When an ES1 or ES2 are used in the instructions, the program halts after termination of the instruction entry phase to await the seek parameters. The operator must then enter the seek address (see below).

The symbolic codes for FRODO are explained below.

<table>
<thead>
<tr>
<th>CODE</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>ES1</td>
<td>Enter Seek parameter #1. Causes the program to wait in a loop, allowing the operator to enter a seek address that will be used by the SK1 instruction. The address is a 5-digit decimal number that must be preceded by &quot;F&quot; or &quot;R&quot; (fixed or removable) if the disc is a 3+3 or 6+6.</td>
</tr>
<tr>
<td>ES2</td>
<td>Enter Seek parameter #2. Same as ES1, except that the number is used by the SK2 instruction.</td>
</tr>
<tr>
<td>SK1</td>
<td>Seek 1. A seek using the parameter given above. If no parameter was given, the parameter is read from the first six positions of the write buffer (where it would have been put). These positions will be hex zeros that were set when the program was read into memory. If one seek follows another, use a CS#4 instruction between the two, to allow the seek to complete before the new seek is attempted.</td>
</tr>
</tbody>
</table>
FRODO

SK2 Seek 2. Same as for SK1, except that parameter #2 is used. When FRODO is loaded, default values for parameter #2 are set to 11111 for 111 discs, 21111 for 114 discs, or 01111 for 5444 discs.

ED Enter Data. Hex data is read from the terminal. If only one or two bytes (two or four hex digits) are entered, these characters will be propagated through the entire write buffer. Otherwise, up to one typewriter line of data may be placed in the first 64 positions of the write buffer following the seek parameter.

WRT Write to the disc. Data in the write buffer is written to the disc. The write routine does not include seek or status check.

RD Read from the disc. The first sixteen bytes of the read buffer are cleared to hex zeros, then data from the disc is entered into those positions. The read routine does not include seek, status check, or data checks.

CD Check Data. The contents of the read buffer are compared against the contents of the write buffer. Unless the SER instruction has been executed, errors are printed if they occur.

FWB Print Write Buffer. The contents of the write buffer are displayed. Flag 2 terminates the current line; Flag 3 terminates the operation.

FRB Print Read Buffer. The read buffer is displayed.

ACS Add Constant to Sector. The sector number in parameter #1 is increased by one. When it overflows from 9 to 0, the head number is increased by one. When the highest allowed seek is exceeded, the program skips to the next symbolic code beyond EL, thus terminating the loop.

ACH Add Constant to Head. The head number in seek parameter #1 is increased by one. Overflow increases the cylinder number by one. When the highest allowable parameter is exceeded, seek parameter is reset to zero, and the program skips to the next instruction beyond EL, terminating the loop.

ACT Add Constant to Track. The cylinder number in seek parameter #1 is increased by one. When the highest allowable parameter is exceeded, seek parameter #1 is reset to zero, and the program skips to the next instruction beyond EL, terminating the loop.

MRW Move Read Buffer to Write Buffer
FRODO

PCS Print Changing Status. Every 425 microseconds a Status-in instruction is issued to the disc. The status obtained is stored in a stack. If the status changes within 250 milliseconds, the new status is entered in the stack. When status remains constant for more than 250 milliseconds, all of the status bytes obtained are displayed.

PS Print Status. Status is printed after each completed operation.

CSxx Check for Status xx. When the status (represented by xx) is reached, the next instruction is executed. If the specified status is not reached within 250 milliseconds, the sector number (in bytes 2-6 of the write buffer) is printed along with the last status byte obtained. Errors are not printed if the SER instruction was executed.

SOF Skip On Failure. Placed after a CD or CSxx instruction. If a failure occurs, the program skips to the next instruction beyond EL, or if there are no loops, to the beginning of the program.

SER Suppress Error Report. Instead of displaying errors, signal lamps one and two blink each time an error is made. Error reporting is automatically restored during program entry.

SR Soft Rom. All seeks issued after this instruction is executed are software programmed, and execute in the micro instruction mode. SR is disabled during program entry.

ITC Inhibit Track Check. Issues CTL 01 to the controller, causing it to disable (the normal) track check that would be made.

IRC Inhibit Read Check. Issues CTL 02 to the controller inhibiting the read-after-write check.

RIO Reset I/O.

BL,EL Begin Loop, End Loop. Instructions to be repeated are placed between these symbols. The loop continues until:
1. The operator presses Flag 2 or 3, causing the program to fall through EL the next time it is reached.
2. A CSxx followed by SOF does not detect the expected status, or a CD followed by SOF detects a data error.
3. An ACS, ACH, or ACT increment seek parameter #1 past the highest valid sector number.

RES Restore. Sends a series of micro instructions to restore the disc and set the cylinder to 00.
FRODO

SWR  Set Write.
SRD  Set Read.
MWxx Micro Write. The one byte of hex (represented by xx) is transferred from the processor to the controller.
MR  Micro Read. One byte of data is read from the controller, and typed or displayed.
TER  Terminate.
RSI  Read Status 1. The status byte from the controller is read and displayed.
CTL  Control. Sends the CTL signal to the controller.
RH  Reset Head register. Resets the head register in the controller.
SCxx Set Cylinder to xxx. The decimal number represented by xxx is converted to hex, and the cylinder is set accordingly. The highest acceptable value for the 114 and 6+6 is 202.
SHxx Set Head to xx. The decimal number represented by xx can be 00-29 for the 111, 00-19 for the 114, or 00 or 01 for the 3+3 or 6+6.
SSCx Set sector to x. The x represents a decimal number of the sector to be set.
SD  Set Difference. Used by the 111 to set the address difference. Not used by the 114. Used for one addressing digit by the 6+6.
SSK  Start Seek.

A practical example of the use of FRODO instructions is given in the Western Dynex portion of this book. In there the instructions are used to allow seek time measurement and test for overshoot. With very little practice, FRODO can be one of your best and easiest tools.

**WHO - Write header only**

2-42
The test points and procedures in the following instructions are for the Model "D", covering all 
machines above serial number 5000. For machines 
below serial number 5000, settings and test points 
differences are shown as follows (*05C04).

Remove the front and rear panels by pulling 
firmly at the top portion of the panel. There are 
two friction catches toward the top. When these 
release, the panel swings out at the top, pivoting 
at the bottom. The panel can then be lifted free 
of the unit.

The card cage/power supply module at the rear 
of the unit swings out for access to motherboard 
test points. The entire module is secured by a 
large captive screw at the lower left of the 
module.

Individual procedures are labeled in the text. 
Spot checks can be made on any of these single 
adjustments. However, if adjustments are made, the 
alignment must be made in the order that is given: 
Power Supply, Servo, and Read/Write System.

POWER SUPPLY

Use the S1 switch inside the lower unit to 
apply power. Do not activate The operator switch 
on top. Using a digital voltmeter, check the 
voltages as shown in the figure and table below. 
The 36 volts must be checked at the motherboard, 
15853 (see the illustration on the next page for 
adjustment location).

<table>
<thead>
<tr>
<th>Voltage Terminals</th>
<th>Ground Terminal</th>
</tr>
</thead>
<tbody>
<tr>
<td>+5V (+50 mvdc, adjustable)</td>
<td>DC COM (T52-3)</td>
</tr>
<tr>
<td>+24V (-11.2 vdc)</td>
<td></td>
</tr>
<tr>
<td>-24V (-10.48 vdc)</td>
<td>adjustable</td>
</tr>
<tr>
<td>-24v SVO (-0.48 vdc)</td>
<td>adjustable</td>
</tr>
<tr>
<td>+45V (+2.25, -4.5 vdc)</td>
<td></td>
</tr>
<tr>
<td>+36 vdc (+1.8 vdc, at 15853)</td>
<td>adjustable</td>
</tr>
</tbody>
</table>

NOTE
On earlier model units, the physical 
location of the dc voltage terminals 
differs from that shown in Figure.
SERVO SYSTEM

100 KHz Oscillator

Connect the Channel 1 oscilloscope probe to 06B01 (card 6, bank B, pin 01). Gnd. lead to chassis.

- Set the oscilloscope as follows:
  
  SYNC = INT POSitive
  HORIZ = 10 usec/cm
  VERT = 1 v/cm dc

- The display should be a 5v p/p + .05v sinewave centered about ground, with a cycle length of 10 Usec + 1 Usec. If it is not within these limits, adjust the Oscillator control, R6 on
CENTURY 114

card 6B (see illustration). The frequency is not adjustable, and if it is beyond limits, the oscillator card (6B) must be replaced.

Cylinder Transducer Check

- Remove power from the drive by turning S1, on the relay panel at the front of the unit to the OFF position.
- Remove card 15A (servo amplifier) from the unit.
- Remove the top cover (over the servo motor).
- Remove ONE wire (only) from the linear motor.
- Cover the end of the wire to avoid accidental contact.

----------WARNING----------
DO NOT REVERSE THE LINEAR MOTOR LEADS; it will destroy the drive and injure nearby persons.

- Connect the oscilloscope CH1 probe to 13A31 (*05C04) (cylinder transducer). This probe stays at this point through the Cylinder Transducer check, the DC Offset adjustment, Servo Balance,
CENTURY 114

Position Gain, and Velocity checks.

• Be sure to ground the shield connection on the oscilloscope lead.

• Connect the CH2 probe to 05B33 (*11A30)(GTDAWN). This probe will not be used until later.

• Connect an external sync lead to 06B01 (Osc).

• Set the oscilloscope as follows:
  - Sync = Ext pos
  - Horiz = 2 ms/cm
  - CH1 = 200 mv/cm

• If it is not already connected, install the I/O cable between the IOU/24 and the drive.

• Return the ac power switch, S1, to the ON position.

• Power up the main processor.

• Power up the drive from the operator panel on top of the unit.

• Wait for the pack to reach speed, and the SELECT LOCK indicator to light.

• Grasping the head carriage toward the rear (near the coil, or bobin), move the heads onto the disc pack.

• While moving the heads back and forth over the entire range, observe the pattern on the oscilloscope. The waveform (shown below) must be $800 + 200\text{mv}$ with less than 20% variation. This movement should be approximately

The waveform illustration is shown on the following page.
between tracks 000 and 202. The end of rack magnets will induce large distortions into the transducer signal. Ignore this effect.

- Manually retract the heads, unloading them.
- Power down the disc unit at the operator panel, and wait for the pack to stop.
- Turn S1, on the relay panel, to the OFF position.
- Re-install the 15A card.
- Re-connect the linear motor lead.

-----------------WARNING---------------------
DO NOT REVERSE the linear motor leads; it will destroy the drive and cause personnel injury to anyone nearby.

-----------------WARNING---------------------

- Return the S1 switch to the ON position.
- Power up the drive from the operator panel.
CENTURY 114

Set the oscilloscope to:
CH 1 = 100 mv/cm
HORIZ = 5 usec/cm

- Load the ATPs from either another disc drive, or from a tape unit.

- Call for the "disc6" test, and initialize to the point where the test asks "manual section disc x?".

- Depress IPL, and enter 1000A7 (TRANSMIT).

- The FRODO portion of the disc6 test will print "instructions".

- Enter:
  ESl ES2 BL SK1 SK2 EL
  .Depress F2, then RETURN

- Enter:
  00000 (RETURN)
  00100 (RETURN)

- The disc will begin a repetitive one cylinder seek.

D.C. Offset

- Observe the pattern on the oscilloscope screen
  The correct waveform is shown below.

- Adjust the DC OFFSET (bottom pot on 13A)(*pot close to pins on 5C) and the SERVO BALANCE (next pot above DC OFFSET)(*top pot on 13A) to make the heavy center line in the display as flat as possible.

- Stop the repetitive seeks by depressing the START/STOP button on the processor.

- With the processor stopped, the heavy line can usually be made a little flatter by slightly adjusting the DC OFFSET and SERVO BALANCE. Do not radically change either control unless the repetitive seek is restarted.
• Start the seek again by depressing the processor START/STOP button.

• Verify that the heavy center line stays flat. If it does not, the adjustments must be repeated.

**Position Gain**

• Move the oscilloscope sync probe to 09A5 (SKFD).

• Reset the oscilloscope as follows; do not move the CH1 and CH2 probes.

\[
\begin{align*}
\text{CH1} &= 100 \text{ mv/cm ac} \\
\text{CH2} &= 2 \text{v/cm dc} \\
\text{Horiz} &= 2 \text{ ms/cm added} \\
\text{Sync} &= \text{Ext. Pos.}
\end{align*}
\]

• Vary the horizontal sweep (uncalibrated) until both forward and reverse waveforms are displayed. The illustration below shows approximately the way it should look.

![Illustration](image)

• Take note of the waveform for comparison to the one that will follow.

• Depress F2 on the controlling terminal, and enter:
  40100 (RETURN)
  40200 (RETURN)

  The disc will start a single cylinder seek between tracks 201 and 202.

• Compare the display to the previous one. They should be nearly identical. If they are not, adjust the POSITION GAIN (2nd pot from top on 13A) (*farthest pot from pins on 5C) slightly to make both waveforms alike. Keep in mind that the GTDATN portion must not exceed 130 mv.

• If adjustment was made, return to the seek between
cylinders 0 and 1 by pressing F2 on the controlling terminal, and entering:
00000 (RETURN)
00100 (RETURN)

- Alternate between the two one track seeks until both waveforms are alike.

Velocity

- Depress F2 on the controlling terminal, and enter:
  00000 (RETURN)
  40200 (RETURN)
  This creates a repetitive 203 track seek.

- Reset the oscilloscope to the following; do not change the probe positions.
  
  CH1 = 200 mV/cm
  CH2 = 2 V/cm ADD
  Horiz = 10 ms/cm Neg. (CAL)

- Adjust the VELOCITY (top pot on 13A)(bottom pot on 13A) if necessary, to make the GTDATN signal start 65 ms + 5 ms from the beginning of the sweep. The display should look like the illustration below. Change trigger if start point does not "swoop" out as shown.

- Depress F2 and release. Depress F2 again, then F3 and RETURN.

READ/WRITE SYSTEM

Sector And Index Pulses

Continue from the tests and adjustments above (scratch pack mounted and spinning)

- Connect the Channel 1 probe to 0B13 (index/sector)

- Set the oscilloscope controls as follows:
  Vert = .5v/cm
  Horiz = 5 ms/cm
CENTURY 114

Sync = Int Neg

• The pulses should be 1.3v peak-to-peak minimum, to 2.25v p/p maximum (similar to the illustration below). Ignore the INDEX pulse.

![Illustration of pulses](image)

• Move the CH1 probe to 03A30 (index)

• Reset the oscilloscope as follows:
  - Horiz = 5 ms/cm
  - Sync = Internal Pos
  - CH1 = 2 v/cm

The wave should be positive pulses approximately 25 ms apart.

![Waveform](image)

Head Amplitude

• Move the Channel 1 probe to 02C13.

• Connect the Channel 2 probe to 02C15.

• Connect the Sync lead to 07A50 (read gate).

• Set the oscilloscope controls as follows:
  - CH1 = 100 mv/cm ac
  - CH2 = 100 mv/cm ac inverted
  - Sync = Ext. neg.
  - Horiz = .1 ms/cm
  - Mode = Add, inverted

• On the controlling terminal the printout will say, "instructions".

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CENTURY 114

-------------WARNING-----------------
Keep hands out of head carriage area.
-------------

• Enter:
  ES1 ED SK1 WRT BL SK1 RD EL
  (F2 RETURN)

• Enter:
  00000 (RETURN)
The computer will respond with "ED:"

• Enter:
  FF (RETURN)
The oscilloscope will display a stream of
one bits alternated with zero bits.

• Note the amplitude (in mv) of the one bits
  (the smaller amplitude).

  zero bits       one bits

• If the amplitude is less than 75 mv on a single
  head, replace the head. However, if several heads
show less than the 75 mv reading, the READ pre-amp
board for that side may be faulty, and require
replacing.

• Depress F2.

• Enter: 00010 (RETURN)
The computer will respond with "ED:"

• Enter: FF (RETURN)
This produces the same pattern as above, but
selects the next head.

• Increment to each head by pressing F2 and
  entering the next higher number, then entering
  FF for data. Head numbers progress from 00000
  to 00090 (head 9), then from 10000 (head 10) to
  10090 (head 19).

2- 52
When all heads have been checked at track zero (the previous procedure) depress F2.

- Enter: 29900 (RETURN)
  ED: FF (RETURN)

This causes the same pattern of ones and zeros at track 199.

The minimum amplitude for the pattern of 1's must not be less than 75mv.

- Increment to each head as before by adding one, to the head number, then entering FF for data. The head sequence is 29900 (head zero, track 199) through 29990 (head 9, track 199), then 39900 (head 10, track 199) to 39990 (head 19, track 199).

-----------------NOTE-------------------
The amplitude of head 19 is usually lower than all of the others, but must be 75mv minimum.

-----------------------------
- When all heads are checked, power down the disc drive depressing the POWER ON button.

HEAD ALIGNMENT

- Remove the power from the disc drive by turning S1 (on the relay panel at the front of the unit) to the OFF position.

- Remove the Write Driver card, 3C, from its slot.

- If the CE pack has only an index slot (no sector slots), card 11B must also be removed.

----------CAUTION----------
If card 11B is removed, only a single slot disc pack can be used. Failure to return the 11B card to its slot before running a sectored disc pack will cause an immediate HEAD CRASH.

-----------------------------
- If the IOU-24C controller board is at level G or higher, ground pin 13 on the oneshot
CENTURY 114

located at D 9 on the IOU-24C board. (These jumper points are shown in the illustration that follows)

- Depress IPL on the main processor.
- Restore power to the drive by returning Sl to the ON position.
- Mount the CE pack in the drive.
- Power up the unit from the operator panel, and start the disc spinning.

----------------NOTE-------------------
The CE pack must be allowed to equalize in temperature before head alignment is made. This can be as long as an hour if the pack has just come from a greatly different environment.

- If the Disc6 test is not already in main memory, load it and initialize to where the printout reads "manual section disc x?".
- Call in the FRODO subroutine by entering (IPL) 1000A7 (TRANSMIT)
- When the program prints "instructions" enter:
  ES1 BL TSP SK1 ACH RIO EL (F3 RETURN)
- Enter:
  07300 (RETURN)

The START/STOP lamp will light.
- Depress the ST/SP button on the processor. The seek parameter will be printed out.
---WARNING-----
Keep hands and tools clear of the head carriage when depressing ST/SP. At various stages in the program, the heads will quickly restore or seek, and can cause serious injury.

- Connect the oscilloscope CH 1 probe to 02C13.
- Connect the CH 2 probe to 02C15.
- Set the oscilloscope as follows:
  CH 1 = 100mv/cm  
  CH 2 = 100MV/CM INV. ADD  
  Horiz = 2mv/cm  
  Sync = Ext Pos (on 03A30, INDEX)
- Noting the warning above, depress ST/SP until the heads seek (to track 73). Head zero will be selected.
- Observe the pattern on the oscilloscope. It will have a pattern that resembles a figure eight, or sometimes called cats eyes. This figure is sometimes difficult to see until the oscilloscope brightness is adjusted to the right level.

Some examples of the "cats eyes" display are shown below. The sweep time on the oscilloscope may require adjustment to center the display on the screen.

Proper head alignment is shown when both loops are equal. The loops need not be the same amplitude, as long as they are the same length. If the loops are less than 100 mv, do

![](image1)

![](image2)

a weak head check for the head that is selected. Do not adjust the head if the loops are within 10% of each other (2 small
To select the next head, depress the ST/SP button four times. This can be done to select each head in order.

If after checking all heads, a particular head must be selected, use the following procedure.

• Depress F2

• Depress ST/SP repeatedly until the ENT lamp on the entry station lights.

• Enter the head number, retaining the track address of 73. For example, head #3 would be 07330; head #15, 17350.

• After the address is entered, press (F3) RETURN, then ST/SP until the seek is made.

• Adjust any heads that may require it. Adjust as follows:

  • Loosen each of the four screws, but retighten the screw slightly to allow the head to be moved with resistance.

  • Move the head beyond the proper adjustment using a screwdriver.

  • With a screwdriver, carefully pry the head back to the proper position.

  • Tighten all four screws to 5.5 inch/pounds (use a torque wrench for this).
When all heads are aligned or checked, depress F2.

Enter:
21890  (F3) (RETURN)

Change the oscilloscope sweep to 1 us/cm.

Observe the pattern, it should look similar to the following illustration. The separation of index to burst must be 3 usec. + 2 usec.

Verify the above by looking at head 10.

(F2) Enter:
31800 (F3) (RETURN)

The pattern should be nearly the same as above.
<table>
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<tr>
<th>BOARD LOC</th>
<th>PART NO.</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
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<td>9</td>
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<td>DR53</td>
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<td>X X X</td>
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</table>
PREVENTIVE MAINTENANCE SCHEDULE

When On Site

Clean Heads X X X X X
Ck. Lamps/Ind., Switches X X X X X

Every 30 Days

Clean Heads X X X X X
Ck. Filters/Air Press. X X X X X
Ck. Index Ring Thru Hole X
Ck. Lamps/Ind., Switches X X X X X
Clean Heads X
Cover Microswitch X

Every 90 Days

Cln. Fixed Disc X X
Ck. 24A/C Adj. * X X X
Ck. SR8’s (See F. A. #50) X X X X
Ck. Head Alignment * X X X X
Ck. Sect/Idx. Amp & Idx/burst* X X X X
Ck. Cyl. Xducer X X X X
Run Diagnostics X X X X
Ck. Pack Standoffs X

Every 180 Days

Clean Entire Unit X X X X X
Ck. Pwr. Supply X X X X X
Ck. Osc. * X X
Ck. Servo * X X X X
Ck. Cln. Lube Carrier/Pos. X X X X
Ck. Absolute Filter X X
Advise Cust. To Have Packs Cld. (DO NOT DO IT YOURSELF) X X X X

* Run diagnostics if any changes or adjustments are made.
MEMOREX

MEMOREX DRIVE ADJUSTMENTS

D.C. VOLTAGES

- Insert a Memorex extender card into the A01 slot. Using a DVM, measure the supply voltages on the extender. They should be:
  
  +15 ± 0.1V on Pins L/R 15
  +.05 ± 0.05V on Pins L/R 25
  -5.2 ± 0.05V on Pins L/R 30
  -15 ± 0.1V on Pins L/R 35

REAR

<p>| | | | | |</p>
<table>
<thead>
<tr>
<th></th>
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<tr>
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<td>B01</td>
<td>INTF A</td>
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</tbody>
</table>

CARD CAGE

- Adjust the voltage pots in the pedestal to bring all voltages to within tolerance.

SERVO OFFSET ADJUSTMENT

- Enter the following macro program: Do not enter spaces unless a Q29 is used.

  IPL
  00020A7 (CR) (0002D)
  1d4090 004980 0028008 0040A8 xd3030303030 (CR) (0004D)
  1d4090 004980 0028008 0028008 00240A8 xd3330343434 0020A8 (CR) (0004D)
  d = Controller Address x = Port Address
  * If on a 75 MB drive the seek parameter should be:
  xd393133343434333434

Everytime the Start/Stop button is pressed the drive should do a seek between tracks 0 and 348.

- Using a DVM measure the voltage between TP11 on the PSER card and ground, with the heads at the track 0 position.
MEMOREX

SERVO OFFSET ADJUSTMENT Continued

• Use the Start/Stop button to seek to track 348 and again measure the voltage at TP11.

• The voltage measurement should change slightly. The difference measured should be centered around zero volts. Use the small one-turn pot on the PAMP board (visible through a hole on the back of the PDU) to center the offset voltage around zero volts. The voltage difference between tracks ø and 348 should not be greater than 150mV.

TACH SLOPE ADJUSTMENT

• Set up a scope as follows: CH1 = 2V / Div
  Sweep Rate = 10 ms / Div
  Trig Source = CH1
  Trig Slope = Neg
  Trig Mode = Normal

• Power the drive down and extend the SCON board. Connect the CH1 scope lead to IC 6C-6 or to pin R46 on the SCON board. Power the drive back up.

• Enter the following macro program to cause the drive to alternate seek between tracks ø and 105:

  IPL
  0020A7 (CR) (00020)
  1d4890 004080 1d4890 006080 0020A7 (CR) (00040)
  xd30303030 (CR) (00060)
  xd3039323438 * (XMIT)
  d = Controller Number  x = Port Number
  * For 75 MB drives enter xd3237373238 on the last line instead.
A scope pattern similar to the one shown below should appear. The two-seek time "t" should be 68 ms for new style servo drives (large bearing cap) or 72 ms for old style servo drives (small bearing cap). Adjust R54 (the largest pot) on the VSER board to bring the seek time to within 45 ms of specification.

\[
t = 68 \text{ ms for New Servo Types}\\
\]
\[
t = 72 \text{ ms for Old Servo Types}\\
\]
MEMOREX INTERFACE CARD

SLOT B01

DRIVE ADDRESS SWITCHES

NOT USED - SET TO OFF
SECTOR PULSES DISABLE - SET TO OFF
WRITE ENABLE SWITCH - SET TO ON
NOT USED - SET TO OFF

*ON = 1
OFF = Ø
Servo Loop Gain Adjustment

- Connect scope lead to TP11 on PSER card (Refer to illustration of PSER card below).

- Set oscilloscope as follows:
  
<table>
<thead>
<tr>
<th>Channel</th>
<th>Voltage/cm</th>
</tr>
</thead>
<tbody>
<tr>
<td>Horiz</td>
<td>2ms/cm</td>
</tr>
<tr>
<td>Sync</td>
<td>R18 on VSER Card</td>
</tr>
</tbody>
</table>

- Using the colt tester execute routine 24 (incremental seek).
  (If Colt Tester is unavailable refer to pg. 2-65b).

- Adjust the servo loop gain potentiometer R103 on PSER (Refer to illustration of PSER below) to minimize servo overshoot without causing the servo system to oscillate. The correct adjustment is illustrated below.

  CAUTION - DO NOT change the setting of the AGC pot on PSER (R63)

  ![Diagram showing overshoot](attachment:image.png)

  Properly adjusted overshoot

- Run Colt Tester routines 27 and 28 to verify operation.
PSER adjustment without a Colt:

Incremental seeks may be obtained through the use of MXATPH or MXQ29 tests using the following procedure.

1) Load MXATPH or MXQ29 Test
2) Specify address. Be sure to place an R after the address. This will put the test in Repeat Mode.
3) Respond to the questions in the following manner:
   - Select options? Yes
   - FRODO Only? No
   - Print Alt. Sectors? Yes
   - Halt after Error? No
   - Enter iteration count? Yes
     Manual - (XMIT or CR)
     Address - FF
     All remaining Test blocks - (XMIT or CR)
4) Proceed with overshoot adjustment.
   Note: The drive will do incremental seeks forward and then restore to Cyl# when the max Cyl# is reached. The adjustment waveform is invalid during this short restore period. Make the adjustment while the drive is seeking forward.
TOOLS NEEDED

- A short flat-bladed screwdriver (for removing the drive from the 210 housing).
- Small flat-bladed screwdriver (for adjusting pots).
- Medium flat-bladed screwdriver (for various mechanical adjustments).
- Allen wrenches (for various mechanical adjustments).
- Oscilloscope with three probes.
- 91% isopropyl alcohol and cotton swabs.

PRELIMINARY SETUP

- Remove the drive from the housing.
- If the alignment ROMs are being used, the drive must be connected to the controller. However, the cabling is often too short and the drive must be set on some object to allow the cable to reach the controller. The top casting will usually be adequate enough for this job.
- Remove the shields if they are used on the drive.

FLOPPY DRIVE LAYOUT
SERVO ADJUSTMENTS

Quick Reference

<table>
<thead>
<tr>
<th>REF. TO STEP</th>
<th>ADJUSTMENT</th>
<th>POT</th>
<th>TEST POINT</th>
<th>SETTING</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.1</td>
<td>Track Sense Amp.</td>
<td>R8</td>
<td>TP6</td>
<td>-1.75V Change when moving from track 43 to 54.</td>
</tr>
<tr>
<td>1.2</td>
<td>Track Sense Bal.</td>
<td>R19</td>
<td>TP6</td>
<td>-1.25V with positioner anywhere between track 54 and the spindle.</td>
</tr>
<tr>
<td>1.3</td>
<td>Fine Position Amp.</td>
<td>R8</td>
<td>TP2</td>
<td>3.3V op ±0.3V when positioner is moving.</td>
</tr>
<tr>
<td>1.4</td>
<td>Fine Position Bal.</td>
<td>R10</td>
<td>TP2</td>
<td>Centered about ground ±0.1V when positioner is moving.</td>
</tr>
<tr>
<td>1.5</td>
<td>Seek Symmetry</td>
<td>R33</td>
<td>SEEK CONPL.</td>
<td>Same seek times for forward and reverse when doing alternate seeks between tracks 51 and 52.</td>
</tr>
<tr>
<td>1.6</td>
<td>Seek Speed</td>
<td>R5</td>
<td>SEEK CONPL. &amp; TP2</td>
<td>No overshoot while doing alternate seeks between tracks 51 and 52. Maximum allowable seek time is 12ms, minimum is 10ms.</td>
</tr>
</tbody>
</table>

* Use the test point on the exerciser or P1-10 on the drive interface connector.

NOTE: All pots and test points are on the Lamp Amplifier board except R33, which is on the servo board. (Refer to preceding illustration). Use the eject motor ground strap for ground.

![Diagram of Lamp Amplifier Board](image)

LAMP AMPLIFIER BOARD

2-67
SERVO ADJUSTMENTS

1.1 TRACK SENSE AMPLITUDE

• Set up a scope as follows:
  CH1 : 0.5V/cm, DC coupled
  Channel Select: Ch. 1
  Sweep Rate : 1 ms/cm
  Trig. Mode : Auto
  Trig. Source : Normal

• Observe TP6 on the scope.

• Make the positioner cross over tracks 43 and 44 by unplugging the voice coil and moving it by hand.

• Plug the voice coil back in and perform one of the following:
  a) Use the ALRM command DT to alternate seek between tracks 32 and 64, or
  b) use the exerciser to alternate seek between tracks 40 and 47.

• Adjust R4 so the waveform is 1.75V peak-to-peak.

1.2 TRACK SENSE BALANCE

• Use the same scope setup as in step 1.1.

• Adjust R19 to make the negative portion of the display -1.25V. Be sure the ground is calibrated correctly.

1.3 FINE POSITION AMPLITUDE

• Use the same scope setup as in step 1.1.

• Observe TP2 on the scope.

• Move the positioner back and forth by:
  a) Unplugging the voice coil and moving it by hand.
  b) Using the ALRM command DP to alternate seek between tracks 0 and 8. Voice coil must be plugged in.

• Adjust R8 to get a 3.5v - 3.8vp-p waveform.
SERVO ADJUSTMENTS

1.4 FINE POSITION BALANCE

- Use the same scope setup as in step 1.1.
- Be sure the ground is calibrated correctly.
- Adjust R10 to balance the signal about ground = 0.1V.

1.5 SEEK SYMMETRY

- Set up a scope as follows:
  - CH1: 0.5V/cm, DC coupled
  - CH2: 2.0V/cm, DC coupled
  - Channel Select: Ch. 2
  - Sweep Rate: 1 ms/cm
  - Trig. Mode: Normal
  - Trig. Source: Ch. 2
  - Trig. Slope: Positive

- Connect Ch. 1 to TP2.
- Connect Ch. 2 to seek complete using either the exerciser test point, Pin 10 on the interface connector, or P8, Pin 2.
- Do an alternate seek between tracks 51 and 52 by,
  a) using the alignment ROM command SY or
  b) using the exerciser.
- Observe Ch. 2. There should not be any jitter on its trailing edge. If the trailing edge is not visible adjust the scope's sweep rate calibration until the edge can be seen.
- Adjust R33 on the Servo Board to eliminate the jitter as in the illustration on the following page.
- Use 10X to increase visibility of jitter.
SERVO ADJUSTMENTS

1.6 SEEK SPEED

- Use the same setup as in step 1.5.
- Observe Ch. 2 and adjust R5 to make the seek complete signal high, 10 ms. The Seek Time must not exceed 12 ms. If jitter appears on the trailing edge, go back to step 1.5.
- Change the channel select to Ch. 1 and look for overshoot as illustrated below.

OVERSHOOT

- Observe the trailing portion of the waveform, if it is not visible adjust the scope's sweep rate calibration until it can be seen.
SERVO ADJUSTMENTS

1.6 SEEK SPEED Continued

- If overshoot is visible, adjust R5 to slow down the seek speed until the overshoot is eliminated.

- Recalibrate the scope and check the seek time on Ch. 2.

1.7 DIRTY SCALE CHECK

- Restore the positioner to the track 0 position and move the positioner slowly by hand out to the spindle and back.

- Sudden changes in resistance (which may feel like a bump or dent in the positioner guides) indicates a dirty spot on the tach scale.

- When unsure whether the bump is a dirty tach or a physical problem in the positioner, power the drive down and move the positioner by hand. If the symptom goes away it is a dirty scale problem. Use a dry swab to clean the scale.
# PERSCI 277

## 277 DRIVE ADJUSTMENTS

### MECHANICAL ADJUSTMENTS

#### Quick Reference

<table>
<thead>
<tr>
<th>STEP TO STEP</th>
<th>ADJUSTMENT</th>
<th>PROCEDURE</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.1 or 3.1</td>
<td>HEAD RESOLUTION</td>
<td>Reduce head gain for high frequency, amplitude ≤ 95% Low frequency amplitude ≤ 95%. Use an unformatted diskette.</td>
</tr>
<tr>
<td>2.2 or 3.2</td>
<td>HEAD UNLOAD</td>
<td>Adjust the pickup ball and of head penetration so the read data is reduced by at least 50% when the head unloads.</td>
</tr>
<tr>
<td>2.3 or 3.3</td>
<td>CONE CHECK</td>
<td>Less than 15% amplitude modulation on read data after several ejects and insertions.</td>
</tr>
<tr>
<td>2.4 or 3.4</td>
<td>RADIAL ALIGNMENT</td>
<td>Skilled eye lobe ≤ 95% of the larger data eye lobe when on track 16.</td>
</tr>
</tbody>
</table>

* In all cases use TP4 and TP5 for Side 0 read signal and TP6 and TP7 for Side 1 read signal. Use the eject motor ground strap for ground.

On radial alignments use index (P11-2 on Side 0 and P9-2 on Side 1) for scope sync.

#### DATA AND INTERFACE PRINTED CIRCUIT BOARD

```
[Diagram showing connections, TP4, TP5, C21, C26, TP6, TP7, and a reference to PB]```

* Across C21, C26 for data stream monitoring.

Older version drives below level K* may not have these test points.

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MECHANICAL ADJUSTMENTS

DATA BOARD CONNECTOR DEFINITIONS
(Refer to Data and Interface PCB illustration on the previous page.)

<table>
<thead>
<tr>
<th>COMMON</th>
<th>SIDE 0</th>
<th>SIDE 1</th>
</tr>
</thead>
<tbody>
<tr>
<td>P1 I/O Cable</td>
<td>P11 Index Transducer</td>
<td>P9.</td>
</tr>
<tr>
<td>P3 Power Input</td>
<td>P12 Index LED</td>
<td>P10</td>
</tr>
<tr>
<td>P4 Spindle Power</td>
<td>P14 Head Load Solenoid</td>
<td>P13</td>
</tr>
<tr>
<td>P5 Spindle Servo</td>
<td>P16 R/W Head</td>
<td>P15</td>
</tr>
<tr>
<td>P6 Data Separator</td>
<td>P18 Eject Motor</td>
<td>P17</td>
</tr>
<tr>
<td>P7 Positioner Servo Power</td>
<td>P20 Write Protect</td>
<td>P19</td>
</tr>
<tr>
<td>P8 Positioner Servo</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The system may be forced to IPL to Side 1 by exchanging P11/P9, P14/P13, and P16/P15. To read and write with Side 0/1 exchanged, the write protect lines P20/P19 must also be exchanged.

2.1 HEAD RESOLUTION - SIDE 0

- Set up a scope as follows:
  - CH1: 0.1V/cm AC coupled
  - CH2: 0.1V/cm AC coupled, Inverted
  - Channel Select: Add
  - Sweep Rate: 1 ms/cm
  - Trig. Mode: Auto
  - Trig. Source: Normal

- Connect Ch. 1 to TP4 on the Data Board and Ch. 2 to TP5. Use the eject motor ground strap as the scope's ground point.

- Insert a test diskette into the Side 0 slot. The diskette should have a double frequency pattern written on track 1 and 76. This can be accomplished by writing a standard 1's and 0's pattern with the exerciser or by first seeking to track 1 (S1 option) and then using the W01 option with the alignment ROMs.

**NOTE:** Writing the above pattern will ruin the soft-sectoring on the diskette, making it useless for all other purposes.

- Seek to track 1 and observe the scope pattern. It should be similar to the illustration on the following page. The inner envelope amplitude should be less than 95% of the outer amplitude.
2.1 HEAD RESOLUTION - SIDE 0 Continued

- Seek to track 76 and observe the scope pattern. It should be similar to the illustration below. The inner envelope amplitude should be greater than 50% of the outer amplitude.

- If either of the above checks fail, slightly increase the Pressure Pad pressure by hand. The signal should improve; if it does not, the head or diskette is bad. If the signal improved, rotate the head pad slightly and again increase the pressure. Continue this process until a minimum change in signal quality is observed.
MECHANICAL ADJUSTMENTS

2.2 HEAD UNLOAD - SIDE Ø

- Use the same scope setup as in step 2.1.
- Load and unload the head, both tracks Ø and 76 by using either the exerciser head load switch or the alignment ROM HD option.
- When the head is unloaded, the read signal on the scope should drop by 50% or more.
- If the read signal did not decrease enough, lift the Pressure Pad Arm by hand (illustrated below) to determine whether the unload bail or the head penetration is at fault.

- If the signal did not improve, the head penetration is too great and little may be done to correct the problem.
- However, if the pickup bail is at fault adjust it using a (3/32") allen wrench.
MECHANICAL ADJUSTMENTS

2.3 CONE CHECK - SIDE Ø

- Use the same scope setup as in step 2.1.
- Eject and reinsert the diskette several times and observe the read signal at track Ø.
- The signal should never have more than 15% amplitude modulation. If it does, either the positioning cone is bad or the diskette hole is worn.

2.4 RADIAL ALIGNMENT - SIDE Ø

- Use the same scope setup as in step 2.1 except sync. external on Side Ø index (Pl1 Pin 2) and set the sweep rate to 20 ms/cm.
- Seek to track 38 by using the exerciser or the S38 option in the alignment ROM.
- Insert an alignment diskette and observe the cat's eye pattern on the scope. The smaller lobe must have 95% or more of the larger lobe's amplitude.
- If realignment is needed, remove the optical slide cover and loosen the two 3/32" allen screws holding the optical transducer assembly as illustrated below.
MECHANICAL ADJUSTMENTS

2.4 RADIAL ALIGNMENT - SIDE 0 Continued

• Reinsert the alignment diskette and gently move the optical transducer assembly, using the Lamp Amplifier Board as a handle.

• When proper alignment has been achieved, eject the alignment diskette and tighten down the mounting screws.

• Recheck the alignment before replacing the slide cover.

3.1 HEAD RESOLUTION - SIDE 1

NOTE: The Side 1 alignment depends on the Side 0 alignment being properly adjusted. DO NOT attempt to adjust Side 1 without first checking Side 0.

• Follow the same procedures outlined in step 2.1, except use TP6 and TP7.

3.2 HEAD UNLOAD - SIDE 1

• Follow the same procedures outlined in step 2.2, except use TP6 and TP7.

• The head penetration can be adjusted on Side 1 by using the head penetration adjustment screw as shown in the following illustration.

![Diagram of head penetration adjustment](image)
MECHANICAL ADJUSTMENTS

3.3 CONE CHECK - SIDE 1

- Follow the same procedures outlined in step 2.3, except use TP6 and TP7.

3.4 RADIAL ALIGNMENT - SIDE 1

- Follow the same procedures outlined in step 2.4, except use TP6 and TP7.
- Sync. externally on P9-2 for Side 1 Index.
- The Side 1 head may be moved into alignment by loosening the two 3/32" allen screws as shown below.

NOTE: Be careful to keep the head seated against its upper mounting surface to avoid Azimuth misalignment.
4.1 FREE RUNNING FREQUENCY

- Set up a scope as follows:
  - CH1: 2V/cm DC coupled
  - Channel Select: Ch. 1
  - Sweep Rate: 1μs/division
  - Trig. Mode: Normal
  - Trig. Source: Ch. 1
  - Trig. Scope: Negative

- Adjust R16 (R41 on later revisions) on the PLO card for a sawtooth waveform (illustrated below) with a one microsecond period on TP3.* There should not be a diskette inserted in the drive during this adjustment.

*Measure the period over ten cycles to insure accuracy.
PLO ADJUSTMENTS

4.2 FREQUENCY FINE ADJUSTMENT

- Move the Ch. 1 probe to TP2 on the PLO Board.
- Change the vertical sensitivity to 0.1V/cm, DC Coupled.
- Insert a formatted diskette.
- Select Side A and seek to Track 0.
- Fine-adjust R16 to center the noise band around 0 volt. Make sure the scope ground is properly calibrated.
- The sawtooth frequency may no longer be 1.0 MHz after performing this adjustment. Readjust if necessary.

4.3 PHASE ADJUSTMENT

- Insert a formatted diskette and read track 0.
- Change the Ch. 1 vertical sensitivity to 2.0V/cm.
- Setup Ch. 2 to 2.0V/cm, DC Coupled and set the channel select switch to ALT.
- Move the Ch. 1 probe to TP6 and the Ch. 2 probe to TP7. The scope display should be similar to the one shown below.

- Adjust R4 to center the trailing edge of the Ch. 2 signal between the rising and falling edges of the Ch. 1 signal.
PERSCI 277

277 DRIVE ADJUSTMENTS

ADDITIONAL ADJUSTMENTS

The following adjustments may need to be checked under rare circumstances. Refer to FA 107C for details.

1) Spindle Instantaneous Speed Variation (ISV) Check
2) Spindle Speed Check
3) Azimuth Alignment (Side 0 and Side 1)
CONFIGURATION SWITCH SETTINGS

<table>
<thead>
<tr>
<th>Memorex</th>
<th>Marksman/Fuji</th>
</tr>
</thead>
<tbody>
<tr>
<td>25MB</td>
<td>10MB</td>
</tr>
<tr>
<td>$81</td>
<td>$01</td>
</tr>
<tr>
<td>75MB</td>
<td>40MB</td>
</tr>
<tr>
<td>$83</td>
<td>$03</td>
</tr>
<tr>
<td>84MB</td>
<td>168MB</td>
</tr>
<tr>
<td>$92</td>
<td>$94</td>
</tr>
</tbody>
</table>

0 = OFF
1 = ON

Personality ROM

<table>
<thead>
<tr>
<th>Label</th>
<th>Memorex</th>
<th>Marksman/Fuji</th>
</tr>
</thead>
<tbody>
<tr>
<td>42MEM(y)</td>
<td>42MRK(y)</td>
<td></td>
</tr>
<tr>
<td>Part Number</td>
<td>A42671-001</td>
<td>A42671-002</td>
</tr>
</tbody>
</table>

y = Rev Level

2-82
WRITE CONTROL INSTRUCTIONS.

Idyy9D  \( \text{d} = \text{Controller Address} \)  \( \text{yy} = \text{Control Byte} \)

<table>
<thead>
<tr>
<th>Control Byte</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>$01</td>
<td>Inhibit Track Verify</td>
</tr>
<tr>
<td>$02</td>
<td>Inhibit Read After Write Check</td>
</tr>
<tr>
<td>$03</td>
<td>Set Memory I/O Mode</td>
</tr>
<tr>
<td>$04</td>
<td>Reset Memory I/O Mode</td>
</tr>
<tr>
<td>$05</td>
<td>Jump to Memory Location $4200</td>
</tr>
<tr>
<td>$10</td>
<td>Disable Termination Interrupt</td>
</tr>
<tr>
<td>$14</td>
<td>Enable Termination Interrupt</td>
</tr>
<tr>
<td>$60</td>
<td>Disable Alternate Sectoring</td>
</tr>
<tr>
<td>$70</td>
<td>Enable Alternate Sectoring</td>
</tr>
<tr>
<td>$7F</td>
<td>Send Identification String</td>
</tr>
<tr>
<td>$99</td>
<td>Incremental Seek</td>
</tr>
<tr>
<td>$A0</td>
<td>Move ROM Version to Status 0</td>
</tr>
<tr>
<td>$B0</td>
<td>Reset Status 0 to 04</td>
</tr>
<tr>
<td>$BC</td>
<td>Add ( n ) times 100,000 to Next Seek Parameter</td>
</tr>
</tbody>
</table>

STATUS INSTRUCTIONS

Idyy9D  \( \text{d} = \text{Controller Address} \)  \( \text{yy} = \text{Test Bit Value} \)

<table>
<thead>
<tr>
<th>Status Byte</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>$80</td>
<td>See Table on Next Page</td>
</tr>
<tr>
<td>$40</td>
<td>Seek or Write Terminated</td>
</tr>
<tr>
<td>$20</td>
<td>Service Request</td>
</tr>
<tr>
<td>$10</td>
<td>End</td>
</tr>
<tr>
<td>$08</td>
<td>Write Busy</td>
</tr>
<tr>
<td>$02</td>
<td>Read Busy</td>
</tr>
</tbody>
</table>

5/82

2-83
### Bit 80, 40, 20

<table>
<thead>
<tr>
<th>Bit</th>
<th>Read</th>
<th>Write</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0 0</td>
<td>OK</td>
<td>OK</td>
</tr>
<tr>
<td>0 0 1</td>
<td>Hard Error</td>
<td>Defective disc pack</td>
</tr>
<tr>
<td>0 1 0</td>
<td>Marked Sector</td>
<td>Marked Sector</td>
</tr>
<tr>
<td>0 1 1</td>
<td>Invalid Seek</td>
<td>Invalid Seek</td>
</tr>
<tr>
<td>1 0 0</td>
<td>Inoperable</td>
<td>Inoperable</td>
</tr>
<tr>
<td>1 0 1</td>
<td>Sector Should be in Map but is not</td>
<td>Sector Should be in Map but is not</td>
</tr>
<tr>
<td>1 1 0</td>
<td>Undefined</td>
<td>Undefined</td>
</tr>
<tr>
<td>1 1 1</td>
<td>Error While Alternating</td>
<td>Error While Alternating</td>
</tr>
</tbody>
</table>

**Read Status 2**

xdyy86  
\(d = \text{Controller Address} \quad yy = \text{Test Bit Value} \quad x = \text{Don't care} \)

**IOU 42**  
Returns $FD$ status on RS2

2-84
ALL SWITCHES SHOULD BE IN THE OFF POSITION
INITIAL SETUP OF THE FUJITSU DISC DRIVE

NOTE: BEFORE you apply power to the disc drive, be CERTAIN that the Spindle Lock, Motor Lock, and the Actuator Lock are all released.

Unlocking the Spindle
The spindle lock is located at the bottom of the spindle. The spindle lock must be released by loosening the two screws as shown below. Be certain to slide the lock back on BOTH screws so that the spindle grounding brush is centered on the spindle shaft. Do not allow the plate to move while tightening the screws.

Spindle Pulley
(Rotate only in direction of the arrow)

Spindle Grounding Plate

SPINDLE LOCKED

SPINDLE RELEASED

LABEL DETAIL

Center of Spindle Shaft

Spindle Pulley

Spindle Grounding Brush

Spindle Grounding Plate

SPINDLE RELEASED - SIDE VIEW

2-58
Unlocking the Actuator
The actuator must be released by rotating the lock as shown below. The lock can be seen through the top of the disc enclosure. The actuator locking lever can be reached directly beneath the lock, under the disc enclosure (see the illustration below).

ACTUATOR LOCKED  ACTUATOR RELEASED
Unlocking the Spindle Motor
To unlock the motor, loosen the two screws that hold the motor cover against the pulley and slide the motor cover to the left as far as it will go. With the motor lock in the "off" position, as shown in the illustrations below, tighten the screws.
+24V DC Select
Confirm that the +24vdc is selected by the switch on the rear side of the power supply unit as shown below:

Note: There should be a restraining bracket holding the select switch in the +24v position. If there is no bracket and/or the switch is not in the +24v position, the unit has probably been severely damaged. DO NOT APPLY POWER IN THIS CASE. Call Tech Support.

Power Cable Connection
The Fujitsu Disc Drive utilizes three power cables that run from the power supply. The AC cable runs from PW1, the DC cable runs from PW2, and the Power Control cable runs from CN18 as shown below. Three of the wires that run from CN18 merge with the harness that runs to front panel connector J1. The other three wires in this harness must be connected to CN1 on the motherboard which is under the card cages. CN1 must be reached from underneath the disc drive. For operation, SW1 (the main power switch) should be switched on and the other switch should be in "remote." WARNING SW1 must be turned off before any PWAs can be removed.
FUJITSU

DC Voltage Adjustment
Using a DVM, measure the DC voltages at TRM1 (on the card cage motherboard) shown in the illustration below. Adjust the voltages as necessary by turning the respective potentiometers, which are located on the power supply chassis. These potentiometers are shown in the previous illustration.

Interface Cabling
The illustration below shows the PWA chassis at the front of the disc drive and the cable connections to it. The interface cabling consists of two cables, "A" and "B." Cable A is to be connected to OM2, and Cable B is to be connected to OM3. A line terminator is to be connected to OM1. The grounding wire from the line terminator must be connected to the signal ground at TRM1(-2). Be sure that the small connector coming from the TRM1 harness is connected to PWC1.
The switches on the PWA chassis function as follows:

CKCLR - This is a momentary contact switch that resets a Device Check Status. Note that if a fault persists, it will not be reset.

FPT - This Switch inhibits any write operation, and it should normally be in the off position.

H-V - This jumper switch must be set in the H (for horizontal mounting) position.

The illustration below shows the location of the switches and diagnostic LEDs. The table defines the LED signal configurations.

Note: TG2 "ON" and TG1 "OFF" indicates "No Fault."
# TABLE OF ERROR CODES

<table>
<thead>
<tr>
<th>Unit Status</th>
<th>Error Code</th>
<th>TG1</th>
<th>TG2</th>
<th>ST1</th>
<th>ST2</th>
<th>ST3</th>
<th>ST4</th>
<th>ST5</th>
<th>ST6</th>
<th>Error/Fault</th>
</tr>
</thead>
<tbody>
<tr>
<td>Not Ready Status</td>
<td>00/02</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>X (2) 0 Not Power Ready</td>
</tr>
<tr>
<td></td>
<td>01/03</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>X 1</td>
<td>Not Speed OK</td>
</tr>
<tr>
<td></td>
<td>05/07</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>1</td>
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<td>1</td>
<td>X 1</td>
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<td></td>
<td>1D/1F</td>
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<td>0</td>
<td>0</td>
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<td>1</td>
<td>1</td>
<td>X 1</td>
<td>Not STARTP</td>
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<td>1</td>
<td>Seek or RTZ Time Out</td>
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<td>Linear Mode Guard Band</td>
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<td>RTZ Outer Guard Band</td>
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<td>1</td>
<td>0</td>
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<td>0</td>
<td>Over Track Crossing Pulse</td>
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<td></td>
<td>E0</td>
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<td>1</td>
<td>1</td>
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<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>Illegal Cylinder Address</td>
</tr>
</tbody>
</table>

*1) Error Code "3F" activates DVCK status, which is issued to the control unit and lights the DVCK LED on the optional front panel.
*2) "X" is irrelevant.

**NOTE:** TG2 ON and TGl OFF indicates "No Fault."
FUJITSU

SWITCH SETTINGS AND SERVO ADJUSTMENTS

Be sure that the switches on the CMKM, VOFM, and the SDIM boards are set as shown in the following illustrations.
Position Signal Adjustment on SDIM (servo) Board

- Set up the scope as follows:
  CH 1 1 v/cm (10x probe), DC coupled
  CH 2 1 v/cm (10x probe), DC coupled
  Trigger on CH 1 Pos.
  Horizontal 5ms/cm

- Type in the alternate seek instruction to seek between cylinders 0 and 512 as shown below:
  For 160 mb:
  (IPL) Q0020 004080 1d1D9D 006080 0020A7 (CR)
  Q0040 0d3030303030 (CR)
  Q0060 0d3232383830 (XMIT)
  Note: d = device number.
  For 84 mb:
  Q0060 to 0d3631343430
  Omit 1d19D and change

- Connect the channel 1 oscilloscope probe to TP19 on the SDIM (servo) board.
- Connect the channel 2 oscilloscope probe to TP7 on the same board.
- Observe channel 2 and adjust RV2 until the signal is 8.0v ± 0.4v peak-to-peak.

Note: If adjustments are so far off that the alternate seek will not run, use the RTZ (return to zero) command by repeatedly hitting IPL. Watch the scope and do the RV2 adjustment as described above. This should allow you to adjust the position signal accurately enough to allow the alternate seek to run.
Overshoot Adjustment on the SDIM (servo) Board

- Leave the scope probes and scope settings as they were for the previous adjustment.
- Start with the same scope settings that you used for the above tests.
- Issue the alternate seek command as shown below to seek between cylinders 0 and 8:

  For 168 mb:
  (IPL) Q0100 012080 014080 0100A7 (CR)
  Q0120 Od3030303030 (CR)
  Q0140 Od3031393230 (XMIT)
  Note: d = device number.

  For 84 mb:
  Change Q0140 to Od3030393630

- Center the channel 2 trace about ground and then set the channel 2 scope voltage selector to .5v/cm. Set the horizontal sweep selector to 1ms/cm to superimpose the forward and reverse overshoot traces.

- Adjust RV4 so the larger of the two overshoot signals is 1.0v above or below ground. (as shown in the illustration). Set the adjustment as close as possible to ±1.0v (be sure not to exceed 1.0v) as fastest seek times occur as 1.0v is approached.

---

PERFECT

[Diagram of overshoot adjustments]

---

5/82

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FUJITSU

Positioning Time Adjustment on the SDIM (servo) Board

• Use the alternate seek command as shown at the beginning of the "Position Signal Adjustment" section to seek between cylinders 0 and 512 (if the processor has been running continuously since the beginning of these adjustments, IPL and branch to address 0020 by typing: 0020A7 [XMIT]).

• Leave the channel 1 probe on TP19. Set the channel 2 voltage selector to 1V/cm and the horizontal sweep to 2ms/cm.

• Connect channel 2 (DC coupled) to TP26 and trigger on the negative edge of the channel 1 signal or on the positive edge of the channel 2 signal (one may be easier to sync on than the other).

• Observe channel 2 and adjust the deceleration time (Tdc) to 13ms ± 1ms by turning RV1. Be sure the entire waveform is visible on the scope. Be certain that both the horizontal and vertical are accurately calibrated, as this is a very critical adjustment.

• Connect channel 2 of the oscilloscope to TP30 and keep the channel 1 probe connected to TP19. Trigger on the positive edge of the signal on channel 1.

• Observe channel 2. Turn the channel 2 voltage sensitivity to 5V/cm and the horizontal sweep to 5ms/cm. If the horizontal time base is uncalibrated, it will enable observation of the entire waveform.

• Check the Vdc amplitude (deceleration amplitude) of the signal on channel 2. It must be 6.0V or less. Adjust RV1 only if the signal is greater than 6.0 volts. This will slightly increase the deceleration time measured at TP26, but this is acceptable.

• Go back and check the overshoot adjustment. Readjust if necessary (IPL and branch to 0100-- by typing: 0100A7[XMIT]-- to seek from 0 to 8).
COMPLETE SIGNAL LOOKS LIKE THIS. MEASURE THE LOWER, CURVED PEAKS AS SHOWN ABOVE.
**IOU-4**

**PERTEC**

**NOT READ-AFTER-WRITE**

**QANTEL MODEL 5200**

---

### ADDRESS SWITCHES 0-F

---

### MAX. PWR. REQUIREMENTS

- 12v = .03A
- + 5v = 1.69

Tape Unit Pwr.

115 vac 60 Hz

2.0A for .1 sec.

0.8A normal

---

### 800 BPI 12.5 IPS MAGNETIC TAPE MODULE

**WRITE CONTROL INSTRUCTIONS**

1XYY9D  \( x = \) DEVICE ADDRESS  \( yy = \) CONTROL BYTE

**CONTROL BYTE**

<table>
<thead>
<tr>
<th>Function</th>
<th>Control Byte</th>
</tr>
</thead>
<tbody>
<tr>
<td>SET ERASE</td>
<td>00</td>
</tr>
<tr>
<td>SET CHECK READ</td>
<td>01</td>
</tr>
<tr>
<td>SET BACKSPACE</td>
<td>02</td>
</tr>
<tr>
<td>SETREWIND</td>
<td>03</td>
</tr>
</tbody>
</table>

**STATUS IN INSTRUCTIONS**

4XYY9D  \( x = \) DEVICE ADDRESS  \( yy = \) STATUS BYTE

**STATUS BYTE**

<table>
<thead>
<tr>
<th>Status</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY</td>
</tr>
<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>MAGNETIC TAPE ALARM (ERROR)</td>
</tr>
<tr>
<td>20</td>
<td>ILLEGAL BLOCK LENGTH</td>
</tr>
<tr>
<td>40</td>
<td>E.F.M. / E.O.T. / LOAD POINT</td>
</tr>
<tr>
<td>80</td>
<td>INOP.</td>
</tr>
</tbody>
</table>

---

3-1

5/82
NOTES:

1. The IOU-20 can daisy chain up to 4 MAG TAPE UNITS. THE SWITCHES TELL THE SYSTEM THE NUMBER OF DRIVES ATTACHED.

2. The IOU-20 can handle either 25 ips, 37.5 ips or 75 ips.

WRITE CONTROL INSTRUCTIONS

1XYY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

FUNCTION

<table>
<thead>
<tr>
<th>BYTE</th>
<th>SET ERASE</th>
<th>SET CHECK READ</th>
<th>SET BACKSPACE</th>
<th>SET R/M</th>
<th>SET UNLOAD (REWIND AND OFF LINE)</th>
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<tr>
<td>00</td>
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</tr>
<tr>
<td>04</td>
<td></td>
<td></td>
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</tr>
</tbody>
</table>

STATUS INSTRUCTIONS

4XYY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

MEANING

<table>
<thead>
<tr>
<th>BYTE</th>
<th>MEANING</th>
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<td>01</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY</td>
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<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>ERROR</td>
</tr>
<tr>
<td>20</td>
<td>ILLEGAL BLOCK LENGTH</td>
</tr>
<tr>
<td>40</td>
<td>B.O.T. / E.O.T. / LOAD POINT</td>
</tr>
<tr>
<td>80</td>
<td>INOP.</td>
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</table>

3-2  5/82
NOTES:
THE IOU-21 CAN DAISY CHAIN UP TO 4 MAG TAPE UNITS. THE SWITCHES TELL THE SYSTEM THE NUMBER OF DRIVES ATTACHED.

THE IOU-21 CAN HANDLE EITHER 25 ips, 37.5 ips or 75 ips.

WRITE CONTROL INSTRUCTIONS

1XYY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

CONTROL

FUNCTION

BYTE

00  SET ERASE
01  SET CHECK READ
02  SET BACKSPACE
03  SET REWIND
04  SET UNLOAD (REWIND AND OFF LINE)

STATUS IN INSTRUCTIONS

4XYY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

STATUS

MEANING

BYTE

01  READ BUSY
02  WRITE BUSY
04  END
08  SERVICE REQUEST
10  UNCORRECTABLE ERROR
20  ERROR CORRECTION PERFORMED
40  B.O.T. / E.O.T. / LOAD POINT
80  INOP.

QANTEL MODEL

5211, 5212

MAX. POWER

REQUIREMENTS

+12v = .01A
+5v = 3.78A

TAPE DRIVE PWR.

Kennedy
115 vac 60 Hz
4.2A for .1 sec.
2.5A normal

Pertec
115 vac 60 Hz
2.0A for .1 sec.
1.0A normal

THE IOU-21 CAN HANDLE EITHER 25 ips, 37.5 ips or 75 ips.

WRITE CONTROL INSTRUCTIONS

1XYY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

CONTROL

FUNCTION

BYTE

00  SET ERASE
01  SET CHECK READ
02  SET BACKSPACE
03  SET REWIND
04  SET UNLOAD (REWIND AND OFF LINE)

STATUS IN INSTRUCTIONS

4XYY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

STATUS

MEANING

BYTE

01  READ BUSY
02  WRITE BUSY
04  END
08  SERVICE REQUEST
10  UNCORRECTABLE ERROR
20  ERROR CORRECTION PERFORMED
40  B.O.T. / E.O.T. / LOAD POINT
80  INOP.

00  Control  status 3-3

5/82
THE IOU-20A CAN DAISY CHAIN UP TO 4 MAG TAPE UNITS.
THE SWITCHES TELL THE SYSTEM THE NUMBER OF DRIVES ATTACHED.
THE IOU-20A CAN HANDLE EITHER 25 ips, 37.5 ips, or 75 ips.

WRITE CONTROL INSTRUCTIONS

\[ d \ y y \ y y \ y y \]  
\[ d = \text{DEVICE ADDRESS} \quad y y = \text{CONTROL BYTE} \]

CONTROL FUNCTION

<table>
<thead>
<tr>
<th>BYTE</th>
<th>FUNCTION</th>
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</thead>
<tbody>
<tr>
<td>00</td>
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<td>SET CHECK READ</td>
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<td>03</td>
<td>SET REWIND</td>
</tr>
<tr>
<td>04</td>
<td>SET UNLOAD (REWIND AND OFF LINE)</td>
</tr>
</tbody>
</table>

STATUS IN INSTRUCTIONS

\[ d \ y y \ y y \ y y \]  
\[ d = \text{DEVICE ADDRESS} \quad y y = \text{STATUS BYTE} \]

STATUS MEANING

<table>
<thead>
<tr>
<th>BYTE</th>
<th>MEANING</th>
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<tbody>
<tr>
<td>01</td>
<td>READ BUSY</td>
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<tr>
<td>02</td>
<td>WRITE BUSY</td>
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<tr>
<td>04</td>
<td>END</td>
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<td>SERVICE REQUEST</td>
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<td>ERROR</td>
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<td>B.O.T. / E.O.T. /</td>
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<td>INOP.</td>
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</tbody>
</table>

3-4 5/82
THE IOU-21A CAN DAISY CHAIN UP TO 4 MAG TAPE UNITS.
THE SWITCHES TELL THE SYSTEM THE NUMBER OF DRIVES ATTACHED.
THE IOU-21A CAN HANDLE EITHER 25 IPS, 37.5 IPS, OR 75 IPS.

WRITE CONTROL INSTRUCTIONS
4dyy90  d = DEVICE ADDRESS  yy = CONTROL BYTE

CONTROL FUNCTION
BYTE
00  SET ERASE
01  SET CHECK READ
02  SET BACKSPACE-
03  SET REWIND
04  SET UNLOAD (REWIND AND OFF LINE)

STATUS INSTRUCTIONS
4dy90  d = DEVICE ADDRESS  yy = STATUS BYTE

STATUS MEANING
BYTE
01  READ BUSY
02  WRITE BUSY
04  END
06  SERVICE REQUEST
10  UNCORRECTABLE ERROR
20  ERROR CORRECTION PERFORMED
40  B.O.T. / E.O.T. / END FILE
80  INOP.
PERTEC

PERTEC TRANSPORT ALIGNMENT

The mechanical / electrical adjustments for the Pertec 5200 (800 BPI, 12.5 IPS), 5201 (800 BPI, 25.0 IPS) and the 5211 (1600 BPI P.E., 25 IPS) are included in the following instructions.

Each check and/or adjustment should be performed in the order that it is given. This is important, as most adjustments affect much or all of the alignment that follows.

In most cases the minimum and maximum acceptable limits for adjustments will be defined within their particular adjustment procedure. If a value is checked and falls within the acceptable limits, no adjustments should be attempted. However, if a value does not fall within the acceptable limits, then and only then, should the adjustment be performed.

INSPECTION - 5200, 5201, and 5211

Careful inspection of the transport will actually save time and prevent minor problems from becoming major.

Visually inspect and check the following:

- Inspect the Head and all Tape Guides; the Tension Arms for travel; Capstan; Roller Guides; and all Push Buttons.

- Inspect all P.C. Boards; verify that they are properly seated.

- Inspect Arm Optics and miscellaneous Mechanical Items in back.

- Inspect all Molex Connectors (if any of the individual pin connectors have separated, crimp them back together).
PERTEC

ELECTRICAL ADJUSTMENTS - 5200

POWER SUPPLY

The +5V and -5V regulators are located on the Tape Control A Board, see Figure 2 below.

- With the power on, use a DVM to measure the voltage between TP9 (+5V) and TP7 (0V, ground). The voltage should fall between +4.85V and +5.15V.

- If the voltage in the previous step doesn't fall within the specified limits, adjust R1302 to +5V as seen on TP9.

- Now use the DVM to measure the voltage between TP8 (-5V) and TP7 (0V, ground). The voltage should fall between -4.85V and -5.15V.

- If the voltage in the previous step doesn't fall within the specified limits, adjust R1308 to -5V as seen on TP8.

EOT/BOT

NOTE: On all Control Boards the EOT/BOT pots must be adjusted for zero ohms resistance before performing adjustments on the EOT/BOT Amp Board.

- Load a scratch tape.

- With the power ON, the tape should be tensioned but not at Load Point.
PERTEC

ELECTRICAL ADJUSTMENTS - 5200

EOT/BOT CONTINUED

• Using a DVM, connect the probes to TP24 (ground) and TP1 (on the EOT/BOT amp board). See Figure 3.

• If necessary, adjust R3 (on the amp board) to obtain between +4V and 4.25V on TP1.

\[ \text{FIGURE 3} \]

• Again, using a DVM, connect the probes to TP24 (ground) and TP2.

• If necessary, adjust R9 (on the amp board) to obtain between +4V and 4.25V on TP2.

RAMP

• Load a scratch tape and apply power to the drive.

• Depress and release the LOAD control two times. The tape will advance to the Load Point and stop.

• IPL the system and from device zero enter the following program:

```
0010F301000C00010F201006d1d02900006A7 (d = Device)
TRANSMIT
ENTER 32 F's
TRANSMIT
```

3-9 8/78
PERTEC

ELECTRICAL ADJUSTMENTS - 5200

RAMP CONTINUED

• Connect the scope probes to TP7 (ground) and TP6 (on the Tape Control A Board, refer to Figure 2).

• Observe the drive hubs, they should be rotating back and forth.

• The waveform (as viewed on the scope) should fall 30 ms ±1, level off to zero and fall another 30 ms ±1. The entire waveform should fall 60 ms. See Figure 4.

![Figure 4]

-30 ms — 30 ms
START   STOP 10 ms/div.

• If the waveform does not meet the values as stated above, adjust R913 on the Tape Control A Board until it is corrected.

SPEED

• Mount a SKEW tape on the transport.

• To advance the tape, IPL the system and from device zero enter the following program:

```
0D10F201000d0000A7 (d = Device)
TRANSMIT
```

• If the system is not available for use, load a SKEW tape and then manually advance the tape beyond Load Point. If tape tension is lost, press Load Point twice and the tape will advance.
PERTEC

ELECTRICAL ADJUSTMENTS - 5200

SPEED CONTINUED

- Connect the scope probes to TP5 (ground) and TP603 (Data A Board). See Figure 5.

![Diagram of electrical connections](image)

**FIGURE 5**

- The waveform (as viewed on the scope) should appear as in Figure 6. The distance between the peaks must be .2 ms.

![Waveform diagram](image)

**FIGURE 6**

- If the waveform does not meet the above value adjust R1001 on the Tape Control A Board until it is corrected. Refer to Figure 2.
READ AMPS

- Load a scratch tape.
- Advance the tape to Load Point.
- IPL the system and from device zero enter the following program:
  
  0010F30100000010F201008d0006A7 (d = Device)
  
  TRANSMIT
  
  Enter - 32 F's
- Allow the program to run for a good portion of the tape. This procedure creates an 'all ones' tape.
- Rewind the tape back to the Load Point.
- IPL the system and from device zero enter the following program:
  
  0010F201000d0000A7 (d = Device)
  
  TRANSMIT
- Connect the scope probes to TPS (ground) and TP103 (Data A Card). Refer to Figure 5.
- The waveform (as viewed on the scope) should appear as in Figure 7. The distance from peak-to-peak must fall between 10.0V to 11.0V.

![Figure 7](image-url)
PERTEC

ELECTRICAL ADJUSTMENTS - 5200

READ AMPS CONTINUED

• All Read Test Points must be checked in the manner described above. Check points TP103-TP903 (on the Data A Board).

• If any of the wave forms do not meet the above value, adjust the proper pot R111-R911 on the Data A Board until they are corrected.

SKEW

• Mount a SKEW tape on the transport.

• Advance the tape to Load Point.

• Connect the scope probes to TP5 (ground) and TP3 (Data A Board).

• Display one full waveform, as in Figure 8.

![Figure 8]

- Fall time must be less than one division

- The distance between the rise and the fall cannot be more than five (5) of the small divisions.

- Perform the adjustment by shimming one of the guides on either side of the head. **ONLY SHIM ONE OF THE GUIDES.** There should never be shims under both guides.
POWER SUPPLY

The +5V and -5V regulators are located on the Tape Control B1 Board (see Figure 9).

With the power ON, use a DVM to measure the voltage between TP17 (+5) and TP24 (OV, ground) on the Tape Control Board. The voltage should fall between +4.85V and +5.15V.

If the voltage did not meet the above values, adjust R1202 (+5) on the Tape Control Board to +5V as seen on TP17.

Now use the DVM to measure the voltage between TP23 (-5V) and TP24 (OV, ground) on the Tape Control B1 Board. The voltage should fall between -4.85V and -5.15V.

If the voltage in the previous step did not fall within the specified limits, adjust R1208 on the Tape Control B1 Board (Figure 9) to -5V as seen on TP23.

EOT/BOT

NOTE: On all Control Boards the EOT/BOT pots must be adjusted for zero ohms resistance before performing adjustments on the EOT/BOT Amp Board.

Load a scratch tape.

With the power ON, press the LOAD control one time. The tape should be tensioned but not at load point.
PERTEC

ELECTRICAL ADJUSTMENTS - 5201 AND 5211

EOT/BOT CONTINUED

● Using a DVM, connect the probes to TP11 (ground) on the Data C9 Board (Figure 10) and TP1 on the EOT/BOT Amp Board.

![Diagram](image)

**Figure 10**

● If necessary adjust R3 (on the Amp Board) to obtain between 3.95 and 4.05V on TP1.

● Again, using a DVM, connect the probes to TP11 (ground) on the Data C9 Board and TP2 on the EOT/BOT Amp Board.
— If necessary adjust R9 (on the Amp Board) to obtain between +3.95V and 4.05V on TP2.

RAMP

— Load a scratch tape and apply power to the drive.

— Depress and release the LOAD control two times. The tape will advance to Load Point and stop.

— IPL the system and from device zero enter the following program:
  0010F30100000010F201008d1d029D0006A7 (d = Device)
  TRANSMIT
  Enter 32 F's
  TRANSMIT

— Connect the scope probes to TP11 (ground) on the Data C9 Board (Figure 10) and TP1B on the Tape Control B1 Board (Figure 9).

— The waveform (as viewed on the scope) should fall 15ms ±1, level off to zero and fall another 15ms ±1 (see Figure 12).

If the waveform does not meet the values as stated above, adjust R1113 on the Tape Control B1 Board (Figure 9) until it is corrected.
PERTEC

ELECTRICAL ADJUSTMENTS - 5201 AND 5211

SPEED

• Mount a SKEW tape on the transport.

• To advance the tape, press the FORWARD button.

• Connect the scope probes to TP11 (ground) and TP603 on the Data C9 Board (refer to Figure 10).

• The wave form (as viewed on the scope) should appear as in Figure 13. The distance between the positive peaks must be .1 ms.

![Waveform Image]

FIGURE 13

• If the wave form does not meet the above value adjust R1501 on the Tape Control B1 Board (Figure 9) until .1 ms between peaks is obtained.

READ AMPS

• Load a scratch tape.

• Advance the tape to Load Point.

• IPL the system and from device zero enter the following program:

  0010F30100000010F2010Bd0006A7 (d = Device)

  TRANSMIT; Enter 32 F's; TRANSMIT

3-17

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READ AMPS CONTINUED

- Allow the program to run for a good portion of the tape. This procedure creates an 'all ones' tape.

- Rewind the tape back to Load Point.

- Connect the scope probes to TP11 (ground) on the Data C9 Board (Figure 10) and the following test points, depending upon which drive is being adjusted:
  - 5201 - TP103 on the Data C9 Board.
  - 5211 - TP104 on the Data H Board (see Figure 14).

- Take the transport OFF LINE and push the FORWARD button.

- The wave form (as viewed on the scope) should appear as in Figure 15. The distance from peak-to-peak must fall between the following voltages:
  - 5201 - 10.0V to 11.0V
  - 5211 - 5.0V to 6.0V

- Check all the Read Amp Test Points as described above. The following Test Points must be checked:
  - 5201 - TP103 to TP903 on the Data C9 Board.
  - 5211 - TP104 to TP904 on the Data H Board.
PERTEC

ELECTRICAL ADJUSTMENTS - 5201 AND 5211

READ AMPS CONTINUED

FIGURE 15

- If any of the wave forms do not meet the correct values, adjust the proper pot until it is corrected.

5201 - R112 to R912 on the Data C9 Board
5211 - R117 to R917 on the Data H Board

READ STATICISER DENSITY - 5201 TRANSPORT ONLY!

- Generate an 'all ones' tape as in the Read Amp procedure.

- Rewind the tape back to Load Point, take OFF LINE and push the FORWARD button.

- Connect the scope probes to TP11 (ground) and TP10 on the Data C9 Board (Figure 10).

- Observe the wave form at TP10. It should appear as in Figure 16.

- Adjust R32 on the Data C9 Board to obtain 25 usec. However, do not exceed 26.3 usec maximum or 23.7 usec minimum.
PERTEC

ELECTRICAL ADJUSTMENTS - 5201 AND 5211

READ STATICISER DENSITY CONTINUED

FIGURE 16

25 usec.

WRITE SKEW - 5201 TRANSPORT ONLY!

• Generate an 'all ones' tape.

• While the tape is writing and On Line, connect the scope probes to TP11 (ground) and TP15 on the Data C9 Board (Figure 10).

• Observe the wave form at TP15. It should appear as in Figure 17.

FIGURE 17

YES

NO
PERTEC

ELECTRICAL ADJUSTMENTS - 5201 AND 5211

WRITE SKEW CONTINUED - 5201 TRANSPORT ONLY!

• If necessary, adjust R18 on the Data C9 Board until the waveform appears as in Figure 17.

SKEW

• Mount a SKEW tape on the transport.

• Advance the tape to Load Point.

• Connect the scope probes to TP11 (ground) and the following test points:
  5201 - TP15 on the Data C9 Board
  5211 - TP10 on the Data H Board.

• Display one full waveform as in Figure 18.

![Figure 18](image)

- FALL TIME MUST BE LESS THAN ONE DIVISION

• The distance between the rise and the fall cannot be more than five (5) of the small divisions.

• Perform the adjustment by shimming one of the guides on either side of the head. ONLY SHIM ONE OF THE GUIDES. There should never be shims under both guides.
NOTE: Before attempting any of the following adjustments, thoroughly clean the tape path and then check the transport for proper operation. If a problem exists, proceed with the necessary adjustment.

**TENSION ARM LIMIT SWITCH**

Observe the Tension Arm as it is resting against its backstop. Perform the following adjustment if the position of the limit switch roller in relation to the cam, does not appear as in Figure 19.

- Loosen the cam retaining set-screw.
- Rotate the cam on its shaft until the limit switch roller is in the position shown in Figure 19.
- Tighten the cam retaining set-screw.

**NOTE:** The set-screw must be tightened sufficiently to prevent rotation of the cam when the tension arm strikes against its backstop.
TENSION ARM LIMIT SWITCH CONTINUED

- The limit switch plate should be rotated to a position where the limit switch trips when its roller is one-half of the distance up the slope from its rest position.

- The switch should be closed when the roller moves on the cam lobe between the semi-circular cutouts.

TENSION ARM SENSORS

NOTE: The +5V and -5V Regulators, Ramp Timing and Tape Speed must be correctly adjusted before adjusting the tension arm sensor.

- Remove tape from the transport.

- When adjusting the sensors, they should be shielded from high concentrations of light.

- Loosen the retaining nut securing the optical shutter to the tension arm shaft. See Figure 19.

- Loosen the nut so that the shutter can be rotated by hand, yet tight enough to prevent the setting from changing when the nut is tightened.

- Apply power to the transport.

- Rotate the shutter until moving the tension arm to the middle of its range stops reel motion.

NOTE: The LOAD control must be continuously depressed or the Limit Switch shorted to perform this procedure.

REEL SERVO BELT TENSION

- Loosen the three screws that secure the motor mounting plate to the deck standoffs.
Adjust the pulley so that the timing is snug. Note the last belt tooth that is completely seated in a slot on the large pulley. See Figure 20.

Count two to three teeth from the last engaged tooth. Hold the large pulley to ensure that it does not turn. Depress the toothed belt at the point between the second and third tooth with sufficient force to deflect the belt against the gear.

Adjust the drive motor assembly so that the second tooth is firmly engaged in a slot on the large pulley, but the third tooth is not engaged.

Tighten the three screws on the motor mounting plate.

Tape Cleaner

Remove the screw holding the tape cleaner to the transport and loosen the two screws which hold the cleaner blade to the housing.

Use a cotton swab moistened in isopropyl alcohol to clean the blade and housing.

Be sure that the tape cleaner blade surface is parallel to the tape when mounted on the deck.
The following instructions include mechanical and electrical adjustments for both the 500 bpi and 1600 bpi Kennedy tape transports. All of the mechanical adjustments are identical between the two types of recording systems, and most of the electrical adjustments are also the same. Where differences exist between types, the differences are indicated within the text.

Make each check or adjustment in the order that it is given, checking and/or performing all of the adjustments that follow. This is important, as most adjustments affect much or all of the alignment that follows.

Access to the internal test points and adjustments (on the printed circuit cards) is gained from the front (operator side) of the transport. To expose the printed circuit boards, release the captive screw directly below the head assembly, and swing the entire panel and mechanism outward.

**MECHANICAL ADJUSTMENTS**

The following adjustments are used to align the tape handling hardware. Before making the adjustment, check for proper operation as described at the beginning of each procedure. Adjust if needed, but only when needed.

**Tape Path**

- Tape should wind evenly on the center of the takeup reel with no tendency to pile up toward one side or the other.

  The tape should enter the roller guides correctly, exerting equal pressure on both the inside and outside edges of the tape.

  Adjust as follows:
  - Remove the roller guide that is to be adjusted.
  - Temporarily replace the guide with a short length (about 4") of 18-8 or other non-magnetic stainless steel ground stock that is .1875" (3/10") diameter.
  - Loosen the roller guide adjustment lock screw (A in the drawing below) slightly until the rod can be moved with moderate pressure.

---

**Diagram**

---

3-25
KENNEDY

- Thread the recording tape over the rod.
- While holding the rod, run the tape forward. Adjust the angle by tilting the rod back and forth until the tape has equal tension toward the edges, and enters the guides (or reel) properly.
- When the rod is positioned correctly, tighten the lock screw. Leave the tape threaded for adjustment below.

**Replace and Adjust Roller Guide** (after adjusting angle, above)

- Loosen the roller guide retaining screw (item C in the illustration on the previous page). Do not loosen item A.
- Insert the roller guide shaft until part of the threaded portion of the shaft protrudes from the other side. Then lightly snug the clamping (retaining) screw, C.
- Place several number 10 flat washers over the threaded portion of the shaft, and install a 10-32 nut.
- Using the 10-32 nut as a fine adjustment, tighten until the roller is aligned (centered on the tape). If this adjustment is moved too far, loosen the clamp screw (C) again and the 10-32 nut to re-position the shaft for another try.

**Idler Alignment**

- The tape should track at the same place on the capstan during forward and backward operation.
  
  Adjust as follows:
- Loosen the locknut on the idler adjustment screw (see the illustration below).

![Diagram of roller guide and idler alignment](image-url)
KENNEDY

• Loosen the collar set screw (refer to the illustration on the previous page).
• Adjust the idler adjusting screw until the tape tracks at the same place on the capstan in either direction.
• Re-tighten the collar screw and the locknut.

Head Face Shield Adjustment

• Remove the tape from the transport and lift the shield as shown in the illustration below.

NOTE: Head cover removed for clarity.

• Loosen the stop screw on the face shield.
• Insert .006" thickness of tape (three layers of .002" tape) between the shield surface and the head. Do NOT use feeler gauges; metal will destroy the head by scratching it.
• Compress the shims firmly by holding the shield toward the head, and tighten the the stop screw.
• Lift the shield and remove the tape pieces.

3-27
Power Supply Voltage Checks

Power supply voltages should be checked at the test points shown in the illustrations above. The plus and minus 24v (case of Q8 and Q9 on A5) will be a nominal ± 26v under light load. The 10v supplies should be regulated to within ± .5v. The +5v should be adjusted if necessary to within ± .25v.
KENNEDY

Tape Speed

The following adjustment is one of the most important and most neglected of the entire procedure. The adjustment is exactly the same for 800 bpi and 1600 bpi transports. The 800 bpi Skew Master tape is used for both types.

- Mount the Skew Master tape onto the transport.
- Connect an oscilloscope probe to one of the preamplifier test points. The preamplifier printed circuit board is located on the back of the "swing out" portion of the transport front panel (shown on page 3-7).
- Start the tape reading in a forward direction.
- Adjust the NORMAL SPEED pot, R14, on the Ramp Generator printed circuit board until the waveform shows 100usec for each sinewave. The sinewave may not be entirely stable on the screen because of small rapid speed variations.

Ramp Time

- Load a scratch tape onto the transport, and place the unit ON LINE.
- Connect an oscilloscope probe to TEST POINT A (see the illustration above).
- From system device zero, enter the following program:

```
0020f30100000100a7 CARRIAGE RETURN or TERMINATE
0010f20a00bx1x029d0010f20a000x1x029d0109a7 CARRIAGE RET.
```

or

- The waveform should appear as shown; adjust the START and STOP ramp times by using pots R3 and R4 (labeled START and STOP in the illustration above).
KENNEDY

Photo Sensor

- Verify by looking at the light source that both lamps are lit.
- Connect a dc voltmeter between test points E and F on the sensor amplifier/driver module (see below).

![Diagram of Photo Sensor

1 2 3 4 5 6

NOTE

Tape should NOT be mounted in the drive for this check or adjustment.

- Vary potentiometer R16 until there is zero voltage between the two points.

Capstan Servo

- Test the capstan for proper operation as follows:

  The capstan should never "creep" when it should be standing still.

  Load a tape and advance to the LOAD point. Gently grasp the capstan and try to rotate it in either direction. The resistance to rotation should be the same in both directions. A small "dead" zone should be felt midway between the resistance to rotation.

  Adjust as follows:

- With the tape at LOAD point, adjust the ZERO ADJUST potentiometer until there is zero volts between TPA and chassis ground.

![Diagram of Capstan Servo

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KENNEDY

Reference Frequency (1600 bpi Transport Only)

- Load a scratch tape onto the transport (include a Write Enable ring).
- Place the transport into the TEST MODE.
- Place an oscilloscope probe on TEST POINT A of the READ CONTROL printed circuit board.
- Depress the WRITE TEST button on the test panel.
- Adjust the REFERENCE FREQUENCY, R1, on the READ CONTROL board until the frequency is 320 kHz, or 3.1 usec for a single cycle.

Read Level

- Load a scratch tape (with a Write Enable ring) onto the transport. DO NOT USE THE SKEW TAPE.
- Place an oscilloscope probe on the test point for Read Preamplifier Channel 1. The Read Preamplifier test points and adjustments are on the back of the "swing out" panel (see illustration on page 3-7).
- Place the transport into TEST MODE.
- Depress the WRITE TEST and FORWARD RUN buttons.
- Adjust the potentiometer to make the signal $5v \pm 0.0v$ for 1600 b.p.i. drives or $9v \pm 0.0v$ for 800 b.p.i. drives.

Notes:
1. There will be some crosstalk noise from the write heads, ignore this effect.
2. The amplitude is approximately 10% higher in the Read-After-Write mode because of magnetic remanence in the write and erase heads.
- Adjust the remaining channels by moving the test probe to each channel and adjusting in the same manner.

Skew Adjustment

- Load the Skew Master tape onto the transport and place the unit into the TEST MODE.
- Place an oscilloscope probe on the test point at the bottom of the test panel.
- Depress FORWARD RUN. The pattern will be a grouping of nine pulses that are repeated (similar to that below).
- Adjust the skew until the grouping is as small as possible.

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Write Skew (800 bpi Transports Only)

The write skew is a digital delay that is controlled by a four-switch selector for each channel that is adjusted. All channels are referenced to the P (parity) channel which has no adjustment.

- Perform the Read Skew adjustment as given on the previous page. Connect an oscilloscope probe to the P (parity) channel on the Write Amplifier Module. See the illustration below for test point locations.

- Mount a scratch tape on the drive. Be sure that the scratch tape contains a Write Enable ring.

- Connect a second oscilloscope probe to the CH 5 test point. Channel 5 is one of the outermost channels on the tape.

- Set the oscilloscope to trigger on the P channel test point (CH 1, INT), and ALTer nate sweep.

- With the unit in the TEST MODE, depress WRITE and FORWARD RUN.

- Set the CH5 deskewing switches for a minimum separation between the two displayed peaks.

- Move the probe from CH5 to CH4. Channel 4 is the other outer extreme track on the tape.

- Use the CH4 deskewing switches to minimize the separation between peaks.

- Adjust each channel in the same way as above.

Deskewing switches are located on boards 6 and 7.
DIGI-DATA TRANSPORT ALIGNMENT

The mechanical/electrical adjustments for the 800 bpi and 1600 bpi Digi-Data tape transports are included in the following instructions. Most of the adjustments for the two types of transports are the same. However, some differences do exist and are noted in the text.

Each check and/or adjustment should be performed in the order that it is given. This is important, as most adjustments affect much or all of the alignment that follows.

INSPECTION

Careful inspection of the transport will actually save time and prevent minor problems from becoming major.

Visually inspect and check the following:

- Inspect Flux Gate and Heads; both Tension Arms for travel; Reel Hub; all Tape Guides; and all Push Buttons.

- Inspect EOT/BOT Sensor; the BEC P.C. Board (on back of panel); all the Boards, verify seating and that boards are held in with (2) screws; and inspect the EOT/BOT pots on the RA-16 card (Verify that they are turned counter-clockwise, all the way).
NOTE: To gain access to the MC-17 card, remove the two screws holding the card cage and swing it open.

CAUTION should be taken when replacing the screws if the power is ON.

Inspect Arm Optics; miscellaneous Mechanical Items in back; the Harness (wire) in order; and the Write Enable Assembly.

![Diagram of DIGI-DATA 1730](image)

**FIGURE 2**

**INITIAL OFFLINE CHECK AND ADJUSTMENT**

Check that the input voltage noted on the transports model number tag and the actual line voltage at the intended installation agree. If not, refer to the Table below (Figure 3) to rearrange the transformer taps. The transport is supplied with a standard U.S. power cord and polarized 3-prong plug. In other countries it may be necessary to remove this plug and install the locally required power plug.

**TRANSFORMER PRIMARY CONNECTIONS ON TB1**

<table>
<thead>
<tr>
<th>VOLTAGE RANGE</th>
<th>TB1-1</th>
<th>TB1-2</th>
<th>TB1-3</th>
<th>TB1-4</th>
<th>TB1-5</th>
<th>TB1-6</th>
</tr>
</thead>
<tbody>
<tr>
<td>95-115 VAC</td>
<td>BRN,YEL</td>
<td>-</td>
<td>RED,GRN</td>
<td>(GRN)</td>
<td>(BLU)</td>
<td>-</td>
</tr>
<tr>
<td>105-125 VAC</td>
<td>BRN,YEL</td>
<td>-</td>
<td>GRN,BLU</td>
<td>(RED)</td>
<td>(GRN)</td>
<td>-</td>
</tr>
<tr>
<td>195-235 VAC</td>
<td>BRN</td>
<td>-</td>
<td>GREEN, RED,YEL</td>
<td>(GRN)</td>
<td>(BLU)</td>
<td>-</td>
</tr>
<tr>
<td>205-245 VAC</td>
<td>BRN</td>
<td>-</td>
<td>GREEN, ORN,YEL</td>
<td>(RED)</td>
<td>(BLU)</td>
<td>-</td>
</tr>
<tr>
<td>215-255 VAC</td>
<td>BRN</td>
<td>-</td>
<td>BLUE, GRN,YEL</td>
<td>(RED)</td>
<td>(GRN)</td>
<td>-</td>
</tr>
</tbody>
</table>

**FIGURE 3**

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- Check all three fuses visually. (Refer to Figure 2)
- Check/Adjust +12V, ±1V (after 10 min. warm-up) (Refer to Fig. 4)
- Check -12V and +5V, for ±5%.
- Check +5(s) V for 4.5 - 5.5.
- Check ARM P.C. for 5.6V.

You May Ground The Meter To The Chassis.

**Figure 4**

**EOT/BOT SENSOR**
- Thread a 10.5" reel of brown tape on drive and "load".
- Warm up for at least (1) minute.
- Locate the three-position service switch (see Figure 2) and run the tape forward away from the BOT mark.
- Refer to Figure 2 for BEC card location and check the voltage from ground to the EOT and BOT test points. It should be about 8.0 volts. If you use black tape it should be about 9.0 volts. See Figure 5 for EOT/BOT test point locations.

TP 7. ground supply 8.0V (centered)

TP 8. ground take-up

Photo cell disc adjustment
NOTE: The BOT and EDT pots on the RA-16 (1600 bpi Figure 6) card and the RC-11 (800 bpi Figure 16) card must be turned all the way counter-clockwise before you adjust the pots on the BOT/EDT Sensor card.

DO NOT REMOVE ANY CARD FOR 1 MINUTE AFTER THE POWER IS OFF!
• Push Rewind (hold the button for one second). After the tape has reached load point, check the ON/OFF LINE pushbutton switch by pressing it repeatedly and verifying that the associated indicator light alternately glows and is extinguished.

• With the transport ON LINE, verify that the service switch is inoperable.

• With the transport OFF LINE, move the service switch to the FORWARD position. After several feet of tape have run onto the take-up reel, return the service switch to the NORMAL position; tape motion should cease.

• Move the service switch to the REVERSE position. After several feet of tape have run onto the supply reel, return the switch to the NORMAL position; tape motion will again cease.

  CAUTION: The service switch ignores the BOT and EOT markers.

• Use the service switch to run the tape forward again. Visually check all the tape path components for smooth operation.

• Check the arm positions, (1) maximum positions for forward and reverse, and (2) mid positions for stopped.

  NOTE: The punch marks on deck.

• Perform the previous two steps near EOT.

• After stopping the tape, place the transport ON LINE, then press the REWIND pushbutton switch. The transport will go OFF LINE and the tape will rewind until the BOT reflective marker is sensed. When rewinding, the tape will overshoot the load by several inches, and then return forward to the exact load point location.

• Use the service switch to again run fifty to a hundred feet of tape onto the take-up reel. Return the service switch to the NORMAL position. Press the REWIND switch. After the tape has reached the full rewind speed, press the POWER switch off. The transport should stop smoothly without spilling the tape. Do this several times; make sure the tape stays on the guides.

• If possible perform the previous step on a small reel that is almost empty.

• Press the POWER switch on again. Press LOAD and REWIND simultaneously; continue to hold them down until the transport begins to rewind.
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EOT/BOT SENSOR CONTINUED

• After the tape has returned to the load point, press REWIND again. The tape should unload at slow speed.

• Remove the tape from the transport.

• Important!! Since the service switch must be in the NORMAL position for normal on-line operation, make sure that is where you left it.

PRELIMINARY REEL SERVO SETUP

ADJUST ONLY IF DRIVE DOES NOT FUNCTION PROPERLY.

• Put a piece of tape in the EOT/BOT Sensor and depress the LOAD button. The take-up and supply reel motors should rotate as long as the button is depressed.

• Hold the supply tension arm in the center of the available arc.

• Depress LOAD and ON LINE simultaneously. Verify that this causes the capstan motor to halt.

• With the supply motor turning and the arm held center, adjust the SUP CEN potentiometer to make the supply reel motor stop turning. See Figure 4 for the location of all pots and test points.

NOTE: The capstan must not be turning.

• Secure the supply arm so it cannot return to the rest position. A write ring looped around the arm roller and the nearby tape guide is a convenient way to accomplish this.

NOTE: If the supply arm is accidentally moved to either extreme of the operating arc, it will trip a microswitch which shuts down all motors. If this occurs, secure the supply arm, and press LOAD to start the motors. Then press LOAD and ON LINE to halt the capstan motor. The capstan motor must be halted to properly perform the preliminary arm centering adjustment.

• Check both extremes by doing the above.

• Hold the take-up tension arm in the center of its available arc and adjust the TU CEN pot to make the take-up reel motor stop turning.

• Load a scratch tape and press LOAD. The tape should advance to BOT and halt. If the arms move out of range and shut down the machine, adjust the TU ARC and/or SUP ARC pots counter-clockwise to keep the arms within the operating area (see Figure 4).
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PRELIMINARY REEL SERVO SETUP CONTINUED

- Run the machine forward and reverse with the service switch. If the arms move out of range, adjust the pots to keep the arms within the operating area. This is a preliminary adjustment. 
  NOTE: Use the punch marks on the front deck.

REEL SERVO ADJUSTMENT

- Load the transport with a small or practically empty reel of tape. Press LOAD. The tape should advance to BOT and halt.

- Run the machine forward and reverse with the service switch. Adjust the CEN and ARC pots for each arm to position them exactly at the locations indicated by the punch marks on deck. Make repeated small adjustments of the two pots, checking the forward and reverse position of the arm after each adjustment. The ARC pot is used to move the two arm positions apart, while the CEN pot shifts both positions in a common direction.

CAPSTAN SERVO ADJUSTMENT

The following adjustments must be performed after replacement of the capstan motor-tachometer or the MC-17 card.

- Connect a DVM to TP15. This point is indicated on the MC-17 card drawing, Figure 7. This figure shows all the test points and potentiometers used in this procedure.

**FIGURE 7**
CAPSTAN SERVO ADJUSTMENT CONTINUED

- Turn on the tape transport.
- Adjust the ZERO pot to get zero volts on the meter.
- Put the meter on TP5, on the MC-17 card.
- Forward speed. Load a SCRATCH TAPE on the machine. Press LOAD, and wait for the tape to halt at BOT.
- Place the service switch in the forward position. Adjust the FWD SP pot for the proper voltage on TP5, as per label next to motor.
- Repeat for reverse speed.
- Repeat for rewind speed.

Load a SKEW TAPE.
- Set the scope to (2V/cm, 5us/cm) TPO-7 or TPP (on PA-16, Figure 8) and measure speed for a 55.55 microsecond period to verify the adjustments.
- Repeat for reverse speed.
- Repeat for rewind, except verify for 16.67us. Set the scope to (10V/cm, 2us/cm).

FIGURE 8
DIGI-DATA 1730

READ SKEW MEASUREMENT AND ADJUSTMENT

- Following replacement of the tape head or any tape guiding component, check the transport skew. A master skew tape is required (800 cycles per inch).

- Set the scope for 1us/cm and 1V/cm.

- 1600 BPI - Connect the scope probe to skew TP9 on the PA-16 card.

- 800 BPI - Connect the scope probe to the right side of diode CR108, labeled STP on the RC-11 card. (See Fig. 15)

- With a master skew tape loaded, IPL the system and enter 00000d (d = device) on the CRT and TRANSMIT. A staircase wave-form (see Figure 9) should be displayed, with the first positive step triggering the oscilloscope sweep. The time between the first and last step represents the total read skew and should be less than 3.3 microseconds.

![Figure 9](image)

- If the skew exceeds the specification by a large amount, check the head mounting screws and head orientation. The direction of the skew may be determined by simultaneously observing TP4 and TP5 on an oscilloscope.

NOTE: ADJUST THE SKEW ONLY WHEN ABSOLUTELY POSITIVE IT IS REQUIRED.

- If the skew is excessive, determine which of the two tracks (4 or 5) arrives first.

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READ SKEW MEASUREMENT AND ADJUSTMENT CONTINUED

• 1600 BPI - (See Figure 8) To establish which track arrives first, position the scope probes as follows:
  CH1 - TP4 on the PA-16 board (Track 4).
  CH2 - TP5 on the PA-16 board (Track 5).

• 800 BPI - (See Figure 16) To establish which track arrives first, position the scope probes as follows:
  CH1 - The right hand side of R406 on the RC-11 board (Track 4).
  CH2 - The right hand side of R506 on the RC-11 board (Track 5).

• Correct the skew by inserting precision shims between the recorder front plate and one of the two spring loaded tape guides. If track 4 arrives first, shim the guide on the supply reel side of the head.

• If track 5 arrives first, shim the guide on the take-up side of the head. A shim thickness of 200 microinches will change the skew by approximately 15% of the specification. Shim only one guide. After installation of the shimming, check SKEW TP to ensure that the machine is now within specifications.

CONNECTION TO CONTROLLER AND ADJUSTMENT

NOTE: DO NOT PlUG OR UNPLUG ANY CABLE OR P.C. BOARD IN THE DRIVE FOR (1) MINUTE AFTER TURNING OFF THE POWER.

• Set the switches on the IOU-21A as shown on Page 3-42

• Plug in 4.608 MHz crystal and 45 IPS speed module into IOU-21. (Speed module has two .0027 microfarad caps and one 56 pico-farad cap).

3-42 5/82
DIGI-DATA 1730

CONNECTION TO CONTROLLER AND ADJUSTMENT CONTINUED

- Connect the cable to IOU-21.

![Connection Diagram]

Termination resistor modules inserted if this is the last drive in a daisy chain.

FIGURE 10

- Route the cables up through the base. Connect the other end of the cable (P.C. Board) into the back of the drive. Squeeze the "DEV 45" into the J101 connector on the RA-16 card (refer to Figure 6), and attach it to the chassis with screws.

![Routing Diagram]

If this is the last transport, insert resistor modules.

If another transport is used put on extension cable.

FIGURE 11

3-43 5/82
The flux gate is a hinged shield located directly in front of the tape head and provides magnetic shielding between the write and read gaps. Its adjustment should be checked following head replacement or if errors are occurring in the Read-After-Write mode. The drive must be operating and writing "burst ones" (16 chars.) patterns to properly check the flux gate position.

- Load a scratch tape.
- [IPL] 0010F2000ABd0000A7FFFF (F's to end of line)
  (d = DEVICE ADDRESS)
  [Transmit]
- Set the scope to (2ms/cm, 1V/cm)
- Put the scope on TPO. (Refer to Figure 8)
- Measure the cross talk. (See Figure 13)
- Repeat the above two procedures for TP1-7 and TPP.
When you lift the flux gate, the signal should increase (.8 - 1.9V).

If the waveform exceeds the specification, readjust the flux gate by loosening the two screws securing it to the front plate. Move it to obtain the smallest possible peak-to-peak signal. The shielding material must not touch the tape. The top of the flux gate rests against the edge of the head. Adjust the assembly so that it is parallel to the head, and as close as possible without interfering with the tape motion. Tune for minimum cross talk amplitude by shifting the assembly from side-to-side very slowly to locate the optimum position.

Then carefully tighten the two mounting screws and verify that the assembly is still at the optimum position.

RAMP ADJUSTMENT

With the test switch, move the tape forward a few feet.

[IPL]
0100F200000d1d82F0000A7
(d = DEVICE ADDRESS)
[Transmit]

The tape will vibrate and creep forward.

Set the scope.
Uncalibrate verticle and .5V/cm
TRIG - or + to see all parts of wave form.

NOTE: Adjust the verticle wave form height to (4) divisions.
(See Figure 14)
DIGI-DATA 1730

RAMP ADJUSTMENT CONTINUED

● Observe TP5 on the MC-17 card and adjust the ramp pot for 7.5ms ramp time.

![Figure 14]

READ AMPLIFIER GAIN ADJUSTMENT

● Load a scratch tape.

● Advance the tape to LOAD point.

● IPL the system and from device zero enter the following program:

```
0010F3010000010F01000B0006A7
(d = DEVICE)
[Transmit]
Enter - 32 F's
```

● Allow the tape to write for 30 to 60 seconds. This procedure creates an all ones tape.

● Rewind the tape back to the LOAD point.

● IPL the system and from device zero enter the following program:

```
0010F201000d000A7
(d = DEVICE)
[Transmit]
```

● 1600 BPI - Connect the scope probe to TPP and adjust the voltage with the respective pot. (Refer to Figure 8) Adjust for a maximum of 6 volts peak-to-peak.
READ AMPLIFIER GAIN ADJUSTMENT CONTINUED

- Repeat the above procedure for TPO-7.

- 800 BPI - Connect the scope probe to R106 on the RC-11 board and adjust the respective pot (R111). Adjust for a maximum of 6 volts peak-to-peak.

- Repeat the above procedure for TP R206 through TP R906, adjusting each respective pot as necessary. (See Figure 15)

NOTE: Check/adjust the EOT/BOT on the RC-11 board ONLY when the board has been replaced. See Page 3-16 for proper adjustment of EOT/BOT sensor.

CLEANING

Careful attention should be given to the performance of the daily cleaning instructions. Small particles of dust and oxide from the tape will cause data errors and possibly permanent damage to the magnetic tape.
DIGI-DATA 1730

CLEANING CONTINUED

Daily Cleaning of Head and Guides

Perform the following procedure after every eight hours of system use, or after every reel of tape if system use is infrequent.

- Remove tape from the transport.
- Moisten Q-Tip applicator, cotton swab, or any lint-free clean cloth with isopropyl (91%) alcohol.
- Lifting the flux gate clean the head(s), then the two tape guides.
- Clean the tape cleaner, the tension arm rollers, and the stationary rollers. (Excessive liquid is not required!)

Weekly Cleaning of Capstan

Clean the capstan outer surface weekly following this procedure:

- Moisten Q-Tip applicator, cotton swab, or cloth with water. USE ONLY WATER!!
- Rotate the capstan with one hand on the center shaft (not the outer surface, as it may be accidently deformed, while holding the cleaning implement with the other hand against the outer surface.
- Dry in a similar fashion.
- If the capstan outer surface shows signs of cracking or polish advise the maintenance engineer.

CHECK OUT ON THE SYSTEM

- Close the transport and tighten thumbscrews (hand tighten only).
- Run ATP Test
  Run Manual;
  INOP Test: (one time)
  Timing Test: (one time)
  End of Tape Test: (two times)
  Run Automatic Test: (10 iterations) (On full 10.5" reel)
  Run Automatic Test: (4 iterations) (On mostly empty 7.5" reel)
PARTS REPLACEMENT

Tension Arm Roller

The current practice of replacing the entire tension arm assembly when the arm roller malfunctions is unnecessary and can possibly create skew problems.

The part number for the arm roller assembly is: 4110051-0001. To remove/replace a binding or noisy arm roller see Figure 16 and perform the following steps.

• Remove the 4-40 screw from the center of the arm roller.

• Pull off the arm roller; watch for any shims.

• Slip on the replacement roller. Insert and tighten the screw, check height.

• Load the tape and use the service switch to verify proper operation.

FIGURE 16
POWER SUPPLY
The transport has four regulated voltages, two of which are adjustable. Max AC ripple is 50 mV p-p.

1) Connect voltmeter between TP10 (grid) & TP5 (+5) on the MC18 card. Adjust R62 to obtain +5 ± 0.05

2) Adjust potentiometer R52 to obtain +12V ± 0.05 between TP10 & TP2 (+12). See fig 20.

3) Verify that the voltage at TP3 is +5 ± 0.1 and that the voltage at TP6 is -12 ± 0.6.

CAPSTAN SERVO ADJUSTMENT
These adjustments determine the transport's tape speed and tape acceleration.

ZERO ADJUSTMENT
Load a reel of magnetic tape to BOT. With no tape motion, adjust R46 (ZERO) to achieve a reading of 0.00V ± 0.01 between TP7 and TP10 of MC18.

SPEED ADJUSTMENT
The transport's forward and reverse speeds are each adjustable with a unique potentiometer. The rewind speed, since it is directly proportional to the reverse speed, requires no adjustment. THESE ADJUSTMENTS SHOULD BE PERFORMED WHEN REPLACING THE MOTOR CONTROL CARD, MC18.

1) Locate the round label affixed to the capstan tachometer. This label indicates the voltage readings at TP1 (tachometer feedback) when the factory technician properly adjusted the forward and reverse speeds at the time of manufacture.

2) Connect an accurate voltmeter between TP10 (GND) and TP1 of MC18.

3) Move tape forward using the service switch. Adjust R48 to duplicate the voltage preceded by an "F" on the capstan label.

4) Switch tape motion to reverse, using service switch, and adjust R49 to duplicate the voltage preceded by an "R" on the capstan label.

Note: The service switch ignores the BOT marker.
RAMP ADJUSTMENT

This procedure requires the use of an oscilloscope.

1) Load a reel of scratch tape to BOT and place the transport on line.

![RAMP TIMING WAVEFORM](image)

2) IPL the system and from device zero enter the following program:

```plaintext
0100F20000Bd 1d029D 0000A7 [XMIT]
```

3) Trigger the scope negative on TP12 of the MC18 card and view TP1.

4) Uncalibrate the scope amplitude, and position the trace such that the 90% amplitude point of the start ramp may be easily ascertained. See figure 21.

5) Adjust the RAMP potentiometer, R45, to achieve a ramp time of 7.5 milliseconds as shown in the figure.

6) Trigger the scope positive and view the stop ramp. Verify that it is approximately equal in time to the start-ramp time.

Note: The tape should be creeping forward.

PHOTODISK ALIGNMENT

1) Power up, but do not load tape.

2) Connect a voltmeter between ground and TP16 (supply photocell) or TP15 (take-up photocell) on the MC18 card.

3) With your hand hold the appropriate arm in the approximate center of its operating arc.
4) The voltmeter should read 2.7V±0.1. If not loosen the photodisk and rotate it until the meter does read 2.7V.

ARM LIMIT SWITCH ALIGNMENT
Photo disk alignment must be checked before performing this adjustment. Refer to figure 22 with regard to this alignment.

1) Insure that the screws holding the microswitch in place are tight. Power off the unit.

2) Connect an ohmmeter between the two connected lugs of the switch.

3) Lift the take-up arm off its stop. The meter should go from "short" to "open" before the first dimple (punch on deck plate) is reached. Release the arm, the meter should return to "short".

4) Move the take-up arm to the opposite end of its arc. The meter should go from "open" to "short" after the second dimple is passed. Bring the arm back again and the meter should indicate "open" before the same dimple is reached.

5) If the conditions indicated in steps 3 and 4 do not exist, loosen and move the appropriate arm limit reactor(s) on the take-up photodisk to achieve those conditions. DO NOT move the entire photo disc.

ARM POSITION ADJUSTMENTS

1) Load the transport with a scratch tape. The tape should advance to BOT and halt.
2) Run the machine forward and reverse with the service switch. Adjust the CEN and ARC pots for each arm to position them exactly at the locations indicated by the punch marks on the deck-plate. Make repeated small adjustments of the two pots, checking the forward and reverse position of the arm after each adjustment. The ARC pot is used to move the two arm positions apart, the CEN pot shifts both positions in a common direction.

**BOT/EOT SENSOR ADJUSTMENTS**

These adjustments are required whenever either the write/control card or the BOT/EOT sensor assembly is replaced. Only a voltmeter is needed.

1) Power up and wait at least one minute for warm-up before performing the voltage measurements indicated in the following steps.

2) Thread magnetic tape on the unit and turn the reels so that the tape is taut through the BOT/EOT sensor. Insure that neither the BOT nor the EOT reflective tab is located within the sensor assembly.

3) Connect the voltmeter between ground and TP15 on the write/control card and adjust R81, the BOT potentiometer, so that the meter reads at least +9 volts.

4) Connect the voltmeter between ground and TP16 on the write/control card and adjust R82, the EOT potentiometer, so that the meter reads at least +9 volts.

5) Remove the magnetic tape from within the sensor assembly. The voltages at TP15 and TP16 must drop below +1 volt.

**FILE PROTECT ALIGNMENT**

The point of depression at which the ring-detecting plunger actuates the RING IN microswitch must be set when the file-protect assembly is replaced.

1) Power off and no tape loaded.

2) Connect an ohmmeter between the two connected lugs of the RING IN (WRT EN) microswitch. The meter should indicate an "open".

3) Depress the plunger which protrudes through the deck-plate. The meter should indicate a "short" when the plunger reaches the inner lip of the supply hub.
4) If necessary loosen the set screw in the plunger cam and move the cam on the plunger so that the condition described in step 3 is obtained.

5) Disconnect the meter. Power up, load a reel of tape with a write enable ring installed and verify that the solenoid retracts the plunger fully when the LOAD pushbutton is pressed, and that the WRT EN indicator lamp is illuminated.

READ GAIN ADJUSTMENTS
The gain of the read pre-amps must be adjusted when the read card or the magnetic tape head is replaced. In making these adjustments a reliable scratch tape should be used. DO NOT use a master alignment tape (skew tape).

![Read Gain Waveform](image)

**Read Gain Waveform**
Fig 24

PE READ GAIN (1600 BPI)
Any transport capable of reading phase encoded tapes should be adjusted by means of the following procedure.

1) Write an all ones tape using the program shown on page 3-063.

2) After writing and rewinding the tape to load point, IPL the system and enter the program for reading an all ones tape.

3) Tape should rewind automatically and continue running to allow you to make the following adjustments without continually checking the front of the drive to see if tape is about to run out.

4) View TP102 with an oscilloscope and adjust potentiometer R108 to obtain a 6 volt peak-to-peak signal. See fig 25 for location of test points and pots.

Pg 3-057
5) Repeat step 2 for each of the remaining eight channels (TP202 and R208, TP302 and R308, etc.).

**NRZI READ GAIN**

To adjust the read amplifier gains on a transport capable of reading ONLY NRZI tapes, perform the following procedure:

1) Load a scratch tape on the drive and place the unit ON LINE.

2) IPL the system and from device zero enter the program to write an all ones tape. Refer to program on page 3-063.

3) After writing and rewinding the tape to load point, IPL the system and enter the program for reading an all ones tape. Refer to page 3-063.

4) Tape should rewind automatically and continue running to allow you to make the following adjustments without continually checking the front of the drive to see if tape is about to run out.

5) View TP101 of NRZI read card with an oscilloscope and adjust potentiometer R108 to obtain a 10 volt peak-to-peak signal. Refer to figs 24 & 25

6) Repeat step 2 for each of the remaining channels (TP201 and R208, TP301 and R308, etc.).

**READ SKEW MEASUREMENT AND ADJUSTMENT**

Following replacement of the tape head or any tape guiding component, check the transport skew. A master skew tape and an oscilloscope are required.

1) Set the scope for 1us/cm sweep time and 1V/cm vertical deflection.

1600 BPI - Connect the scope probe to TP9 (skew) on the PE read card.

800 BPI - Connect the scope probe to the anode side of diode CR109 on the NRZI read card.

2) With a master skew tape loaded, IPL the system and enter 1000d0000A7 (d-device) on device zero and TRANSMIT. A staircase wave-form (see fig 26) should appear, with the first positive step triggering the oscilloscope sweep. The time between the first and last steps represents the total read skew and should be less than 3.3 microseconds.
NOTE: When you reach the end of the skew tape you should reverse the tape at slow speed to keep from stretching it. The program to reverse the tape is this: 1d029d 0000a7. Do not REWIND skew tape.

3.3 MICROSECOND MAX.

JITTER

Skew waveform
Fig 26

5) If the skew exceeds the specification by more than 1 microsecond check the head mounting screws and head orientation.

NOTE: ADJUST THE SKREW ONLY WHEN ABSOLUTELY POSITIVE IT'S NECESSARY.

4) If the skew is excessive, determine which of the two tracks (4 or 5) arrives first.

5) Before concluding that one of the tape guides needs to be shimmed make the following checks.
   a) Verify that the tape has been properly threaded.
   b) Clean all tape-handling components.
   c) Check each tension arm roller for axial play. The roller should not move up and down. Horizontal tilt is normal. If either roller exhibits vertical play, remove and replace it. Arm roller height is adjustable with the center screw.
   d) Check the stationary rollers for the absence of vertical play, and replace either if necessary. Stationary roller height is also adjustable with the center screw.
e) Check the tape guide springs for freedom of movement. When depressed and then released the bottom flange should snap up to its original position. If the flange's movement feels spongy or sticks, remove, disassemble, and clean the tape guide. Recommended solvents are heptane and alcohol.

6) If none of the above is the cause of the skew problem then tape guide shims must be added or removed. A small label on the rear of the front plate near the head indicates which guide, if either, was shimmed at the time of manufacture and how much it was shimmed. To determine which guide to shim perform the following. Press lightly on the outside edge of the tape near each tape guide in turn. The guide where the skew is worsened by this pressing is the one which must be raised.

NRZI SKEW GATE ADJUSTMENT (800 bpi)
This adjustment is required whenever the read card is replaced on any transport capable of reading NRZI tapes. The SKEW GATE is a one-shot which defines the time during which all the data bits in a character must arrive; it is triggered by the first bit to arrive. Nine-track ANSI standards require that all bits arrive within 34% of the character period.

1) Read any tape. View TP6 on the NRZI-read card with an oscilloscope.

2) Adjust the width of the skew gate (TP6) wave-form with potentiometer R27 to achieve a 10us signal width.

CROSS-FEED MINIMIZATION
If errors are occurring in the read-after-write mode, or if a dual gap head is replaced, some adjustment of the "flux gate" may be necessary to minimize the cross-feed between the write and read stacks.

1) Verify that the gain of each read pre-amplifier is properly adjusted; refer to pages 3-063 of this section.

2) Note the position of the density select plug jumper located next to the service switch on the write/control card. If this jumper is not in W19 then move it from W20 or W21 to W19 for the duration of this adjustment. Refer to fig 23 for location of W19, W20 & W21.

3) Turn power on, and thread a scratch tape on the transport without going around the capstan.

4) Refer to page 3-063 to write all "ones" tape at 800 BPI or a 3200 FRPI tape 1600 BPI. Press the LOAD pushbutton switch and the reel motors will apply tension to the tape and the capstan.

Pg 3-061
will rotate CCW. Press LOAD and ON LINE simultaneously to simulate BOT detection; the capstan will stop and then start forward again as the pushbuttons are released. The write circuits are now active. However, since the tape is not moving the read circuits cannot really read-after-write, and therefore, any activity in the read channels is cross-feed from the write circuits.

5) With an oscilloscope view the pre-amplifier output TP101 of the Read Card. The peak-to-peak signal must not exceed .5 Volts P-P. Refer to Fig 27.

6) If signal amplitude exceeds the specification in step 5 loosen the two screws which hold the "flux gate" assembly to the plate. Move the assembly slowly from side to side noting the position which results in the smallest peak-to-peak cross-feed signal. The assembly should be parallel to the head and as close as possible to it without touching the tape.

7) When the optimum position for minimum cross-feed has been determined, tighten the two mounting screws; then verify that the assembly has not shifted while tightening the screws. View also TP201 through TP901 and verify that these signals are within the maximum values indicated in step 5.

8) Turn off power, remove the tape, and return the density select plug jumper to W20 or W21 if previously moved in step 2.
**** HELPFUL PROGRAMS ****

WRITING OF ALL ONES TAPE

wait for most of tape to be written ... then ....

READING ALL ONES TAPE

This procedure will read the tape looking for End Of File marks. Upon finding one, the tape will rewind and begin reading again until stopped by the IPL button.
IOU 28 CRT
Qantel Model 4011, 4021

IOU-Z8A

1  2  4  8
ADDRESS SWITCHES 0-F

IOU-Z8B

KANA  9600
ASCII 1200

MAX. PWR. REQUIREMENTS
-12v = .50A
+12v = .10A
+ 5v = 2.69A
TERMINAL PWR.
115 vac 60 Hz
2.0A for .1 sec.
1.0A normal

NOTE:
SELECT PROPER BAUD RATE ON THE I/O CARD IN THE TERMINAL ALSO.
IOU-28

CRT

Qantel Model 4011, 4021

(QCRT 2) (BLACK AND GREEN SCREEN)

WRITE CONTROL INSTRUCTIONS

1XYY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

FUNCTION

00  RESET INTERRUPT
10  RESET ALLOW TERMINATION INTERRUPT
14  SET ALLOW TERMINATION INTERRUPT
2N  POLL TERMINAL "N"
30  START SCANNING (BEGIN WITH NEXT SEQUENTIAL TERMINAL AFTER STOP).
50  SEND MEMORY CONTENTS ON NEXT READ INSTRUCTION

STATUS-IN INSTRUCTIONS

4XYY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

STATUS

01  READ BUSY
02  WRITE BUSY
04  END
08  SERVICE REQUEST
10  INTERRUPT (FLAG 1)
20  FLAG 2
40  FLAG 3
80  Slave printer inop

READ STATUS 2

YXY86  X = DEVICE ADDRESS  Y = DON'T CARE

THE READ STATUS 2 INSTRUCTION HAS NO MEANING IN THE TYPEWRITER MODE ON THE QCRT. IN DIRECT ACCESS MODE, THE STATUS 2 BYTE HAS THE FOLLOWING MEANINGS:

STATUS

0N  SCAN  ALL 16 TERMINALS TIMED OUT
1N  SCAN  READ REQUEST
4N  READ  READ COMPLETE
6N  READ  READ OVERFLOW
7N  READ  ERROR OR TIMEOUT
8N  WRITE  WRITE COMPLETE
BN  WRITE  ERROR OR TIMEOUT
CN  POLL  POLL COMPLETE
DN  POLL  READ REQUEST
EN  POLL  TERMINAL BUSY
FN  POLL  ERROR OR TIMEOUT

N = TERMINAL NUMBER

Revised 3/77
-A- LOCAL / REMOTE
SELECT REMOTE FOR MODEM USE
ALSO 1200 BAUD
BLANK ILLUMINATES DISPLAY OF MEMORY POSITIONS WITH SUPPRESS BIT PRESENT
KANA SET TO KANA FOR JAPANESE CHARACTER GENERATOR.
1200/9600 SELECT TRANSMISSION RATE

-B- STANDARD ADDRESS SWITCHES 0-F

-C- HALT & STEP
USED TO HALT EXECUTION OF ROM INSTRUCTIONS AND STEP SINGLE INSTRUCTION AT A TIME.

NOTE:
CRT-1 HAS TWO-LINE, NON-BLINKING CURSOR
I/O

- A - BLANK illuminates display of memory positions with suppress bit present.
1200/9600 transmission rate selection.
Local/remote select remote for modem use.
Also 1200 baud

- B - Standard address switches 0-F

- C - HALT & STEP halts ROM execution and allows single stepping of ROM instructions.
50-60 Hz power source selection
15-27 lines screen size select.

NOTE:
CRT-2 has single-line, blinking cursor.

4-4
IOU-39T

TYPICAL POWER REQUIREMENTS

\[ \begin{align*}
-12V & = .100A \\
+12V & = .150A \\
+5V & = 1.5A
\end{align*} \]

CONFIGURATION SWITCHES

\$1, \$2, \$4, and \$8 should be set to the highest terminal address connected in the network. The system terminal addresses should be sequential (beginning with \(0\)). This saves time by limiting controller scanning to only the terminal addresses connected. If the configuration switches are set for the highest terminal address of \(0\), the controller scans all the 16 possible terminal addresses when in scan mode.

\$10 is not used and should be set to the \(0\) position.

<table>
<thead>
<tr>
<th>$40</th>
<th>$20</th>
<th>Delay</th>
<th>Installation Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>OFF</td>
<td>OFF</td>
<td>15ms.</td>
<td>Local installations operating at 19,200 or 9600 baud.</td>
</tr>
<tr>
<td>OFF</td>
<td>ON</td>
<td>50ms.</td>
<td>Most installations where modems are employed (4800, 2400, 1200 baud).</td>
</tr>
<tr>
<td>ON</td>
<td>OFF</td>
<td>150ms.</td>
<td>If low speed modems are used (600, 300 baud) and in some cases where higher speed modems are used with limited Telco facilities that impose a high propagation delay.</td>
</tr>
<tr>
<td>ON</td>
<td>ON</td>
<td>500ms.</td>
<td>Should only be used for modem links where severe propagation delays occur.</td>
</tr>
</tbody>
</table>

4-4A
IOU-39T

WRITE CONTROL INSTRUCTIONS

1dyy90  d = Device Address  yy = Control Byte

STATUS BYTE:  FUNCTION
$00  Reset Interrupt
$10  Reset Allow Termination Interrupt
$14  Set Allow Termination Interrupt
$2N  Poll Terminal 'N'
$30  Start Scan
$40  Reset Print Complete
$50  Send Memory
$60  Diagnostic Write

STATUS INSTRUCTIONS

4dyy90  d = Device Address  yy = Test Bit Value

STATUS BYTE:  FUNCTION
$80  IPL Mode : CRT Ø INOP
       SCAN Mode: Video Printer INOP
$40  FLAG 3
$20  FLAG 2
$10  Termination (Interrupt)
$08  Service Request
$04  End
$02  Write Busy
$01  Read Busy

4-4B
IOU-39T

READ STATUS 2

xdyy86  d = Device Address     yy = Test Bit Value
             x = Don't Care

CONTROL BYTE FUNCTION

$AO  After IPL or Reset I/O: Mode of Operation
$1N  SCAN: Read Request (On CRT indicated: N)
$2N  SCAN: Print Complete
$3N  SCAN: Read Request & Print Complete
$4N  READ: Complete
$6N  READ: Overflow
$7N  READ: Timeout/Error
$8N  WRITE: Complete
$8N  WRITE: Error
$CN  POLL: Complete
$DN  POLL: Read Request
$EN  POLL: Printer Busy
$FN  POLL: Error

4-4C
SAFETY NOTES

The internal components of the CRT terminal contain some voltages that, if contacted, could cause severe discomfort or personal injury. These circuits are well protected with insulation when the unit leaves the factory, but later damage or disassembly of a live unit could expose a hazardous condition.

Always remove all power (unplug the power cord) when disassembling a portion of the terminal. Inspect the entire unit for damage to protective insulation, and replace or repair any wiring or connectors that are damaged.

The picture tube of the monitor unit is protected from damage on the face by a bonded safety shield. Never replace the tube with a variety that does not contain this shield. Use care when the cover is off the unit to avoid damage to the glass. A scratch or sharp blow can sometimes cause a tube to implode. In effect, an implosion resembles a small explosion, sending glass particles in all directions.
DISASSEMBLY

TOP COVER

- Remove the two cover screws under the rear ledge of the cover assembly (see the illustration).

- Grasp the cover just above the chassis level, and slide it about 2 inches or more toward the rear (away from the tube face).

- Press the sides inward at the front edges, releasing the retainers, then lift the cover straight up and away from the unit. Set the cover in a safe place where it will not be damaged or fall and damage something else.

REMOVE PRINTED CIRCUIT CARDS

- Remove power from the unit by switching the power switch OFF or unplugging the line cord.

- Remove the jumper board at the rear of the printed circuit cards (near the power supply fan). This jumper is best removed by gently pulling alternately at the top and bottom. Place the jumper card where it cannot be damaged or the contacts become dirty.

- Remove the desired printed circuit card by lifting gently on the loose end of the card, then pulling toward the rear of the unit.
REMOVE MOTHERBOARD

• Remove all of the printed circuit cards.
• Disconnect J5, allowing it to lay to one side.
• Remove the four pan-head screws that hold the motherboard.
• Lift the motherboard from the unit.

REMOVE THE LOGIC POWER SUPPLY

• Remove all of the printed circuit cards.
• Disconnect P13 and P13A (the two smaller connectors, one at the side and one toward the rear of the unit).
• Remove the four retaining screws for the power supply at the bottom of the unit. To remove these screws, slide the power supply and of the terminal over the end of the workbench or table until the screw heads can be reached. It is not recommended that the unit be tilted up to reach the screws, as the power supply may fall into the monitor when the screws are released.
REMOVE THE MONITOR ASSEMBLY

• Tip the terminal up so that the two retaining screws that are near the bottom center of the unit can be removed. Remove these two screws only, then set the terminal back down.

• Slide the rear edge of the terminal to the edge of the table or workbench so that the rear panel assembly extends over the edge.

• Remove the two screws at each side of the rear panel assembly (on the rear surface of the terminal).

• Allow the rear panel assembly to move down and to the rear (out of the way).

• Remove the two remaining monitor mounting screws that were exposed by moving the rear panel assembly.

• Unplug all electrical connections to the monitor assembly.

• Lift the monitor assembly up and back (away from the bezel).
REMOVE THE MONITOR HIGH VOLTAGE ASSEMBLY

• Loosen the monitor assembly as if it were being removed. It will not be necessary to disconnect the internal electrical connections.

• Lifting the monitor assembly up, remove the two nuts at the bottom of the chassis (under the high voltage assembly).

• Set the monitor assembly back down.

• Unplug the two low voltage connectors (P104 and P105) from the high voltage module.

• Disconnect the high voltage lead from the tube (the one that goes into the top of the tube).

--- WARNING ---

When disconnecting the high voltage lead, grasp only the insulated portion of the connection. Under some conditions, the picture tube will retain a charge in the same way as a capacitor.
REMOVE THE KEYBOARD ASSEMBLY

- Slide the keyboard portion of the terminal partially over the edge of the table or workbench.

- Remove the protective panel from below the keyboard by removing the two screws that hold the front rubber feet and the panel.

--- CAUTION ---

The speaker wires will allow only about 6" of panel movement. Disconnect P5 (speaker wires) if the panel is to be moved farther.

- Loosen and remove the four screws that fasten the keyboard assembly to the main frame.

- Disconnect the small connector to the speaker (P5) and the larger connector at the edge of the keyboard assembly.

REPLACE THE INDICATOR LIGHT

- Remove the motherboard.

- Remove the indicator lights by carefully unplugging the LED unit from the motherboard. Be sure, when replacing the indicator, that the LED contact marked + is inserted into the + socket on the motherboard.
CR2 (GREEN CHARACTER DISPLAY)
<table>
<thead>
<tr>
<th>SWITCH</th>
<th>OPERATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>HALT</td>
<td>Test switch that is used for troubleshooting. It stops the normal logic</td>
</tr>
<tr>
<td></td>
<td>processing within the CRT. Logic sequences can be stepped through one</td>
</tr>
<tr>
<td></td>
<td>micro-instruction at a time by using the STEP switch.</td>
</tr>
<tr>
<td>STEP</td>
<td>Test switch that is used in conjunction with the HALT switch. When the</td>
</tr>
<tr>
<td></td>
<td>HALT switch is in the HALT position, move the the STEP position and then</td>
</tr>
<tr>
<td></td>
<td>return to step the microprogram one instruction at a time.</td>
</tr>
<tr>
<td>50/60 CY</td>
<td>Changes the internal timing to be compatible with the power line frequency.</td>
</tr>
<tr>
<td>15/27 LINES</td>
<td>Allows full screen display of 27 lines or limits the logic to 15 lines of</td>
</tr>
<tr>
<td></td>
<td>display and memory (the unused memory is physically removed in the 15-line</td>
</tr>
<tr>
<td></td>
<td>units).</td>
</tr>
<tr>
<td>ADDRESS</td>
<td>These switches set the terminal address as seen by the controller (not the</td>
</tr>
<tr>
<td></td>
<td>IOU device address as seen by the CPU). The subordinate order is reversed</td>
</tr>
<tr>
<td></td>
<td>from one model to the other but up is always ON. The illustration shows</td>
</tr>
<tr>
<td></td>
<td>the terminal address as hexadecimal F.</td>
</tr>
<tr>
<td><strong>BLANK</strong></td>
<td>When the switch is set to the BLANK position (as shown in the illustration), all characters that contain a blanking bit will not be displayed on the screen. When set away from the BLANK position, all characters except space characters will be displayed. Invalid characters, such as control characters, are displayed as blanks. Blanks that are used to fill a suppressed background field are displayed when the switch is away from the BLANK position.</td>
</tr>
<tr>
<td><strong>BAUD</strong></td>
<td>Sets the baud rate of the CRT input and output data lines. If the LOCAL/REMOTE switch is in the REMOTE position, the baud rate must be set at 1200. LOCAL baud rate can be 1200 but preferably should be 9600. All terminals that are connected to the same IOU controller (either directly or through modems) must be set to the same baud rate.</td>
</tr>
<tr>
<td><strong>LOCAL/REMOTE</strong></td>
<td>Changes the I/O characteristics of the CRT unit. The LOCAL position is used where all units of one controller are directly attached through cables (within a maximum of 2000 feet). REMOTE must be used on all terminals of a controller that has one or more terminals that are remote enough to require a modem. When the REMOTE setting is used, the baud rate for all terminals that are connected to the same controller must be set to 1200.</td>
</tr>
<tr>
<td><strong>AUXMODE</strong></td>
<td>Enables an auxiliary ROM that is programmed for the Katakana keyboard. This ROM is not present in an ASCII terminal. However, if the AUXMODE switch is enabled in an ASCII terminal, certain Greek letters and some special symbols will be displayed when the LTRS key is in the down position. Releasing the LTRS key will not automatically release the condition; either SHIFT or RETURN must be depressed to exit the special character condition. The AUXMODE switch should normally be OFF in an ASCII terminal.</td>
</tr>
</tbody>
</table>
ADJUSTMENTS

This section describes adjustments that are normally only performed by field engineering personnel. External operator adjustments are described in the OPERATION section of this manual. All adjustments within this section require that the CRT has power, the cover is off and is not online. These steps are not repeated for each procedure. Each adjustment is complete and independent in itself, but many adjustments are either directly or subtly interactive and may require that another adjustment be repeated. Those situations that are clearly interactive are indicated in the adjustment procedure.

+15 V ADJUSTMENT

Power supply voltage is extremely important for proper display monitor operation. Always check the power supply voltage and low/high load regulation when random errors or failures are indicated. Use a good quality 20,000 Ω/volt meter (or better) to measure the voltage when adjusting. Make the +15 volt adjustment as follows:

- Attach the measuring probes at P103 (see the illustration for location of P103). The positive probe connects to the orange wire; the return connects to the black wire.

- Adjust R208 (on the small PC board behind the monitor power supply) until the measured voltage is as close as possible to 15.00 volts.
VERTICAL FREQUENCY

The vertical frequency is the synchronism that keeps the screen from "rolling." Center the adjustment as follows:

- Slowly move the control (R16) in one direction until the screen begins to roll. Note the position of the control.
- Slowly move the control in the opposite direction until the screen begins to roll in the opposite direction. Note the position of the control.
- Return the control to the midpoint between the two extremes.
- Check the vertical linearity; this adjustment may have affected it.

TILT (LEVEL)

If the top and bottom lines of the display are not level with the bezel (facemask), adjust as follows:

- Slightly loosen the yoke clamp screw (on the neck of the tube). Loosen only as much as necessary to allow movement of the yoke yet retain a firm "drag."
- Rotate the entire yoke assembly slightly until the top and bottom of the display are level.
- Carefully re-tighten the clamp screw until the yoke is again secure. Do not over tighten, as the tube can be broken.
- It may be necessary to adjust centering when tilt has been corrected.
VIDEO DRIVE

The video drive adjustment is located at the rear edge of the I/O card. Proper adjustment is essential for clear, undistorted character display. The control is at the top of the card, slightly above the rear jumper board connector. On the older white character display models, the adjustment is located on the rear of the I/O card but is below the jumper board connector. The adjustment requires only a small screwdriver and is performed as follows:

- Disconnect the I/O cable at the rear of the terminal.
- Enter several groups of semicolons (;) at various places on the screen. Be sure there are some at the top, mid screen, and the bottom.
- For CRT 2, use a short jumper wire, touching momentarily from +5V to pin 11 on chip 2A.
- For CRT 1, momentarily touch from pin 2 of chip 2 to ground (5V return).
- When the jumper is touched, the screen display will reverse to a background display; the semicolons will remain as background displays.
- Adjust the video drive (on the I/O board) clockwise until the character intensity becomes bright enough to cause a ghost or shadow beside the character elements.
- Move the adjustment counterclockwise until the shadow disappears and the character is clear. If the adjustment is moved too far in a counterclockwise direction, the characters will fade into the background.
- Clear the screen by simultaneously depressing SHIFT and F2.
HEIGHT AND VERTICAL LINEARITY

These two adjustments are so interdependent that neither can be adjusted without affecting the other. Make sure the vertical frequency is electrically centered (as described in a previous paragraph) before adjusting the vertical linearity, as the vertical frequency being off has a tendency to "squeeze" the screen.

- Fill the screen with upper case letters. Hs, Ms, or Ws are best, because they fill the character space, making it easier to see linearity problems.

- Adjust the height control (R124) until the display fills the screen. If either the top or bottom has a larger space, re-center the display.

- Adjust the linearity control (R121) until the letters are the same height on all lines. The total display height will increase or decrease as linearity is adjusted; ignore this effect temporarily.

- Re-adjust the height until the display again fits the vertical space on the screen.

- It may be necessary to alternate between linearity and height, making smaller and smaller adjustments each time.

CENTERING

To center the display on the screen, rotate the two tabs that are located on the deflection yoke until the display is centered. Each tab moves the display at an angle as compared to vertical or horizontal. By moving one, then the other, and both in conjunction, the display can be centered. After the display is centered, the focus, width, and height may need adjustment.
NULL ADJUSTMENT (IOU-28B)

This adjustment is made at the factory and normally will not require attention. However, if a terminal-to-IOU (one way) communication problem is encountered, the null should be checked. Measure with the most sensitive scale of an oscilloscope or the best VOM that is available. Do not make any adjustment if there is no deflection when the test points are contacted. The measurement and adjustment (if necessary) should be made after the IOU-28 has stabilized to the normal system operating temperature for the system in which it will operate. Proceed as follows to measure and/or adjust.

- Set the measuring device to the lowest (most sensitive) dc scale.

- Touch the positive and return test leads to each other, and carefully note the zero indication. If an oscilloscope is used, use both channels. Set to A + B, and move the display to center screen.

- Attach or touch the leads to the points shown in the illustration below. Polarity of the leads is not important. There should be no deflection in either direction when these points are measured with the IOU-28 under powered up conditions.
• Adjust R50 if necessary to make the test points exactly zero in reference to each other. The adjustment will cause a voltage swing to positive or negative; null to zero as nearly as possible.
FOCUS

Before adjusting the focus control, adjust the BRIGHTness control at the operator level that it will normally be set.

- Adjust the focus control (R107) for the best character definition over the entire screen. Sometimes a portion of the screen focuses at a different adjustment than the rest. In that case, the adjustment is a compromise for the best overall effect.

If the monitor tube has a second set of tabs like those that are used for centering, the overall focus can be adjusted to a better degree. These tabs will be located toward the connector end of the tube.
- Rotate one tab, then the other, then both in conjunction, while watching the screen for the focus across the entire screen.

WIDTH

The width coil can only be adjusted by using a .100 inch hex tool that is made of plastic. This size tool is a common standard for TV adjustments and can be purchased at most electronic parts stores. Never attempt this adjustment using a screwdriver; the powered iron slug will break, making it necessary to replace the entire assembly. Metal, such as an Allen wrench, affects the electrical field, making it impossible to properly adjust the width.

- Using a .100 inch plastic hex tool, adjust the width of the display until it is within a small margin of both edges. The adjustment is made by rotating the threaded slug inside the coil, making it move in or out of the coil area.

- If one edge of the display is closer than the other, re-center the display.
VIDEO TERMINAL

<table>
<thead>
<tr>
<th>Column</th>
<th>000</th>
<th>000</th>
<th>001</th>
<th>010</th>
<th>011</th>
<th>100</th>
<th>101</th>
<th>110</th>
<th>111</th>
</tr>
</thead>
<tbody>
<tr>
<td>Raw 2</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>b_4 b_3 b_2 b_1</td>
<td>NULL</td>
<td>SET CURSOR</td>
<td>SEND TEXT</td>
<td>ETA</td>
<td>ESCAPE</td>
<td>CLEAR SCREEN</td>
<td>CLEAR</td>
<td>F'G'ND</td>
<td>SOUND</td>
</tr>
<tr>
<td>00000</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
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<td>0</td>
<td>0</td>
<td>0</td>
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<tr>
<td>101010</td>
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<tr>
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<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>110113</td>
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<td>111014</td>
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<tr>
<td>111115</td>
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<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

- **Null**: Space
- **Set Cursor**: Vertical movement
- **Send Text**: Placement
- **Clear Screen**: Clear display
- **Clear**: Clear contents
- **Figure and Follows**: Figure or follow
- **Roll Up**: Move up
- **Roll Down**: Move down
- **Print**: Print character
- **Print Message Follows**: Print message
- **Print Screen**: Print screen
VT3 Hardware Installation

1. Install IOU39Q's (for VT3) per customer installation specs.
2. Set IOU39Q switches per page 4-25.
3. Install cables, line drivers, hub units, terminals and terminators per pages 4-26 thru 4-30.
4. Set switches on VT3 Logic boards per page 4-31.
5. Check VT3 power supply per page 4-33.
6. Check/perform VT3 monitor adjustments per pages 4-33 thru 4-37
8. Run CRT3 and ACRT3 ATP's

Remember that a VT3 is a Type 4 terminal when running the CFIG program.
IOU - 39Q SWITCH SETTINGS FOR VT3

**ION**

Set to a one in all IOU39Q - VT3 configurations

- **$80**

**Terminal response**

Time-out delay.

*Time-out for non-modem installation*

<table>
<thead>
<tr>
<th>$40</th>
<th>$20</th>
<th>TIMEOUT</th>
<th>TYPICAL BAUD RATE</th>
</tr>
</thead>
<tbody>
<tr>
<td><em>G</em></td>
<td><em>G</em></td>
<td>15 msec</td>
<td>19,200 - 3,600</td>
</tr>
<tr>
<td><em>G</em></td>
<td>1</td>
<td>50 msec</td>
<td>4,800 - 8,000</td>
</tr>
<tr>
<td>1</td>
<td><em>B</em></td>
<td>150 msec</td>
<td>1,200 - 500</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>500 msec</td>
<td>300</td>
</tr>
</tbody>
</table>

- **S08**

Switches should be set to the highest terminal address connected in the network. The system terminal addresses should be sequential (beginning with 0).

- **S04, S02, S01**

- Address switches should be set to the highest terminal address connected in the network.
VT3

IOU-39Q Switch Settings for VT3 continued

<table>
<thead>
<tr>
<th>ROM</th>
<th>ROM</th>
<th>ROM</th>
<th>ROM</th>
</tr>
</thead>
<tbody>
<tr>
<td>39Q-14y</td>
<td>39Q-13y</td>
<td>39Q-12y</td>
<td>39Q-11y</td>
</tr>
</tbody>
</table>

y = Current Rev Level

I/O Cable Part Numbers

- **On 9xx/14xx Systems:**
  - IOU to Hub Unit, Line Driver, VT3, Modem, IOU 40, RDI Printer, or VT3 to VT3
    - RS232/V24 cable:
      - 6 ft. 42055-1
      - 12 ft. 42055-2
      - 25 ft. 42055-3
      - 50 ft. 42055-4
  - **On 2xx/3xx Systems:**
    - IOU to Hub Unit, Line Driver, Modem or IOU 40
      - EIA/4F cable
        - 12 ft. 43207-1
        - 25 ft. 43207-2
        - 50 ft. 43207-3
      - IOU to VT3, RDI, Printer
        - EIA/4m cable
          - 12 ft. 43102-1
          - 25 ft. 43102-2
          - 50 ft. 43102-3

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VT3

I/O Cable Part Numbers Continued

VT3 to VT3
RS232/V24

<table>
<thead>
<tr>
<th>Length</th>
<th>Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>6 ft.</td>
<td>42055-1</td>
</tr>
<tr>
<td>12 ft.</td>
<td>42055-2</td>
</tr>
<tr>
<td>25 ft.</td>
<td>42055-3</td>
</tr>
<tr>
<td>50 ft.</td>
<td>42055-4</td>
</tr>
</tbody>
</table>

Hub Unit to Line Driver cable

<table>
<thead>
<tr>
<th>Length</th>
<th>Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>100 ft.</td>
<td>42851-001</td>
</tr>
<tr>
<td>500 ft.</td>
<td>42851-002</td>
</tr>
<tr>
<td>1000 ft.</td>
<td>42851-003</td>
</tr>
<tr>
<td>5000 ft.</td>
<td>42851-004</td>
</tr>
<tr>
<td>10000 ft.</td>
<td>42851-005</td>
</tr>
</tbody>
</table>

VT3 CONFIGURATION GUIDE

VT3's attached to a single IOU-39Q may be arranged in the configurations described in the following section. A network involving one IOU-39Q may contain a maximum of 31 VT3's. All Baud Rates should be identical within the network as set by jumper switches J1 through J7. (See diagram on following page.)
In conjunction with Line Drivers, the Hub Unit extends the range of direct strings (1 mile at 19,200 bps; 2 miles at 9600 bps).

- Allows more than one string of VT3's to be connected.
- Connection between Hub Unit and Line Driver should be 4-wire cable (M42851).
- A maximum of 7 Line Drivers may be attached to the Hub Unit.
- Additionally, a local string, without a Line Driver, may be attached.
- Maximum communication speed: 19,200 bps.
- 10U-390 timeout at 19,200 bps: 15 milliseconds.
- Terminator plugs must be attached to female connector of the last VT3 in all strings.

* See page 4-26
VT3

VT3 CONFIGURATION GUIDE CONTINUED

DIRECT STRING NETWORK

- A string of 1 to 31 VT3's connected by 232/V24 cables.
- Total length of string no more than 250 feet.
- No more than 50 feet between individual VT3's.
- Communication speed: 19,200 bits per second.
- IOU-39Q timeout: 15 milliseconds.
- Terminator plug (M42540-001) must be attached to female connector of the last VT3 in the string.
• For longer distances than 1 mile (or 2 miles at 9600 bps), a Remote Network is necessary.
• Asynchronous modems (or Sync. modems with Async. to Sync. Converter) link the IOU39Q and the remote site.
• A string of VT3's may be attached to modem.
• Communication speed is dependent on modem type.
• IOU39Q timeout is dependent on modem speed and line conditions. (See page 4-25).
• Terminator plugs must be installed in the last VT3 in each string.

Note: When communicating over "Leased Line" Modems there may be more than one remote (slave) modem.
Jumper J1 through J7 determine Baud rate. Install only one jumper.

- J1: 19.2K Baud*
- J2: 9600 Baud
- J4: 4800 Baud
- J5: 2400 Baud
- J6: 1200 Baud
- J7: 600 Baud
- J8: 300 Baud

* Denotes typical jumper placement for USA, non-modem installations.

The VT3 device address jumpers J8 through J12 determine the VT3 address. Allowable addresses are $00 to $1E. NEVER address a VT3 to $1F (all switches installed).

- J8: $01
- J9: $02
- J10: $04
- J11: $08
- J12: $10

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4-31
Jumper Settings for VT3 Logic Board Continued

The remaining jumpers J13 through J15 set mode control.
(* Denotes typical jumper placement for USA, non-modem installations).

- **J13**
  - **Installed**: Default is 64 char/line*
  - **Removed**: Default is 80 char/line

- **J14**
  - **Installed**: 60 Hertz*
  - **Removed**: 50 Hertz

- **J15**
  - **Removed**: Normal Operating Position
  - **Installed**: Once installed shift/F2 must be depressed to initiate TEST mode.

- When in TEST mode the following changes in operation occur:
  a) When polled with its address the terminal will display a slash (/) between F2 and F3 on the control line.
  b) When the "clear" key is depressed a lower case alpha character will be displayed to the right of F3 on the control line. This alpha character corresponds to the terminal's address in the following manner:

<table>
<thead>
<tr>
<th>addr.-char.</th>
<th>addr.-char.</th>
<th>addr.-char.</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - a</td>
<td>B - 1</td>
<td>16 - w</td>
</tr>
<tr>
<td>1 - b</td>
<td>C - m</td>
<td>17 - x</td>
</tr>
<tr>
<td>2 - c</td>
<td>D - n</td>
<td>18 - y</td>
</tr>
<tr>
<td>3 - d</td>
<td>E - o</td>
<td>19 - z</td>
</tr>
<tr>
<td>4 - e</td>
<td>F - p</td>
<td>1A - {</td>
</tr>
<tr>
<td>5 - f</td>
<td>10 - q</td>
<td>1B - }</td>
</tr>
<tr>
<td>6 - g</td>
<td>11 - r</td>
<td>1C - ~</td>
</tr>
<tr>
<td>7 - h</td>
<td>12 - s</td>
<td>1D - ☐</td>
</tr>
<tr>
<td>8 - i</td>
<td>13 - t</td>
<td>1E - ☐</td>
</tr>
<tr>
<td>9 - j</td>
<td>14 - u</td>
<td>4-32</td>
</tr>
<tr>
<td>A - k</td>
<td>15 - v</td>
<td></td>
</tr>
</tbody>
</table>
VT3

Monitor Adjustments

- Component Access
  1) Twist two zues fasteners on back of cover 1 turn. Cover should now slide back and off.

  2) There are four screws securing the logic card to the VT3 frame. Remove the two screws nearest the front and loosen the two screws nearest the rear of the unit. Tilt logic card to upright (or personally preferred position) and tighten two rear screws to hold card in place.

- Power Supply
  1) Check for +5, +15, and -12 supplies at pins 3, 5, & 6 respectively on P1 of the logic card. (Refer to next page).

  2) There are no adjustments to the PS9. If any of the supply voltages drift more than 5% at their respective test points replace the entire Pan Assembly Pin, 42198-001.

  \[
  R_{11} - 15V - 4.75 \pm 5.25  \quad 3A
  \]

  \[
  R_{6} - 115V - 14 85 \pm 15 15  \quad 1.5A
  \]

  \[
  Fixed -12V - 10.8 \pm 013.2  \quad 0.05A
  \]
**VT3**

**P1 CONTROL LOGIC BOARD**

<table>
<thead>
<tr>
<th>PIN #1</th>
<th>GND</th>
<th>PIN #2</th>
<th>GND</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>+5V</td>
<td>4</td>
<td>+5V</td>
</tr>
<tr>
<td>5</td>
<td>+15V</td>
<td>6</td>
<td>-12V</td>
</tr>
<tr>
<td>7</td>
<td>V+ (for line driver)</td>
<td>8</td>
<td>V- (for line driver)</td>
</tr>
<tr>
<td>9</td>
<td>XMITD (TD)</td>
<td>10</td>
<td>REQTOS (RTS)</td>
</tr>
<tr>
<td>11</td>
<td>CLRTOS (CTS)</td>
<td>12</td>
<td>RCVDD (RD)</td>
</tr>
<tr>
<td>13</td>
<td>RDY (DTR)</td>
<td>14</td>
<td></td>
</tr>
<tr>
<td>15</td>
<td></td>
<td>16</td>
<td></td>
</tr>
<tr>
<td><strong>17</strong></td>
<td>CLOCK+</td>
<td><strong>18</strong></td>
<td>ALARM-</td>
</tr>
<tr>
<td><strong>19</strong></td>
<td>DKEY+</td>
<td><strong>20</strong></td>
<td>K.B.STROBE+</td>
</tr>
<tr>
<td>21</td>
<td></td>
<td><strong>22</strong></td>
<td>GND</td>
</tr>
<tr>
<td>* 23</td>
<td>VIDEO</td>
<td>24</td>
<td>GND</td>
</tr>
<tr>
<td>* 25</td>
<td>DRVC-</td>
<td>* 26</td>
<td>GND</td>
</tr>
<tr>
<td>* 27</td>
<td>DRVC+</td>
<td>* 28</td>
<td>GND</td>
</tr>
<tr>
<td>29</td>
<td></td>
<td>30</td>
<td></td>
</tr>
<tr>
<td>31</td>
<td></td>
<td>32</td>
<td></td>
</tr>
<tr>
<td>33</td>
<td>+5V</td>
<td>34</td>
<td>+5V</td>
</tr>
<tr>
<td>35</td>
<td>GND</td>
<td>36</td>
<td>GND</td>
</tr>
</tbody>
</table>

**To Keyboard**

**To Monitor**

---

**Video Monitor Board**

- **Vertical Hold**
  
  1) **Vertical Hold** keeps screen from rolling. Adjust as follows:

  Rotate R 302 (V. Hold) toward rear of unit. Display should be rolling. Slowly rotate R 302 in opposite direction until display stabilizes and then continue to rotate R 302 approximately 1/8 turn further.

- **Vertical Linearity & Height**

  The height adjustment makes the space at the top and the bottom of the screen equal. The vertical linearity adjustment makes the letters the same height on all lines.

  1) Fill the screen with upper case letters. Hs, Ms, or Ws are best because they fill the character space, making it easier to see linearity problems. Also, turn on Flags 2 & 3.

  4-35
VT3

Vertical Linearity & Height continued

2) Adjust the height control R(303) until the display fills the screen.
3) Adjust the linearity control R(301) until the letters are the same height on all the lines. The total display height will increase or decrease as the linearity is adjusted; ignore this effect temporarily.
4) Readjust the height until the display again fits the vertical space on the screen.
5) It may be necessary to alternate between linearity and height, making smaller and smaller adjustments each time.

Horizontal Linearity
Only make this adjustment when necessary. The horizontal linearity adjustment makes the characters on the left and right side of equal height and removes any curvature of the characters.
1) Slightly loosen the yoke clamp screw (on the neck of the tube). Loosen only as much as necessary to allow movement of the yoke, yet retain a firm "drag".
2) Move the sleeve under the yoke forward or backward. The sleeve should not have to be moved very far in or out.
3) Make sure the letters are straight across and are not curved at the edges.

Adjust Squareness
If the top and bottom lines of the display are not level with the facemask, adjust as follows:
1) Slightly loosen the yoke clamp screw (on the neck of the tube). Loosen only as much as necessary to allow movement of the yoke, yet retain a firm "drag".
2) Rotate the entire yoke assembly until the top and bottom display are level.
3) Carefully re-tighten the clamp screw until the yoke screw is secure. Do not over tighten, as the tube can be broken.
Adjust Squareness continued

4) It may be necessary to adjust centering when squareness has been corrected.

- Horizontal Width
The width coil can be adjusted by using a .100 hex tool that is made of plastic. Never attempt to do this adjustment using a screwdriver; the metal affects the electrical field making it impossible to properly adjust the width.

1) Using a .100 hex tool, adjust the width of the display until it is within a small margin of both edges. The adjustment is made by rotating the threaded slug inside the coil L(402), making it move in or out of the coil area.

- Centering

1) Adjust R(410) to the center of its adjustment rotation.
2) To center the display on the screen, rotate the two tabs that are located on the deflection yoke until the display is centered. Each tab moves the display at an angle compared to vertical or horizontal. By moving one, then the other, and both in conjunction, the display can be centered, the focus, width and height may need adjustment.

- Brightness

1) Adjust R(1) on back of pan assembly for maximum brightness. Then adjust R(109) on monitor board until raster disappears. Next adjust R(24) video gain, on the main logic card for optimum clarity. Then readjust R(1) on pan assembly for desired brightness.

- Focus

1) Adjust R(407) for optimum focus.
VT3

VT3 MANUAL TEST

1. Turn the VT3 on. After approximately 30 seconds, the cursor will appear in the upper left corner as a space blinking foreground/background.

2. Press A on the keyboard. The letter A should be displayed in the upper-left corner and the cursor is advanced one position left.

3. Press RETURN on the keyboard. The cursor should move to one line below the A.

4. Press F2 on the keyboard. F2 is displayed in background on the bottom screen line (control line). Press F2 again and it should disappear. Repeat with F3.

5. Press TAB. The cursor should disappear. Press A and the VT3 will beep. Press SHIFT/F2 and the VT3 will beep, clear the screen, and the cursor will blink in the upper-left corner.

6. Verify that the following keys repeat after a short delay:
   
   SPACE
   BACKSPACE
   SHIFT/BACKSPACE
   INS CHAR
   SHIFT/INS CHAR

7. The keyboard repeat function is activated by simultaneously pressing SHIFT and the '.' in the numeric cluster. Verify that the next key entered repeats. (It cannot be a control key.)

4-38
8. Verify that the LTRS key toggles the upper/lower case for alphabetic characters.

9. Press each non-control key on the keyboard (with and without the SHIFT) and verify that the correct character is displayed on the screen. Refer to the Serial Keyboard Specifications for the keyboard being tested, e.g., U.S. Domestic and Kana (A30761). On the Kana VT3, verify each key in both Alpha and Kana modes.

10. Set the jumper so that the VT3 is in 64 character default mode. Press SHIFT/F2 and enter some characters to verify the mode. Set the jumper for 80 character mode, press SHIFT/F2, and verify the 80 character mode. Be sure to return the VT3 to 64 character default afterwards.

ATP Checkout

- The VT3 may be tested by the following two ATP's.
  - CRT3 (use operating procedure for CRT 2 ATP)
  - ACRT3 (use operating procedure for ACRT)

Operating System Tests

- Many BEST packs contain a VT3 providing the ability to test the VT3 in the BEST environment.
IOU 6
DATA PROD. CARD RDR.

Qantel Model 5300

MAX. PWR. REQUIREMENTS
-12v \* .30A
+12v \* .16A
+5v = 2.54A
+26v = .59A

CARD RDR. PWR.
115 vac 60 Hz
5.0A for .1 sec.
2.4A normal

WRITE CONTROL INSTRUCTIONS
1XYY9D \( X = \) DEVICE ADDRESS \( YY = \) CONTROL BYTE

CONTROL BYTE

FUNCTION

N/A

STATUS INSTRUCTIONS
4XYY9D \( X = \) DEVICE ADDRESS \( YY = \) STATUS BYTE

STATUS BYTE

MEANING

01
BUSY READING
02
BUSY OFF LINE
04
END
08
SERVICE REQUEST
10
READ ERROR
20
FEED ERROR
40
HOPPER EMPTY / STACKER FULL / HOLD
80
DEVICE INOP.
**IOU 26**

**PDI CARD RDR.**

Qantel Model 5301

**DISABLE**

MAX. PWR. REQUIREMENTS

-12v = .16A
+12v = .12A
+5v = .79A

CARD RDR. PWR.

115 vac 60 Hz
3.0A for .1 sec.
1.55A normal

**ADDRESS SWITCHES 0-F**

* DISABLES ERROR STATUS FOR NON-HOLLERITH CHARACTERS.

**WRITE CONTROL INSTRUCTIONS**

\[1XY9D \ x = \text{DEVICE ADDRESS} \quad yy = \text{CONTROL BYTE}\]

<table>
<thead>
<tr>
<th>CONTROL BYTE</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>02</td>
<td>SET READ BINARY</td>
</tr>
<tr>
<td>04</td>
<td>SET ALLOW TERMINATION INTERRUPT</td>
</tr>
<tr>
<td>0C</td>
<td>SET ALLOW TERMINATION INTERRUPT</td>
</tr>
</tbody>
</table>

**STATUS INSTRUCTIONS**

\[4XY9D \ x = \text{DEVICE ADDRESS} \quad yy = \text{STATUS BYTE}\]

<table>
<thead>
<tr>
<th>STATUS BYTE</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>N/A</td>
</tr>
<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>INTERRUPT</td>
</tr>
<tr>
<td>20</td>
<td>READ CHECK</td>
</tr>
<tr>
<td>40</td>
<td>DEVICE HOLD</td>
</tr>
<tr>
<td>80</td>
<td>DEVICE INOP.</td>
</tr>
</tbody>
</table>

5-2
IOU 2
ROYTRON
PAPER TAPE READER/PUNCH
Qantel Model 5400

ADDRESS SWITCHES 0-F

MAX. PWR. REQUIREMENTS
-12v = .20A
+12v = .17A
+ 5v = .63A
+26v = .92

RDR/PUNCH PWR.
115 vac 60 Hz
3.0A for .05 sec.
1.5A normal

WRITE CONTROL INSTRUCTIONS
N/A

STATUS IN INSTRUCTIONS

4XYY9D X = DEVICE ADDRESS YY = STATUS BYTE

STATUS MEANING
BYTE

01 READ BUSY
02 WRITE BUSY
04 END
08 SERVICE REQUEST
10 N/A
20 READER INOP.
40 PUNCH INOP.
80 INOP.

5/82
**IOU 3 COMMUNICATIONS**

**Qantel Model 6000**

- **DIBIT**
- **Used with 201 Type Data Sets (Sync)**

**Address Switches**
- **0-F**

**Bit Rate**
- **1200**
- **600**
- **300**
- **150**
- **75**

**Max. Pwr. Requirements**
- **-12v = .09A**
- **+12v = .09A**
- **+ 5v = .58A**

**Write Control Instructions**

1XYY9D  \( X = \) Device Address  \( YY = \) Control Byte

<table>
<thead>
<tr>
<th>Control Byte</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>NB1</td>
</tr>
<tr>
<td>02</td>
<td>NB2</td>
</tr>
<tr>
<td>04</td>
<td>NB4</td>
</tr>
<tr>
<td>08</td>
<td>NB8</td>
</tr>
<tr>
<td>10</td>
<td>DPP</td>
</tr>
<tr>
<td>20</td>
<td>CRQ</td>
</tr>
<tr>
<td>40</td>
<td>CD</td>
</tr>
</tbody>
</table>

**Status in Instructions**

4XYY9D  \( X = \) Device Address  \( YY = \) Status Byte

<table>
<thead>
<tr>
<th>Status Byte</th>
<th>Meaning</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>CFS</td>
</tr>
<tr>
<td>02</td>
<td>N/A</td>
</tr>
<tr>
<td>04</td>
<td>N/A</td>
</tr>
<tr>
<td>08</td>
<td>Read Service Request</td>
</tr>
<tr>
<td>10</td>
<td>Interrupt</td>
</tr>
<tr>
<td>20</td>
<td>CCS</td>
</tr>
<tr>
<td>40</td>
<td>N/A</td>
</tr>
<tr>
<td>80</td>
<td>End Time Out</td>
</tr>
</tbody>
</table>

**5/82**

7-1
Qantel Model 6100, 6200

**BUFFERED COMMUNICATIONS**

**ADDRESS SWITCHES**

- 0-F
- 8 4 2 1

**BIT RATE**

- 1200
- 300
- 600
- 150

**XTAL BIT RATES**

- 4.92 1200, 600, 300, 150
- 4.41 1076, 538, 269, 134.5
- 3.60 880, 440, 220, 110

**MAX. PWR. REQUIRED**

- $-12\,\text{v} = .18$
- $+12\,\text{v} = .13$
- $+5\,\text{v} = 1.32$

**FULL DUPLEX**

- HOLDS REQUEST TO SEND AND CLEAR TO SEND TRUE FOR 4-WIRE OPERATION.

**FORMAT**

- 7 & 8 DN= 1 START,
- 8 DATA, 2 STOP BITS (TELETYPE)
- 7 DN 8 UP= 1 START,
- 8 DATA, 1 STOP BIT.
- 7 & 8 UP= 1 START,
- 7 DATA, 1 STOP BIT (8TH DATA BIT MUST BE A 1, SUPPLIED BY PROGRAM).

* SELECT ONLY ONE AT A TIME.
** PART OF RATE SELECTION, SET TO HI FOR 402 OPERATION.

**WRITE CONTROL INSTRUCTIONS**

1XYY9D X = DEVICE ADDRESS YY = CONTROL BYTE

<table>
<thead>
<tr>
<th>CONTROL BYTE</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>1 = SET SELECTED F.F. 0 = RESET SELECTED F.F.</td>
</tr>
<tr>
<td>02</td>
<td>SELECT INHIBIT INTERRUPT F.F.</td>
</tr>
<tr>
<td>04</td>
<td>SELECT MODE F.F.</td>
</tr>
<tr>
<td>08</td>
<td>RESET INTERRUPT</td>
</tr>
<tr>
<td>10</td>
<td>SET DIAL</td>
</tr>
</tbody>
</table>

**STATUS IN INSTRUCTIONS**

4XYY9D X = DEVICE ADDRESS YY = STATUS BYTE

<table>
<thead>
<tr>
<th>STATUS BYTE</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>N/A</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY</td>
</tr>
<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>INTERRUPT</td>
</tr>
<tr>
<td>20</td>
<td>FULL</td>
</tr>
<tr>
<td>40</td>
<td>EMPTY</td>
</tr>
<tr>
<td>80</td>
<td>NO DATA TIME OUT</td>
</tr>
</tbody>
</table>

7-2 5/82
Qantel Model 6201

**WRITE CONTROL INSTRUCTIONS**

1XY9D  \( X \) = DEVICE ADDRESS
YY = CONTROL BYTE

**CONTROL FUNCTION BYTES**

01 SET RUN AND RESET INT
02 SET SIGNAL F.F.
04 P MINUS 1

**STATUS IN INSTRUCTIONS**

4XY9D  \( X \) = DEVICE ADDRESS
YY = STATUS BYTE

**STATUS MEANING BYTES**

<table>
<thead>
<tr>
<th>YY</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY</td>
</tr>
<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>INTERRUPT</td>
</tr>
<tr>
<td>20</td>
<td>SIGNAL F.F.</td>
</tr>
<tr>
<td>40</td>
<td>N/A</td>
</tr>
<tr>
<td>80</td>
<td>N/A</td>
</tr>
</tbody>
</table>

**MAX. PWR. REQUIRED**

-12v = .07A
+12v = .06A
+ 5v = .90A

**FULL DUALPLEX HOLDS REQUEST TO SEND & CLEAR TO SEND TRUE FOR 4-WIRE OPERATION.**

**ESB = EXTRA STOP BIT FOR TELETYPE FORMAT.**

15 COMMUNICATIONS MICROPROCESSOR

IOU-15A

ADDRESS SWICHES 0-F

MAX. PWR. REQUIRED

-12v = .31A
+ 5v = 2.02A

(4X NORMAL SPEED)

FDX (FULL DUPLEX)

ESB 7 BIT

ESB = EXTRA STOP BIT FOR TELETYPE FORMAT.
**IOU 22 PARALLEL COMMUNICATIONS**

Qantel Model 6202

MAX. PWR. REQ.
-12v = .07A
+12v = .06A
+ 5v = 1.27A

ESB = EXTRA STOP BIT FOR TELETYPE FORMAT.

7 BIT= SELECTS EITHER A 7-BIT OR AN 8-BIT DATA FORMAT. 7-BIT IS USED FOR SYNCHRONOUS COMMUNICATION.

WRITE CONTROL INSTRUCTIONS:

1XY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

CONTROL BYTE FUNCTION

- **00**  RESET BYTE MODE
- **01**  SET BYTE MODE
- **02**  RESET SYNC MODE
- **03**  SET SYNC MODE
- **04**  RESET 1/8 SPEED
- **05**  SET 1/8 SPEED
- **06**  RESET 8X SPEED
- **07**  SET 8X SPEED
- **08**  RESET CALL REQUEST
- **09**  SET CALL REQUEST
- **0A**  RESET ALLOW INTERRUPT
- **0B**  SET ALLOW INTERRUPT
- **0C**  RESET INTERRUPT
- **0D**  GIVE NEW SYNC
- **0E**  RESET DATA HI SPEED SYNCHRONOUS
- **0F**  SET DATA HI SPEED SYNCHRONOUS

STATUS IN INSTRUCTIONS

4XY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

STATUS BYTE MEANING

- **01**  N/A
- **02**  N/A
- **04**  TRANSMIT SERVICE REQUEST
- **08**  READ SERVICE REQUEST
- **09**  INTERRUPT
- **20**  EVEN PARITY IN TRANSMIT REGISTER
- **40**  EVEN PARITY IN RECEIVE REGISTER
- **80**  END TIME OUT
TYPICAL POWER REQUIREMENTS

-12V = .100A
+12V = .150A
+5V = 1.5A

$80 - MASTER DEFAULT should be set if the IOU-39Q is at address 0 in the backplane with terminal device 0 (the IPL device) connected.

$40, $20 - RECEIVE TIMEOUT

<table>
<thead>
<tr>
<th>2^A</th>
<th>2^B</th>
<th>TIMEOUT</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>15 msec</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>50 msec</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>150 msec</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>500 msec</td>
</tr>
</tbody>
</table>

$10 - SLAVE TERMINAL ADDRESS

Used only when the IOU-39Q is used in Slave Mode. Addresses $01 through $1F may be used.

$01

$01

x = 0
y = Current Rev. Level

7-5
STATUS IN Instructions

\[ \text{d} = \text{Device Address} \quad \text{yy} = \text{test bit value} \]

Status Byte:

- \( g_1 \): Read Busy
- \( g_2 \): Write Busy
- \( g_4 \): End
- \( g_8 \): Service Request
- \( g_{10} \): Interrupt (if enabled)
- \( g_{20} \): Channel A Termination signal
- \( g_{40} \): Channel B Termination signal
- \( g_{80} \): RAM Parity Error

READ Status 2

\[ \text{xdyy86} \quad \text{d} = \text{Device Address} \quad \text{yy} = \text{test bit value} \]

Hardwired to SA3

WRITE CONTROL Instructions

\[ \text{ldyy90} \quad \text{d} = \text{Device Address} \quad \text{yy} = \text{Control Byte} \]

Control Byte: 1\( st \) = High RAM address
2\( nd \) = Low RAM address

\( \text{Allows read or write to two-port RAM} \)
PROCESSOR

CENTRAL PROCESSING UNIT

The Central Processing Unit (CPU) coordinates program activities by manipulating (add, subtract, edit, etc.), storing (read/write), and moving data. Each function is accomplished by stepping through micro-instruction routine from a Read Only Memory (ROM).

The CPU always operates from macro, or machine language, instructions. Macro instructions can be entered in hexadecimal form directly from a peripheral, but are usually created by a compiler or assembler (QIC or QBAL).

Each macro instruction contains the initial address for a ROM routine. The ROM address is loaded into a counter that is incremented one count at a time, or reloaded with a new ROM address if the ROM routine requires a branch within the ROM program.

During the course of a macro instruction, the CPU generates and stores certain results. Some of these results are placed into flip-flops, some into hardware registers, and some are placed into the lower addresses of main memory. The hardware registers are assigned alphabetic names (A, B, P, etc.) and the portion of memory that is used for temporary storage is called Reserved memory.

Reserved Memory

Reserved memory is not exclusive, protected, or separate from main memory as the name implies. It is the first (lowest) 32 bytes in memory, designated as addresses 0 to 31. Bytes, as referred to in this text, are a set of eight bits. These 8 bits may translate to an ASCII character, but are always stored in memory as the two 4-bit characters of a hexadecimal notation pair. Specific areas of reserved memory are used for each type of storage. The areas and use are listed below.

<table>
<thead>
<tr>
<th>AREA</th>
<th>USE</th>
</tr>
</thead>
<tbody>
<tr>
<td>0000-000F</td>
<td>Accumulator positions, a scratch pad area for the results of multiple add instructions, etc. When a single address instruction is used, the implied second operand is the accumulator and its contents.</td>
</tr>
<tr>
<td>0010-0011</td>
<td>Stores the current program address while the CPU is servicing an interrupt.</td>
</tr>
<tr>
<td>0012</td>
<td>Stores the contents of the condition switches (flip-flops) Carry, Minus, and Non-zero. Also contains the Interrupt Enable bit.</td>
</tr>
<tr>
<td>0013-0014</td>
<td>The address in these two bytes will replace the current program address when an interrupt is recognized.</td>
</tr>
</tbody>
</table>
PROCESSOR

Reserved Memory (Continued)

AREA

USE

0015-0016 One count more than the final address at the conclusion of an I/O operation. During an I/O operation, as data is taken from or placed into memory, the address count is incremented. When the last transfer is completed, the address is incremented once more, and the count is stored. The programmer can use this for self-terminating, continuous, non-counting I/O instructions.

0017 Stores the I/O status byte when a Status-In instruction is executed.

0018-0019 Q5 only: Stores the current program address when a Branch instruction is executed.

Q6, 6.5, 7, and 7.5: Contains the "Match" address for the following instructions.
- Search Equal (SEQ)
- Search Equal L&R (SER, SEL)
- Search Unequal L&R (SNR, SNL)

001A-001D Micro program utility area.

001E-001F Q-29 P.C. before soft IPL

Test Panel

The test panel has the ability to suspend normal timing, and stop processor actions so they can be examined. Test panel displays include the major hardware registers, and the contents of any byte in memory. The test panel is primarily used to examine processor actions as they are being performed. Selectively, the processor can be made to stop at each micro instruction, or at the end of each macro instruction. In either case, the operator must manually step to the next instruction. Using the test panel as a tool, and by knowing what should be each register at different times, the field engineer can isolate difficult hardware or program problems. The test panel operator controls and indicators are shown and described in the illustrations and tables that follow.

Some test panel functions are different when a test panel is modified and placed into a 7.5 (model 1300) processor.
PROCESSOR

TEST PANEL
CONFIGURATIONS
<table>
<thead>
<tr>
<th>NAME</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>FR</td>
<td>A two-position switch, when in the FR position, repeats the current micro instruction continuously.</td>
</tr>
<tr>
<td>Function</td>
<td>When the SF switch (2) is in the SF position, the micro instruction will only be repeated when the ST-SF switch (13) is pressed.</td>
</tr>
<tr>
<td>Repeat</td>
<td></td>
</tr>
<tr>
<td>SF</td>
<td>A two position switch, when in the SF (single function) position, enables the ST-SP switch (13) to perform one micro instruction at a time.</td>
</tr>
<tr>
<td>Single Function</td>
<td>If the SF switch is turned ON when the processor is running, processing halts after the current micro instruction is executed.</td>
</tr>
<tr>
<td>ICSP</td>
<td>A two position switch, when in the ICSP (Instruction Complete Stop) position, causes the processor to halt after executing and completing the current macro instruction. The ST-SP switch (13) is used to fetch and execute the next instruction.</td>
</tr>
<tr>
<td>Instruction Complete</td>
<td></td>
</tr>
<tr>
<td>Stop</td>
<td></td>
</tr>
<tr>
<td>IPL</td>
<td>Momentary-contact switch, provides a general reset to the processor. The IPL routine generates an instruction to read hex (without count) from device zero to memory location zero.</td>
</tr>
<tr>
<td>Initiate Program</td>
<td></td>
</tr>
<tr>
<td>Load</td>
<td></td>
</tr>
<tr>
<td>MIM</td>
<td>A two-position switch, when in the MIM position, places the processor control into the micro instruction mode. Micro instructions are then taken from main memory, rather than from a ROM.</td>
</tr>
<tr>
<td>Instruction Mode</td>
<td></td>
</tr>
<tr>
<td>MASP/ZSP</td>
<td>A three position switch. When in the MASP position, the processor will execute instructions at full machine speed, until it is required to read/write to whatever address is selected on the switch/lamps (12). The read or write is executed, but the processor is halted at that micro instruction. The read/write address may come from the A, B, or P register. In the ZSP position, the processor will proceed at full machine speed until a micro instruction is fetched by the Z register from a ROM address that is the same as that selected by the switch lamps (12). However, the A, B, and P registers will also halt processing if their contents compare with the switch lamp settings. This is to allow the processor to stop at a particular micro instruction when it is in the MIM mode. To avoid confusion, keep all r/w program at memory addresses above 1000. The center switch position allows the processor to proceed normally, unless the single function or instruction stop switches are in use.</td>
</tr>
</tbody>
</table>
PROCESSOR

Test Panel (Continued)

AMI Address A two-position switch. When in the AMI position, prevents incrementing the selected address when either the WR (11) or RD (10) switch is depressed. The AMI switch also controls a periodic READ function to dynamically display the contents of a location. Do NOT operate either the RD (10) or WR (11) switch when the processor is running, and this switch is in the AMI position.

Modification Inhibit A three-position switch to control the display or entry of data and addresses. Is used in conjunction with the RD (10), WR (11), SWITCH/LAMPS (12), Z, P, and SP controls.

DATA/E/ADDR A three-position switch to control the display or entry of data and addresses. Is used in conjunction with the RD (10), WR (11), SWITCH/LAMPS (12), Z, P, and SP controls.

R Reset Push-button switch. When pressed, resets any data or addresses that are selected on the switch/lamps. When the switch/lamps are reset, the lamps are not lit (zero state).

RD A momentary-contact switch. When depressed, reads from a previously selected address in memory. Displays the data in the two least significant columns of the switch/lamps (only if the DATA/E/ADDR switch is in the DATA position). The selected memory address is then automatically incremented by one count, unless the AMI switch is ON.

WR A momentary-contact switch. Writes whatever data is selected on the switch/lamps, to a previously selected position in memory. The selected memory address is then automatically incremented by one count, unless the AMI switch is ON.

SWITCH/LAMPS Four columns of lighted push-button switches. Each column represents four binary bits, with the lower-right bit being the least significant. The lamp is lit when the bit position is in the "1" state. The switch/lamps are used as follows:

a. To address any location in main memory by placing the DATA/E/ADDR switch in the ADDR position, and select the desired address.

b. To read the contents of the selected position in memory by setting the DATA/E/ADDR switch to the DATA position. Data is displayed in the two least significant (right hand) columns.

c. To alter (place data into) memory at the selected location by placing the DATA/E/ADDR switch in the DATA position, selecting the character code in the two least significant columns of the switch/lamps, and depressing the WR switch.

d. To display the contents of the E register (the micro instruction that is being performed) by placing the DATA/E/ADDR switch in the E position.

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8-5
PROCESSOR

Test Panel (Continued)

ST-SP  A momentary-contact switch that will halt the processor at the end of the current macro instruction. It will restart processing if in a halt condition, by initiating the next instruction. If the SF (2) switch is ON, ST-SP must be depressed for each micro instruction. If SF is OFF, and ICSP is ON, ST-SP is depressed for each macro instruction.

Z  A momentary-contact switch that will display the contents of the Z register (the micro instruction address) on the switch/lamps if the DATA/E/ADDR switch is in the ADDR position.

P  A momentary-contact switch that will display the contents of the P register (program address) on the switch/lamps if the DATA/E/ADDR switch is in the ADDR position.

SP  A momentary-contact switch that will enter the address that is displayed (selected) on the switch/lamps if the DATA/E/ADDR switch is in the ADDR position.

B  A momentary-contact switch that displays the 15 low-order bits of the B register on the switch/lamps if the DATA/E/ADDR switch is in the ADDR position.

A  A momentary-contact switch that displays the 15 low-order bits of the A register on the switch/lamps if the DATA/E/ADDR switch is in the ADDR position.

CD  A momentary-contact switch that displays the contents of the C and D registers on the switch/lamps if the DATA/E/ADDR switch is in the DATA position.

DMA  A 10-position thumbwheel switch. Selects one of nine direct memory access devices. When the wheel is moved from zero, all processing stops in the devices and the processor. The ST-SP switch will allow all devices and the processor to perform one micro instruction at a time.

SEL  The A, B, Z, and P registers of the selected direct memory access device can be displayed on the switch/lamps instead of the processor registers.
PROCESSOR
MEMORY ORGANIZATION AND INSTALLATION

The first 16k of memory must reside within one (either) of the MEM slots in the mainframe. It will be either a MEM 3 with the HI/LO switch set to LO, or the lower two MEM 5B cards on a MEM 5AP.

If a fully configured MEM 5AP board (containing 4 MEM 5Bs) is used, it must reside in one (either) of the MEM slots, and the other MEM slot must remain empty. Additional memory can be added, but only in slots IOC1 or IOC2 (max. 16k each).

Only one fully configured MEM 5AP is allowed in 7.0 or lower processors (model 1200 or lower).

LO memory in either of the IOC slots, or greater than 32k total in the MEM slots causes random processor errors. LO memory can be a MEM 3 with the switch set to LO, or a MEM 5AP with one or both MEM 5B boards in the lower position.

The lowest 16k of memory is designated as bank A, and is available regardless which upper memory bank is selected. After a reset (RIO) or IPL, the second 16k of memory, bank C, is automatically selected along with bank A. Memory beyond 32k is selected by bank switching instructions as follows:

- 0000AC = Access A and C (used to return to C)
- 0000AD = Access A and D (third 16k of memory)
- 0000AE = Access A and E (fourth 16k of memory)

NOTE: Bank switching instructions should only be issued by a program running in the A bank (first 16k) of memory.

MEM 3

Mem 3 boards accommodate up to four 4k modules (MEM 3B) of 4k each. Addresses within the card are shown below. Except for the first 16k of memory (addresses 0000-3FFF) in the MEM slot, the switch must be in the HI position.

NOTE: While MEM 3 boards will operate in the 900 and 950 processors (6.5 backplane) the BEST operating system requirements make this combination impractical.

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MEM 5AP boards will accommodate up to four modules (MEM 5B) of 8k each. It also has the capability of parity checking and storage. The MEM 5AP addresses within the board are shown in the illustration below.

NOTE:
The parity reporting portion of the MEM 5AP appears to the processor as an I/O device. If the MEM 5AP address switches are set to the same address as an IOU, the IOU will not function.

Set 7/7.5 switch DOWN (7 position) for Q6, Q6.5, and Q7. Set in UP position (7.5) for all others.

Parity checking and reporting are distinctly separate from memory operation, and the memory is not affected or inhibited in any way by the parity circuits. However, IOUs can be affected if the MEM 5AP address switches are improperly set.

Parity is always generated, and is stored as a ninth bit when data is put into memory. Odd parity is normal, but even parity can be set by microinstruction for special diagnostics. Parity errors are not reported or stored (fuses blown) unless enabled by write control instructions. However, many programs and diagnostics "arm" the memory automatically, using these write control instructions.

If parity checking is used, the memory must be written into in every location (initialized) to assure that every position has proper parity. The disc loader, LOADQ, automatically initializes the memory and enables both parity checking and error storage.

Parity error storage physically blows a fuse, which in turn causes an LED indicator to light. There is one fuse/indicator circuit for each 8k module.

The device control instructions are as follows: 1dyy9D

20 Resets 'Allow Parity Check' (disables checking)
21 Enables Parity Check
40 Inhibit Interrupt
41 Allow Interrupt. When parity error is found when checking is enabled, Interrupt is raised to the processor.
61 Enables parity error storage.

Address switches for the parity reporting portion of the MEM 5AP board are arranged as shown below.

Switches are shown set to address 5.
**Status - In Instructions**

4dtt9D  (d = device number)

<table>
<thead>
<tr>
<th>tt</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>$00</td>
<td>0</td>
</tr>
<tr>
<td>$02</td>
<td>0</td>
</tr>
<tr>
<td>$04</td>
<td>1</td>
</tr>
<tr>
<td>$08</td>
<td>Even Parity</td>
</tr>
<tr>
<td>$10</td>
<td>Parity Error</td>
</tr>
<tr>
<td>$20</td>
<td>Which Chip Group on 5B</td>
</tr>
<tr>
<td>$40</td>
<td>See Below</td>
</tr>
<tr>
<td>$80</td>
<td>See Below</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>$80</th>
<th>$40</th>
<th>Which Mem 5B</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>First</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>Second</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>Third</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>Fourth</td>
</tr>
</tbody>
</table>

**NOTE:** Normal Status is $E4.
PROCESSOR

CPU BOARD LAYOUT

CPU- A/AR

MICROPROCESSOR

Z REGISTER

ROM

E REGISTER

CPU- B/BR

REGISTERS AND ALU

P REGISTER

C REG

A REGISTER

D REG

B REGISTER

ALU

8-10
<table>
<thead>
<tr>
<th>PROCESSOR</th>
<th>REVISION LEVEL</th>
<th>2311 DISK</th>
<th>2314 DISK</th>
<th>3+3/6+6 DISK</th>
<th>QCRT</th>
<th>MEM2</th>
<th>MEM3</th>
<th>MEM5</th>
<th>MAX MEMORY SYSTEM</th>
</tr>
</thead>
<tbody>
<tr>
<td>Q 5 LEVEL 1</td>
<td>CPU A/B ROM 1, 2, &amp; 3</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>8K V</td>
</tr>
<tr>
<td>Q 5 LEVEL 2</td>
<td>CPU A/B ROM 1, 2, &amp; 3</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>8K V</td>
</tr>
<tr>
<td>Q 6 LEVEL 1</td>
<td>CPU AR/B ROM 5, 6, 7</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>32K VI</td>
</tr>
<tr>
<td>Q 6 LEVEL 2</td>
<td>CPU AR/B ROM 5, 6, 7</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>32K VI</td>
</tr>
<tr>
<td>Q 6.5 LEVEL 1</td>
<td>CPU AR/B ROM 5, 6, 8, 9</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>32K 800 * 900 *</td>
</tr>
<tr>
<td>Q 6.5 LEVEL 2</td>
<td>CPU AR/B ROM 5, 6, 8, 9</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>48K 950 *</td>
</tr>
<tr>
<td>Q 7.5 LEVEL 1</td>
<td>CPU AS/BS</td>
<td>NO</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>NO</td>
<td>YES</td>
</tr>
</tbody>
</table>

* Selectric cannot be used on these models. Needs 26V power supply.
### Processor

#### Macro Instruction Set

<table>
<thead>
<tr>
<th>Level</th>
<th>Instruction</th>
<th>Mnemonic</th>
<th>Code</th>
<th>Var 1</th>
<th>Var 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>READ</td>
<td>RD</td>
<td>0</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>READ HEX</td>
<td>RHX</td>
<td>0</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>READ AND COUNT</td>
<td>RDC</td>
<td>0</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>READ HEX AND COUNT</td>
<td>RHC</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>ADD DECIMAL</td>
<td>ADD</td>
<td>0</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SUBTRACT DECIMAL</td>
<td>SBD</td>
<td>0</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>MULTIPLY DECIMAL</td>
<td>MPY</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>DIVIDE DECIMAL</td>
<td>DIV</td>
<td>0</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>LOAD</td>
<td>LD</td>
<td>0</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>STORE ACCUMULATOR</td>
<td>STA</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>MOVE</td>
<td>MOV</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>COMPARE LOGICAL</td>
<td>CMP</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SEEK</td>
<td>SEK</td>
<td>0</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>DISC BOOTSTRAP</td>
<td>---</td>
<td>0</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>LOAD BASE REGISTER</td>
<td>LBR</td>
<td>0</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>TRANSLATE</td>
<td>TRN</td>
<td>0</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>LOAD DMA BANK</td>
<td>LDM</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SEARCH EQUAL</td>
<td>SEQ</td>
<td>0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>COMPARE NUMERIC</td>
<td>CN</td>
<td>0</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>COMPARE ZONE</td>
<td>CZ</td>
<td>0</td>
<td>5</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>READ STATUS 2</td>
<td>RS2</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>LOAD IMMEDIATE</td>
<td>LDI</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>ADD IMMEDIATE</td>
<td>ADI</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SUBTRACT IMMEDIATE</td>
<td>SBI</td>
<td>0</td>
<td>6</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>READ BASE REGISTER</td>
<td>RBR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>TRANSLATE RIGHT TO LEFT</td>
<td>TRR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SCAN UNREQ RIGHT TO LEFT</td>
<td>SNR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SCAN EQUAL RIGHT TO LEFT</td>
<td>SER</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>FILL RIGHT TO LEFT</td>
<td>FR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>MOVE RIGHT TO LEFT</td>
<td>MR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>EXCHANGE RIGHT TO LEFT</td>
<td>XR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>COMPARE RIGHT TO LEFT</td>
<td>CR</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>TRANSLATE LEFT TO RIGHT</td>
<td>TRL</td>
<td>0</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SCAN UNREQ LEFT TO RIGHT</td>
<td>SNL</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>SCAN EQUAL LEFT TO RIGHT</td>
<td>SEL</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>FILL LEFT TO RIGHT</td>
<td>FL</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>MOVE LEFT TO RIGHT</td>
<td>ML</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>EXCHANGE LEFT TO RIGHT</td>
<td>XL</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>COMPARE LEFT TO RIGHT</td>
<td>CL</td>
<td>0</td>
<td>7</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>INTERRUPT SCAN</td>
<td>SCI</td>
<td>0</td>
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<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>LOAD BASE REG &amp; BRANCH</td>
<td>LBB</td>
<td>0</td>
<td>8</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>UNSTACK AND RETURN</td>
<td>RET</td>
<td>0</td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>STACK RET ADD &amp; BRANCH</td>
<td>SUB</td>
<td>0</td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
<td>FULL DATA OFF TOP OF STACK</td>
<td>PUL</td>
<td>0</td>
<td>9</td>
<td></td>
</tr>
<tr>
<td>1 2 1 2 3 1 1 2 1</td>
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<td>DECREMENT BY 2</td>
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Revised 3/10/76
## PROCESSOR

### MACRO INSTRUCTION SET

<table>
<thead>
<tr>
<th>LEVEL</th>
<th>INSTRUCTION</th>
<th>OP</th>
<th>VAR</th>
<th>VAR</th>
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<td>OR</td>
<td>OR</td>
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<td>EXCLUSIVE OR</td>
<td>XOR</td>
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<td>TEST BIT</td>
<td>TBT</td>
<td>9</td>
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<td></td>
<td>MOVE NUMERIC</td>
<td>MN</td>
<td>9</td>
<td>4</td>
</tr>
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<td>MOVE ZONE</td>
<td>MZ</td>
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<td>COMPARE DECIMAL</td>
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<td>SHIFT BIT LEFT</td>
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<td>SHIFT BIT RIGHT</td>
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<td>9</td>
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<td>UNPACK</td>
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<td>9</td>
<td>D</td>
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<td>RESET I/O</td>
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<td>RETURN FROM INTERRUPT</td>
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<td>NO OPERATION</td>
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<td>BRANCH ON OVERFLOW</td>
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<td>BRANCH ON MINUS</td>
<td>BMI(BGT)</td>
<td>A</td>
<td>2</td>
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<td>BRANCH ON NON-ZERO</td>
<td>BNZ(BNE)</td>
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<td>BRANCH EQUAL</td>
<td>BEQ(BZ)</td>
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<td>BRANCH NOT MINUS</td>
<td>BNN(BLE)</td>
<td>A</td>
<td>5</td>
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<td>BRANCH NOT OVERFLOW</td>
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<td>A</td>
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<td>UNCONDITIONAL BRANCH</td>
<td>BRU</td>
<td>A</td>
<td>7</td>
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<td></td>
<td>HALT AND BRANCH</td>
<td>HLT</td>
<td>A</td>
<td>8</td>
</tr>
<tr>
<td></td>
<td>BRANCH AND LINK</td>
<td>BLI</td>
<td>A</td>
<td>9</td>
</tr>
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<td></td>
<td>BRANCH PLUS</td>
<td>BLP</td>
<td>A</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td>MICRO INSTRUCTION MODE</td>
<td>MIM</td>
<td>A</td>
<td>B</td>
</tr>
<tr>
<td></td>
<td>BANK SELECT C</td>
<td>BSC</td>
<td>A</td>
<td>C</td>
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<td>BANK SELECT D</td>
<td>BSD</td>
<td>A</td>
<td>D</td>
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<td>BANK SELECT E</td>
<td>BSE</td>
<td>A</td>
<td>E</td>
</tr>
<tr>
<td></td>
<td>BRANCH NOT PLUS</td>
<td>BNP</td>
<td>A</td>
<td>F</td>
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<tr>
<td></td>
<td>WRITE</td>
<td>WR</td>
<td>B</td>
<td>0</td>
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<tr>
<td></td>
<td>WRITE HEX</td>
<td>WHX</td>
<td>B</td>
<td>1</td>
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<tr>
<td></td>
<td>WRITE AND COUNT</td>
<td>WRC</td>
<td>B</td>
<td>2</td>
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<tr>
<td></td>
<td>WRITE HEX AND COUNT</td>
<td>WHC</td>
<td>B</td>
<td>3</td>
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<td></td>
<td>ADD BINARY</td>
<td>ADB</td>
<td>D</td>
<td></td>
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<tr>
<td></td>
<td>SUBTRACT BINARY</td>
<td>SSB</td>
<td>E</td>
<td></td>
</tr>
<tr>
<td></td>
<td>LOAD ADDRESS</td>
<td>LDA</td>
<td>F</td>
<td></td>
</tr>
</tbody>
</table>

8-13 Revised 3/10/76
The TPA and TPU test panels are compatible with the 1300 (7.5 backplane) but some of the functions display or control different registers. The following is a list and explanation of the test panel functions when used with the 7.5 backplane. Operation of the DMA switch becomes complex with the 7.5, and is explained in text, but is followed by a table. The table summarizes all of the registers that can be selected through the DMA switch.

FR  Creates the $2^{16}$ bit that is needed for test panel addressing in the 7.5.

SF  When the Single Function is ON, the 7.5 runs at machine speed until it reaches a "bus" cycle, then it halts. A bus cycle is either a memory cycle or an I/O cycle.

ICSP  Stops after each macro instruction.

IPL  General reset of the processor. The processor halts just before executing whatever instruction is at memory location zero. IPL does NOT generate a read hex instruction as it does in the earlier processors.

MIM  Generates the $2^{15}$ bit for memory addressing from the test panel.

MASP/ZSP  In the MASP position, causes the processor to halt when the processor is required to access the memory address that is contained in the test panel address register. The contents of the test panel address register is displayed when the DATA/E/ADDR switch is in the ADDR position. In the OFF position, the MSAP/ZSP switch has no effect.

AMI  When in the ON position, inhibits the address advance that is normally automatic when the RD (read) or WR (write) function is used. When the machine is running and AMI is ON, the memory contents from the test panel ADDRess register is read into the test panel DATA register after every refresh cycle.

DATA/E/ADDR  The E position has no effect in the 7.5 backplane processor. The DATA and ADDR positions display the contents of the data and address registers within the test panel. These registers can be reset (by pressing the R button) to a zero state, then reloaded by depressing the desired bit switches.

R  Resets the particular register (DATA or ADDR) that is selected by the DATA/E/ADDR switch. The other (unselected) register is not reset.
SWITCH/LAMPS

Work in conjunction with the register switch to either load or display the contents of the selected register. When the register is selected, the contents are displayed. If the R (reset) button is pressed, the selected register is set to zero, and can then be loaded by pressing the buttons that correspond to the desired bits. All four columns (0-3) of lamps and switches are used for the ADDRESS register, but only the two right hand columns (0 and 1) are used with the DATA register.

SP
Sets the processor program (P) address to the number that is contained in the test panel ADDR register and MIM and FR switches.

CD
Loads the test panel DATA register from the processor MD register. The data can then be displayed by moving the ADDR/E/DATA switch to the DATA position.

HLT
Indicates that the processor is halted, or that a Direct Memory Access (DMA) cycle is in progress.

ST-SP
When the DMA-SEL switch is zero, stops the processor after the current instruction is completed. Processor restarts when the ST-SP button is pressed a second time. When the DMA-SEL switch is NOT at zero, allows only one cycle to execute (each time the ST-SP button is pressed). This applies to the processor and all active DMA devices. HLT coming ON when ST/SP is pressed no longer means that the machine is running.

DMA-SEL
The following definitions apply only when the DMA-SEL switch is in the 0 position.

RD
Reads the contents (one byte) of the memory address that is specified by the number in the ADDR register and the MIM and FR bits. The data that is read is placed into the DATA register where it can be displayed by the DATA/E/ADDR switch.

WR
Writes the contents of the test panel DATA register into the address in memory that is specified by the ADDR register and the MIM and FR bits.

A
Loads the contents of the processor A register into the test panel ADDRESS register.

B
Loads the processor B register into the test panel ADDRESS register.

P
Loads the processor P register into the test panel ADDRESS register.

Z
Loads the test panel ADDRESS register from the processor MA register.
DMA-SEL (Continued)

When the DMA-SEL switch is in positions 3 through 9, the A, B, P, and Z registers of the selected DMA device can be loaded into the test panel ADDRESS register for display. Or the contents of the test panel ADDRESS register can be loaded into the register of the selected DMA device.

DMA SET TO 1 OR 2

The following definitions apply only when the DMA-SEL switch is set to 1 or 2.

RD  Reads the contents of the selected processor register into the test panel DATA register.

WR  Loads the contents of the test panel DATA register into the selected processor register.

Processor registers are selected as follows:

<table>
<thead>
<tr>
<th>Test Panel ADDR reg. (in binary)</th>
<th>DMA-SEL=1</th>
<th>DMA-SEL=2</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>P reg. lower bits</td>
<td>TD reg.</td>
</tr>
<tr>
<td>01</td>
<td>P reg. upper bits</td>
<td>XD reg.</td>
</tr>
<tr>
<td>02</td>
<td>A reg. lower bits</td>
<td>FC reg.</td>
</tr>
<tr>
<td>03</td>
<td>A reg. upper bits</td>
<td>DC reg.</td>
</tr>
<tr>
<td>04</td>
<td>B reg. lower bits</td>
<td>CC reg.</td>
</tr>
<tr>
<td>05</td>
<td>B reg. upper bits</td>
<td>VZ/EC reg.</td>
</tr>
<tr>
<td>06</td>
<td>T reg. lower bits</td>
<td>OP/V1 reg.</td>
</tr>
<tr>
<td>07</td>
<td>T reg. upper bits</td>
<td>MS reg.</td>
</tr>
<tr>
<td>10</td>
<td>Zero reg.</td>
<td></td>
</tr>
<tr>
<td>11</td>
<td>Base reg. 0 lower 8 bits</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>Base reg. 0 middle 8 bits</td>
<td></td>
</tr>
<tr>
<td>13</td>
<td>Base reg. 0 upper bit</td>
<td></td>
</tr>
<tr>
<td>14</td>
<td>Zero reg.</td>
<td></td>
</tr>
<tr>
<td>15</td>
<td>Base reg. 1 lower 8 bits</td>
<td></td>
</tr>
<tr>
<td>16</td>
<td>Base reg. 1 middle 8 bits</td>
<td></td>
</tr>
<tr>
<td>17</td>
<td>Base reg. 1 upper bit</td>
<td></td>
</tr>
<tr>
<td>18</td>
<td>Zero reg.</td>
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</tr>
<tr>
<td>19</td>
<td>Base reg. 2 lower 8 bits</td>
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</tr>
<tr>
<td>1A</td>
<td>Base reg. 2 middle 8 bits</td>
<td></td>
</tr>
<tr>
<td>1B</td>
<td>Base reg. 2 upper bit</td>
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<tr>
<td>1C</td>
<td>Zero reg.</td>
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<tr>
<td>1D</td>
<td>Base reg. 3 lower 8 bits</td>
<td></td>
</tr>
<tr>
<td>1E</td>
<td>Base reg. 3 middle 8 bits</td>
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</tr>
<tr>
<td>1F</td>
<td>Base reg. 3 upper bit</td>
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</table>
Q29B

CENTRAL PROCESSING UNIT

The Q29B logic assembly serves as the CPU for the QANTEL 970 and 1450 Systems. It is a single board CPU, utilizing bipolar bit slice microprocessing elements, field programmable logic arrays (FPLA), and a pipelined architecture. It contains ROM microcode which interprets the standard QANTEL instruction set. The Q29B is slightly faster than the Q7.5 processor and introduces a main memory capability of one megabyte (1024K).

No Direct-Memory Access (DMA) controllers are supported on the Q29B. In particular the controllers and their replacements are:

<table>
<thead>
<tr>
<th>DMA CONTROLLER</th>
<th>REPLACEMENT</th>
</tr>
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<tbody>
<tr>
<td>IOU-15 Communications</td>
<td>IOU-40</td>
</tr>
<tr>
<td>IOU-28 QCRT</td>
<td>IOU-39T</td>
</tr>
<tr>
<td>IOU-32 Memorex Disc</td>
<td>Non-DMA IOU-32</td>
</tr>
</tbody>
</table>

MACHINE LANGUAGE COMPATIBILITY

- All machine instructions available on the Q7.5 processor are implemented on the Q29B. The condition switches (NZ, MI, OV) are set as on the Q7.5.

- The Load Decimal instruction sets the Nonzero switch if the field being moved is not equal to decimal zeros.

- There are two new instructions which enable and disable I/O interrupts. Location $12$ (bit 2**7) is no longer used as the interrupt enable switch.

- There are sixteen 20-bit base registers as compared to four 17 bit registers in the Q7.5. This allows a main memory capacity of 1024K. The old Set Bank and Load Base Register instructions are supported in addition to new base register instructions.
Q29B

Q298 IPL FEATURE

At power-on or when the IPL button is pressed, the CPU performs the following operations:

1) Loads its base registers to point to the first 32K of main memory (banks A/C).
2) Resets all I/O controllers (0-F).
3) Reads from device 0.
4) Branches to fetch the first QANTEL instructions.

The data entered on device 0 may have many forms. If the operator only presses TRANSNIT, the CPU will bootstrap to disc 00. If the entered data consists of two hexadecimal characters, the CPU bootstraps to the disc with this device number (eg, 10 or 0C).

Otherwise, the data should be hexadecimal QANTEL machine language instructions. The data is packed, two hex digits per byte, and placed starting at location 0 in memory. Non-hex characters (except Q and ') are ignored. Execution of QANTEL code begins at location 0.

If a Q is encountered, then the next 4 characters form a hexadecimal address. This address is used as the location to place the subsequent data. As a special case, if Q is the first character entered, then the hex address following the Q is used as the execution start address for the QANTEL machine code. The CPU branches to fetch instructions from the address specified after the Q.

If an apostrophe (') is encountered, then the following data characters are interpreted as ASCII and placed directly into memory (with no packing). No characters are ignored. ASCII mode is terminated by another apostrophe.
# Q29B Machine Language Instruction Set

<table>
<thead>
<tr>
<th>Object Code</th>
<th>Instruction</th>
<th>Mnemonic</th>
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</thead>
<tbody>
<tr>
<td>xxxxF</td>
<td>Read</td>
<td>RD</td>
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<tr>
<td>ccccf2</td>
<td>Read Hex</td>
<td>RHX</td>
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<tr>
<td>ccccf3</td>
<td>Read With Count</td>
<td>RHC</td>
</tr>
<tr>
<td>ccccf4</td>
<td>CRC Calculation</td>
<td>CRC</td>
</tr>
<tr>
<td>[bbbbfl]</td>
<td>Add Decimal</td>
<td>ADD</td>
</tr>
<tr>
<td>[bbbbfl]</td>
<td>Subtract Decimal</td>
<td>SBD</td>
</tr>
<tr>
<td>[bbbbfl]</td>
<td>Multiply Decimal</td>
<td>MDP</td>
</tr>
<tr>
<td>[bbbbfl]</td>
<td>Divide Decimal</td>
<td>DIV</td>
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<tr>
<td>[bbbbfl]</td>
<td>Load Decimal</td>
<td>LD</td>
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<tr>
<td>bbbbf6</td>
<td>Store Accumulator</td>
<td>STA</td>
</tr>
<tr>
<td>bbbbfL</td>
<td>Move</td>
<td>MOV</td>
</tr>
<tr>
<td>[bbbbfl]</td>
<td>Compare</td>
<td>CHP</td>
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<tr>
<td>bbbbf8</td>
<td>Seek</td>
<td>SEQ</td>
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<tr>
<td>bbbbf2</td>
<td>Disc Bootstrapping</td>
<td>DBS</td>
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<tr>
<td>bbbbf3</td>
<td>Load Base Register</td>
<td>LBR</td>
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<tr>
<td>bbbbf4</td>
<td>Translate</td>
<td>TRN</td>
</tr>
<tr>
<td>bbbbf5</td>
<td>Search Equal</td>
<td>SEQ</td>
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<tr>
<td>[bbbbfl]</td>
<td>Compare Numeric</td>
<td>CN</td>
</tr>
<tr>
<td>[bbbbfl]</td>
<td>Compare Zone</td>
<td>CZ</td>
</tr>
<tr>
<td>bbbbf6</td>
<td>Read Status 2</td>
<td>RS2</td>
</tr>
<tr>
<td>bbbbf7</td>
<td>Load Immediate</td>
<td>LD1</td>
</tr>
<tr>
<td>bbbbf8</td>
<td>Add Immediate</td>
<td>ADI</td>
</tr>
<tr>
<td>bbbbf9</td>
<td>Subtract Immediate</td>
<td>SBI</td>
</tr>
<tr>
<td>bbbbf10</td>
<td>Move Immediate</td>
<td>MVI</td>
</tr>
<tr>
<td>ccccf0</td>
<td>Read Base Register</td>
<td>RBR</td>
</tr>
<tr>
<td>ccccf2</td>
<td>Translate Right</td>
<td>TR</td>
</tr>
<tr>
<td>ccccf3</td>
<td>Scan Unequal Right</td>
<td>SNR</td>
</tr>
<tr>
<td>ccccf4</td>
<td>Scan Equal Right</td>
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<td>ccccf5</td>
<td>Fill Right</td>
<td>FR</td>
</tr>
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<td>ccccf6</td>
<td>Move Right</td>
<td>MR</td>
</tr>
<tr>
<td>ccccf7</td>
<td>Exchange Right</td>
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<td>Compare Right</td>
<td>CR</td>
</tr>
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<td>Translate Left</td>
<td>TL</td>
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<td>ccccfA</td>
<td>Hex To Ascii Conversion</td>
<td>HAC</td>
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<td>ccccfB</td>
<td>Scan Unequal Left</td>
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</tr>
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<td>Scan Equal Left</td>
<td>SEL</td>
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<td>ccccfF</td>
<td>Exchange Left</td>
<td>XL</td>
</tr>
<tr>
<td>ccccfG</td>
<td>Compare Left</td>
<td>CL</td>
</tr>
<tr>
<td>bbbbf0</td>
<td>Scan Interrupt</td>
<td>SCI</td>
</tr>
<tr>
<td>bbbbf1</td>
<td>Load Base Register &amp; Branch</td>
<td>LBB</td>
</tr>
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</table>

[ ] Fields enclosed in parentheses are optional.

N Instructions marked with an N are new.
### Q298 MACHINE LANGUAGE INSTRUCTION SET Continued

<table>
<thead>
<tr>
<th>OBJECT CODE</th>
<th>INSTRUCTION</th>
<th>MNEMONIC</th>
</tr>
</thead>
<tbody>
<tr>
<td>ssssf9</td>
<td>Return From Subroutine</td>
<td>RET</td>
</tr>
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<td>ssssf8</td>
<td>Subroutine Call</td>
<td>SUB</td>
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<tr>
<td>ssssf1</td>
<td>Pull Data</td>
<td>PUL</td>
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<tr>
<td>ssssf2</td>
<td>Push Data</td>
<td>PSH</td>
</tr>
<tr>
<td>ssssf3</td>
<td>Pop Data</td>
<td>POP</td>
</tr>
<tr>
<td>tttttf9</td>
<td>Increment By 1</td>
<td>IN1</td>
</tr>
<tr>
<td>bbbbf1</td>
<td>Branch On Table</td>
<td>BT</td>
</tr>
<tr>
<td>ccccf9</td>
<td>Branch On Overflow</td>
<td>BOF</td>
</tr>
<tr>
<td>ccccf1</td>
<td>Branch On Minus</td>
<td>BNI</td>
</tr>
<tr>
<td>ccccf1</td>
<td>Branch On Nonzero</td>
<td>BNE</td>
</tr>
<tr>
<td>ccccf1</td>
<td>Branch On Zero</td>
<td>BZ</td>
</tr>
<tr>
<td>ccccf1</td>
<td>Branch On Not Minus</td>
<td>BNM</td>
</tr>
<tr>
<td>ccccf1</td>
<td>Branch On No Overflow</td>
<td>BNO</td>
</tr>
<tr>
<td>ccccf1</td>
<td>Branch Unconditional</td>
<td>BRU</td>
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Q29B  MACHINE LANGUAGE INSTRUCTION SET Continued

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<thead>
<tr>
<th>OBJECT CODE</th>
<th>INSTRUCTION</th>
<th>MNEMONIC</th>
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<tbody>
<tr>
<td>aaaaA8</td>
<td>Halt &amp; Branch</td>
<td>HLT</td>
</tr>
<tr>
<td>aaaaA9</td>
<td>Branch &amp; Link</td>
<td>BLI</td>
</tr>
<tr>
<td>aaaaAA</td>
<td>Branch On Plus</td>
<td>BP</td>
</tr>
<tr>
<td>xxxxC</td>
<td>Set Bank C</td>
<td>BKC</td>
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<tr>
<td>xxxxD</td>
<td>Set Bank D</td>
<td>B KD</td>
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<tr>
<td>xxxxE</td>
<td>Set Bank E</td>
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<tr>
<td>aaaaAF</td>
<td>Branch On Not Plus</td>
<td>BNP</td>
</tr>
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<td>aaaaBd</td>
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<td>SHA</td>
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<td>ccccF2</td>
<td>aaaaBd</td>
<td>SHA</td>
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<td>ccccF3</td>
<td>aaaaBd</td>
<td>SHA</td>
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<td>N ccccF4</td>
<td>aaaaBd</td>
<td>SHA</td>
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<td>N [bbbbF1]</td>
<td>aaaaCL 02</td>
<td>LDB</td>
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<td>N [bbbbF1]</td>
<td>aaaaCL 03</td>
<td>MPB</td>
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<td>N [bbbbF1]</td>
<td>aaaaCL 04</td>
<td>DVB</td>
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<td>N [bbbbF1]</td>
<td>aaaaCL 05</td>
<td>BOV</td>
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<td>N [bbbbF1]</td>
<td>aaaaCL 06</td>
<td>DBV</td>
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<td>N aaaaCx</td>
<td>aaaaCL 07</td>
<td>MID</td>
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<tr>
<td>N bbbbfm</td>
<td>aaaaCL 07</td>
<td>KSR</td>
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<td>cccc ddee</td>
<td>aaaaCL</td>
<td>ADB</td>
</tr>
<tr>
<td>[bbbbF1]</td>
<td>aaaaCL 07</td>
<td>SBB</td>
</tr>
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</table>

Q29B MACHINE LANGUAGE - NEW INSTRUCTION SUMMARY

- CRC - CRC Calculation
- MVI - Move Immediate
- HAC - Hex-To-ASCII Conversion
- BT - Branch On Table
- LDR - Loader Relocation
- LBL - Load Base List
- RBL - Read Base List
- DI - Disable Interrupts
- EI - Enable Interrupts
- RCH - Read Check
- LDB - Load Binary
- MPB - Multiply Binary
- DVB - Divide Binary
- BOV - Binary To Decimal Conversion
- DBV - Decimal To Binary Conversion
- MID - Machine Identification
- KSR - Key Search
BASE REGISTERS AND MEMORY ADDRESSING

The Q29B system uses base register addressing. There are 16 base registers with 20 bits each for one megabyte of addressing capability. The base registers are numbered 0 - F.

Any memory address referenced in a Qantel machine instruction consists of 16 bits. Bit 15 is the indirect addressing bit which is resolved by the microcode (and is meaningless to the hardware). Bits 11-14 from the memory address are used as the base register selector. Bits 0-10 are used as the displacement added to the base register contents. Displacements are 11 bits in length, giving 2K memory partitioning.

On every memory reference the hardware adds the 11-bit displacement to the 20-bit contents of the base register selected to form the actual 20-bit memory address.

\[
\begin{array}{c}
\text{MEMORY ADDRESS} \\
\begin{array}{cccc}
\text{ibbb} & \text{bddd} & \text{dddd} & \text{dddd} \\
\hline
\text{Base Register Selector} \\
\hline
\text{Displacement} \\
\hline
\text{Indirect Bit}
\end{array}
\end{array}
\]
The first MEM 6A/B module should be placed in slot number one and the following MEM 6A/B's in numerical sequence.
MEM 6A/B

The MEM 6A/B is used with the Q298 CPU exclusively. It consists of a MEM6A board assembly together with from one to eight MEM6B modules. The MEM6B module is organized in 32K nine-bit words (eight data bits and one parity bit). Each MEM6A assembly can accommodate up to eight 32K MEM6B modules for a maximum of 256K bytes of memory per MEM6A board assembly.

The Q298 CPU has the capability of addressing up to 1 megabyte of memory. The QANTEL 1450 can accommodate up to four fully loaded MEM6A assemblies, for a maximum of 1 megabyte of memory per system. The QANTEL 970 System has one memory slot (256K).

The addressing of the MEM6A assemblies is location sensitive to the backplane, which can accommodate from one to four MEM6A board assemblies (refer to backplane illustration on previous page). The addressing of the MEM6B modules (0-7) on each MEM6A board assembly is shown below, and is listed more completely on the following page.

MEM 6A/B PARITY FAULT INDICATOR

P1

4 160K
(20000 - 27FFF)

5 192K
(28000 - 2FFFF)

6 224K
(30000 - 37FFF)

7 256K
(38000 - 3FFFF)

P2

0 32K
(00000 - 07FFF)

1 64K
(08000 - 0FFFF)

2 96K
(10000 - 17FFF)

3 128K
(18000 - 1FFFF)

RESET SWITCH
## MEM 6A/B

### MEMORY MAP

<table>
<thead>
<tr>
<th>1st MEM 6A</th>
<th>2nd MEM 6A</th>
</tr>
</thead>
<tbody>
<tr>
<td>0) $00000 - 07FFF</td>
<td>0) $40000 - 47FFF</td>
</tr>
<tr>
<td>1) 08000 - 0FFFF</td>
<td>1) 48000 - 4FFFF</td>
</tr>
<tr>
<td>2) 10000 - 17FFF</td>
<td>2) 50000 - 57FFF</td>
</tr>
<tr>
<td>3) 18000 - 1FFFF</td>
<td>3) 58000 - 5FFFF</td>
</tr>
<tr>
<td>4) 20000 - 27FFF</td>
<td>4) 60000 - 67FFF</td>
</tr>
<tr>
<td>5) 28000 - 2FFFF</td>
<td>5) 68000 - 6FFFF</td>
</tr>
<tr>
<td>6) 30000 - 37FFF</td>
<td>6) 70000 - 77FFF</td>
</tr>
<tr>
<td>7) 38000 - 3FFFF</td>
<td>7) 78000 - 7FFFF</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>3rd MEM 6A</th>
<th>4th MEM 6A</th>
</tr>
</thead>
<tbody>
<tr>
<td>0) $80000 - 87FFF</td>
<td>0) $C0000 - C7FFF</td>
</tr>
<tr>
<td>1) 88000 - 8FFFF</td>
<td>1) C8000 - CFFFF</td>
</tr>
<tr>
<td>2) 90000 - 97FFF</td>
<td>2) D0000 - D7FFF</td>
</tr>
<tr>
<td>3) 98000 - 9FFFF</td>
<td>3) D8000 - DFFFF</td>
</tr>
<tr>
<td>4) A0000 - A7FFF</td>
<td>4) E0000 - E7FFF</td>
</tr>
<tr>
<td>5) A8000 - AFFFF</td>
<td>5) E8000 - EFFFF</td>
</tr>
<tr>
<td>6) B0000 - B7FFF</td>
<td>6) F0000 - F7FFF</td>
</tr>
<tr>
<td>7) B8000 - BFFFF</td>
<td>7) F8000 - FFFFF</td>
</tr>
</tbody>
</table>

8-25
MEM 6A/B

MEM 6A/B CONTINUED

• The Q298 CPU generates a 20-bit memory address. The two high order address bits, AD19-AD18, are decoded to generate one of four, BRD ENA-N, signals used to enable one of the four MEM6A board assemblies per system.

• The four address lines, AD17-AD14, go directly to each of the MEM6A board assemblies. Each MEM6B module consists of two banks of nine 16K dynamic RAMs for a total of eighteen dynamic RAMs and 32K 9-bit words of memory per MEM6B module. The 16K dynamic RAMs employ a seven-bit multiplexed address in which a seven-bit row address and a seven-bit column address are latched into the RAM at different times by the row address strobe, RAS-N, and the column address strobe, CAS-N, respectively. The four non-multiplexed address bits, AD17-AD14, are decoded by the MEM6A board assembly to generate the row address strobe, RAS-N, for one of the sixteen possible 16K banks of RAM per MEM6A board assembly. The row address strobe acts as a chip select enabling the subsequent memory operation in the 16K bank which is selected.

• Fourteen address lines are multiplexed on the backplane in two 7-bit bytes, AD13-AD07 and AD06-AD00, which are latched and buffered on the MEM6A assembly and go directly to the 16K dynamic RAMs on the MEM6B modules.

• The refresh timing is generated by the Q298 processor. A refresh cycle is performed every 16 microseconds maximum. The refresh address counter is provided by the Q298 processor, which generates an 128-cycle refresh every 2 milliseconds.

CONTROLS

• The MEMORY 6 SYSTEM responds to the I/O control signals from the backplane. The device address for selecting the MEMORY 6A board assembly is fixed at 5. If more than one MEM6A board assembly is in the backplane, all MEM 6A/B boards will be selected when the CPU selects device address 5.
MEM 6A/B

CONTROLS CONTINUED

• The MEM6A board assembly responds to Write Control, Read Status 2, and Reset I/O. The write control byte is defined below:

  DA00 - Reset Parity Enable
  DA01 - Set Parity Enable - Enables Parity Check
  DA02 - Reset Indicator Enable
  DA03 - Set Indicator Enable - Enables Parity Fault Indicator
  DA04 - Reset Parity Swap
  DA05 - Set Parity Swap - Exchanges parity RAM with least significant data bit for testing purposes.
  DA06 - Reset Parity Fault - Resets parity flip-flop.

• The Read Status 0 response from the MEM 6A is $F4.

• The Read Status 2 response from the MEM6A is $F6.

• The Reset I/O resets 'Parity Enable', 'Indicator Enable', 'Parity Swap', and 'Parity Fault'. Power-On Reset performs the same functions.

PARITY

• The MEM6A/B provides byte parity for the detection of single bit errors in the memory. The parity bit is generated during a write cycle, even parity is always stored. The parity is checked during each read cycle. If parity checking is enabled and a parity is detected, the parity fault signal PAR FLT-N will be outputted on the backplane causing a system interrupt. If the parity indicator is enabled, a permanent magnetic indicator is set on the MEM6A board assembly and on the particular MEM6B module which experienced the error.

• The magnetic indicator on the MEM6B module is a permanent record on the failed module and is not affected by power down or subsequent operation.

8-27
MEM 6A/B

PARITY CONTINUED

• The MEM6A board assembly has a momentary switch to reset the magnetic indicator on the MEM6A board when the defective module has been replaced.

• The parity fault signal can be reset by a write control instruction to memory (see control codes given on previous page). The magnetic parity indicators are not affected by the reset of the parity fault. However, to manually reset the magnetic indicator on the MEM6A board assembly, it is necessary that the parity fault be reset.

• A parity swap function is provided for memory testing purposes. The parity RAM is exchanged with the least significant data bit of main memory RAM on the entire MEM6A board assembly. This facilitates the testing of the parity RAM by normal ATP memory programs.
## BEST ERROR CODES

**ERROR CODES FOR 10x, 20x, 30x AND 40x OPERATING SYSTEMS**

**Effective: December 1, 1979**

<table>
<thead>
<tr>
<th>ERROR</th>
<th>DESCRIPTION</th>
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<tbody>
<tr>
<td>E02</td>
<td>END OF FILE</td>
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<tr>
<td>E03</td>
<td>DISC FULL</td>
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<tr>
<td>E04</td>
<td>KEYED FILE WRITE WITHOUT INDEX</td>
</tr>
<tr>
<td>E05</td>
<td>CREATE WITHOUT DISC LABEL</td>
</tr>
<tr>
<td>E06</td>
<td>DIRECTORY NOT FOUND</td>
</tr>
<tr>
<td>E07</td>
<td>FILE UNAVAILABLE--LOCK/UNLOCK ERROR</td>
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<tr>
<td>E08</td>
<td>ATTEMPTED LOCK ON UNOPENED LUN</td>
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<tr>
<td>E09</td>
<td>ATTEMPTED WRITE/DELETE TO DIRECTORY</td>
</tr>
<tr>
<td>E10</td>
<td>FILE HEADER UNAVAIL FOR RE-WRITE</td>
</tr>
<tr>
<td>E11</td>
<td>FILE NOT FOUND</td>
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<tr>
<td>E12</td>
<td>FILE ALREADY EXISTS</td>
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<tr>
<td>E13</td>
<td>NO FREE DIRECTORY ENTRIES</td>
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<tr>
<td>E14</td>
<td>DIR HEADER UNAVAIL FOR RENAME</td>
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<tr>
<td>E19</td>
<td>SPOOL DIRECTORY HEADER OPEN ERROR</td>
</tr>
<tr>
<td>E20</td>
<td>SPOOL JOB FILE OVERFLOW</td>
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<tr>
<td>E21</td>
<td>SPOOL JOB CREATE ERROR</td>
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<tr>
<td>E22</td>
<td>MISSING DEVICE/SPOOL QUE CREATE ERR</td>
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<tr>
<td>E23</td>
<td>WRONG DEVICE TYPE/SPOOL QUE OPEN ERR</td>
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<tr>
<td>E24</td>
<td>CARD READ ERR/SPOOL CONTROL MISSING</td>
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<td>E25</td>
<td>CARD READER ERR/SPOOL QUE FULL</td>
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<td>E26</td>
<td>EOF/SPOOL QUE JOB RECORD WRITE ERR</td>
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<td>TAPE ERROR/SPOOL QUE JOB WRITE ERR</td>
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<td>VFU ERROR/SPOOL JOB OPEN ERROR</td>
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<tr>
<td>E29</td>
<td>LOAD READ ERROR/SPOOL BUFFER UNAVAIL</td>
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<td>DEVICE INOPERATIVE</td>
</tr>
<tr>
<td>E31</td>
<td>DEVICE UNAVAILABLE</td>
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<tr>
<td>E32</td>
<td>KEY NOT FOUND</td>
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<tr>
<td>E33</td>
<td>RECORD UNAVAILABLE--EXTRACT ERROR</td>
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<td>E34</td>
<td>LOGICAL UNIT # UNAVAILABLE FOR OPEN</td>
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<td>FILE NOT OPEN FOR READ OR WRITE</td>
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<td>E36</td>
<td>CANNOT ERASE OPEN FILE</td>
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<tr>
<td>E37</td>
<td>DISC UNAVAILABLE FOR GET OR PUT</td>
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<tr>
<td>E38</td>
<td>DEVICE / FILE CAN'T PERFORM FUNCTION</td>
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<tr>
<td>E39</td>
<td>DISC UNAVAILABLE FOR LOCK OR UNLOCK</td>
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<tr>
<td>E41</td>
<td>EDIT MASK LENGTH INCORRECT</td>
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<tr>
<td>E42</td>
<td>RESERVE AMT GT 30K</td>
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<tr>
<td>E43</td>
<td>OUT OF RESERVABLE MEMORY</td>
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<td>E44</td>
<td>I/O BUFFER OVERFLOW</td>
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<td>E46</td>
<td>NON-NUMERIC INPUT IN NUMERIC FIELD</td>
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<td>E47</td>
<td>PARAMETER TOO LARGE</td>
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<td>E48</td>
<td>INVALID KEY OR RECORD SIZE: CREATE</td>
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<td>E50</td>
<td>ARRAY SUBSCRIPT OUT OF RANGE</td>
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<td>E51</td>
<td>DIVIDE OVERFLOW</td>
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<td>E53</td>
<td>INVALID FILE ACCESS</td>
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<tr>
<td>E54</td>
<td>CANNOT DELETE IN NON-KEYED FILE</td>
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<tr>
<td>E60</td>
<td>CANNOT IPL WHILE OTHER USERS ACTIVE</td>
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</table>
ERROR CODES FOR 10x, 20x, 30x AND 40x OPERATING SYSTEMS
Effective: December 1, 1979

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<thead>
<tr>
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<th>DESCRIPTION</th>
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<td>E51</td>
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<tr>
<td>E62</td>
<td>PROGRAM TOO LARGE FOR PARTITION</td>
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<td>E63</td>
<td>CANNOT RUN NON-OBJECT FILE</td>
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<tr>
<td>E64</td>
<td>INVALID PARTITION NAME</td>
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<tr>
<td>E65</td>
<td>PARTITION BUSY-PROGRAM RUNNING</td>
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<tr>
<td>E66</td>
<td>PARTITION NOT BACKGROUND</td>
</tr>
<tr>
<td>E67</td>
<td>PARTITION BUSY-NORMAL TERMINATION</td>
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<tr>
<td>E69</td>
<td>PARTITION BUSY-ERROR TERMINATION</td>
</tr>
<tr>
<td>E75</td>
<td>AFL LAST REC SECT DOESN'T MATCH FILE</td>
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<tr>
<td>E76</td>
<td>AFL POINTER STACK OVERFLOW</td>
</tr>
<tr>
<td>E77</td>
<td>FILE ACCESS ON INOP DISC</td>
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<tr>
<td>E78</td>
<td>DISC DESCRIPTOR UNAVAILABLE</td>
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<tr>
<td>E79</td>
<td>FEATURE UNAVAILABLE ON THIS SYSTEM</td>
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<tr>
<td>E80</td>
<td>MAXIMUM I/O BUFFER SIZE EXCEEDED</td>
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<td>E81</td>
<td>SECTOR ALLOCATION ERROR - CREATE</td>
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<tr>
<td>E82</td>
<td>TOP KEY SECTOR NOT IN DELETE STACK</td>
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<tr>
<td>E83</td>
<td>DISC ERROR</td>
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<td>E84</td>
<td>KEY SECTOR SEARCH LEVEL IMPOSSIBLE</td>
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<tr>
<td>E85</td>
<td>INVALID REAL PARAMETERS</td>
</tr>
<tr>
<td>E86</td>
<td>ACTIVE FILE LIST OVERFLOW</td>
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<tr>
<td>E87</td>
<td>DISC ERROR DURING ERASE OR OPEN</td>
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<tr>
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<td>INVALID AFL RECORD SECTOR OFFSET</td>
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<tr>
<td>E89</td>
<td>DIRECTORY INSERT ERROR - CREATE</td>
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<tr>
<td>E90</td>
<td>SYSTEM RETURN STACK OVER/UNDERFLOW</td>
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<tr>
<td>E91</td>
<td>INVALID LOAD ITEM IN OBJECT FILE</td>
</tr>
<tr>
<td>E92</td>
<td>GOSUB STACK UNDERFLOW</td>
</tr>
<tr>
<td>E93</td>
<td>GOSUB STACK OVERFLOW</td>
</tr>
<tr>
<td>E94</td>
<td>DIRECTORY ENTRY UNAVAILABLE / CLOSE</td>
</tr>
<tr>
<td>E95</td>
<td>NEXT KEY UNAVAILABLE FOR DELETION</td>
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<tr>
<td>E96</td>
<td>NEW KEY FOUND DURING INSERT</td>
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<td>E97</td>
<td>NEW FIRST KEY FOUND DURING INSERT</td>
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<tr>
<td>E98</td>
<td>EXTRACTED RECORD NOT FOUND IN TABLE</td>
</tr>
<tr>
<td>E99</td>
<td>GET/RETURN SECTOR OUT OF DISC LIMITS</td>
</tr>
</tbody>
</table>
BEST DISC ORGANIZATION

TO BEST 13/14.9.10

SYSTEM PACK

<table>
<thead>
<tr>
<th>Sector Range</th>
<th>Description</th>
</tr>
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<tbody>
<tr>
<td>0-2</td>
<td>System Loader</td>
</tr>
<tr>
<td>3-4</td>
<td>Allocation Bit Map</td>
</tr>
<tr>
<td>5-9</td>
<td>Beginning of User File Directory</td>
</tr>
<tr>
<td>10-299</td>
<td>Core Image Programs</td>
</tr>
<tr>
<td>300-999</td>
<td>Operating System</td>
</tr>
<tr>
<td>1000-...</td>
<td>User Files</td>
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</tbody>
</table>

USER PACK

<table>
<thead>
<tr>
<th>Sector Range</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>0-2</td>
<td>System Loader</td>
</tr>
<tr>
<td>3-4</td>
<td>Allocation Bit Map</td>
</tr>
<tr>
<td>5-9</td>
<td>Beginning of User File Directory</td>
</tr>
<tr>
<td>10-39</td>
<td>Core Image Programs</td>
</tr>
<tr>
<td>40-..</td>
<td>User Files</td>
</tr>
</tbody>
</table>

SIMPLE DFULL USES

<table>
<thead>
<tr>
<th>Command</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SD1D (or OD)</td>
<td>Sets device used by following instructions.</td>
</tr>
<tr>
<td>RD 1000,300</td>
<td>Read 300 (hex) bytes from the selected disc to position 1000 in memory.</td>
</tr>
<tr>
<td>DP 1000,300</td>
<td>List on (dump to) the printer 300 bytes starting at location 1000 in memory.</td>
</tr>
<tr>
<td>SDA xxxxx</td>
<td>Set disc address (enter 5-digit sector address).</td>
</tr>
<tr>
<td>EA 1000,7</td>
<td>Exhibit ASCII translation for 7 bytes starting at location 1000 in memory.</td>
</tr>
<tr>
<td>EH 1000,7</td>
<td>Exhibit the hexadecimal contents of 7 bytes from location 1000 in memory.</td>
</tr>
</tbody>
</table>

10-3  3/10/76
### BEST OPERATING SYSTEMS

#### DISC LAYOUT

**SYSTEM 30X**

<table>
<thead>
<tr>
<th>SECTOR NUMBER</th>
<th>SYSTEM PACK</th>
<th>SECTOR NUMBER</th>
<th>DATA PACK</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - 3</td>
<td>Disc Program Loader</td>
<td>0 - 1</td>
<td>Disc Program Loader</td>
</tr>
<tr>
<td>4</td>
<td>CoreImage Directory</td>
<td>5</td>
<td>Disc Description Record</td>
</tr>
<tr>
<td>6</td>
<td>AU Bit Map</td>
<td>6</td>
<td>AU Bit Map</td>
</tr>
<tr>
<td>7 - 9</td>
<td>Directory's keys</td>
<td>7 - 9</td>
<td>Directory's keys</td>
</tr>
<tr>
<td>10 - 14</td>
<td>First Directory's records</td>
<td>2 - 4</td>
<td>First Directory's records</td>
</tr>
<tr>
<td>15 - 499</td>
<td>BEST Coreimages</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>500 - 814</td>
<td>BEST files</td>
<td>10 - 814</td>
<td>BEST files</td>
</tr>
</tbody>
</table>

**NEW VERSION:**

**System 30X**

<table>
<thead>
<tr>
<th>SECTOR NUMBER</th>
<th>SYSTEM PACK</th>
<th>SECTOR NUMBER</th>
<th>DATA PACK</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - 3</td>
<td>Disc Program Loader</td>
<td>0 - 1</td>
<td>Disc Program Loader</td>
</tr>
<tr>
<td>4</td>
<td>CoreImage Directory</td>
<td>5</td>
<td>Disc Description Record</td>
</tr>
<tr>
<td>6</td>
<td>AU Bit Map</td>
<td>6</td>
<td>AU Bit Map</td>
</tr>
<tr>
<td>7 - 9</td>
<td>Directory's keys</td>
<td>7 - 9</td>
<td>Directory's keys</td>
</tr>
<tr>
<td>10 - 14</td>
<td>Directory's records</td>
<td>2 - 4</td>
<td>Directory's records</td>
</tr>
<tr>
<td>15 - 499</td>
<td>BEST Coreimages</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>500 - 814</td>
<td>BEST files</td>
<td>10 - 814</td>
<td>BEST files</td>
</tr>
</tbody>
</table>

**SYSTEM 40X**

<table>
<thead>
<tr>
<th>SECTOR NUMBER</th>
<th>SYSTEM PACK</th>
<th>SECTOR NUMBER</th>
<th>DATA PACK</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - 3</td>
<td>Disc Program Loader</td>
<td>0 - 3</td>
<td>Disc Program Loader</td>
</tr>
<tr>
<td>4</td>
<td>CoreImage Directory</td>
<td>4</td>
<td>CoreImage Directory</td>
</tr>
<tr>
<td>5</td>
<td>Disc Description Record &amp; beginning of multiple directory description records</td>
<td>5</td>
<td>Disc Description Record &amp; beginning of multiple directory description records</td>
</tr>
<tr>
<td>6 - 9</td>
<td>Multiple directory Description Records</td>
<td>6 - 9</td>
<td>Multiple Directory Description Records</td>
</tr>
<tr>
<td>10 - 14</td>
<td>AU Bit Map</td>
<td>10 - 14</td>
<td>AU Bit Map</td>
</tr>
<tr>
<td>15 - 399</td>
<td>BEST Coreimages</td>
<td>15 - 399</td>
<td>Selected BEST Coreimages</td>
</tr>
<tr>
<td>999</td>
<td></td>
<td>40+</td>
<td>Multiple directories &amp; all BEST files</td>
</tr>
<tr>
<td>1000+</td>
<td>Multiple directories &amp; all BEST files</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
**BEST OPERATING SYSTEMS**

**DISC LAYOUT**

The BEST Operating System defines its reserved space on a disc as follows:

### SYSTEM 10X

<table>
<thead>
<tr>
<th>SECTOR NUMBER</th>
<th>SYSTEM PACK</th>
<th>SECTOR NUMBER</th>
<th>DATA PACK</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - 3</td>
<td>Disc Program Loader</td>
<td>0 - 3</td>
<td>Disc Program Loader</td>
</tr>
<tr>
<td>4</td>
<td>CoreImage Directory</td>
<td>4</td>
<td>CoreImage Directory</td>
</tr>
<tr>
<td>5</td>
<td>Disc Description Record</td>
<td>5</td>
<td>Disc Description Record</td>
</tr>
<tr>
<td></td>
<td>one directory description record</td>
<td></td>
<td>one directory description record</td>
</tr>
<tr>
<td>6 - 9</td>
<td>Not used</td>
<td>6 - 9</td>
<td>Not used</td>
</tr>
<tr>
<td>10 - 14</td>
<td>AU Bit Map</td>
<td>10 - 14</td>
<td>AU Bit Map</td>
</tr>
<tr>
<td>15</td>
<td>BEST Coreimages</td>
<td>15 - 19</td>
<td>Selected BEST Coreimages</td>
</tr>
<tr>
<td>999</td>
<td>One directory file</td>
<td>40+</td>
<td>One directory file</td>
</tr>
<tr>
<td>1000+</td>
<td>all BEST files</td>
<td></td>
<td>all BEST files</td>
</tr>
</tbody>
</table>

### SYSTEM 20X

<table>
<thead>
<tr>
<th>SECTOR NUMBER</th>
<th>SYSTEM PACK</th>
<th>SECTOR NUMBER</th>
<th>DATA PACK</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 - 3</td>
<td>Disc Program Loader</td>
<td>0 - 3</td>
<td>Disc Program Loader</td>
</tr>
<tr>
<td>4</td>
<td>CoreImage Directory</td>
<td>4</td>
<td>CoreImage Directory</td>
</tr>
<tr>
<td>5</td>
<td>Disc Description Record &amp; beginning of multiple directory description records</td>
<td>5</td>
<td>Disc Description Record &amp; beginning of multiple directory description records</td>
</tr>
<tr>
<td>6 - 9</td>
<td>Multiple Directory Description Records</td>
<td>6 - 9</td>
<td>Multiple Directory Description Records</td>
</tr>
<tr>
<td>10 - 14</td>
<td>AU Bit Map</td>
<td>10 - 14</td>
<td>AU Bit Map</td>
</tr>
<tr>
<td>15</td>
<td>BEST Coreimages</td>
<td>15 - 19</td>
<td>Selected BEST Coreimages</td>
</tr>
<tr>
<td>999</td>
<td>Multiple directories &amp; all BEST files</td>
<td>40+</td>
<td>Multiple directories &amp; all BEST files</td>
</tr>
<tr>
<td>1000+</td>
<td>all BEST files</td>
<td></td>
<td>all BEST files</td>
</tr>
</tbody>
</table>

10-5
DIRECTORY FORMAT
10X/20X/30X/40X SERIES OPERATING SYSTEMS

DISC DESCRIPTION RECORD

<table>
<thead>
<tr>
<th>POSITION</th>
<th>CONTENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 8</td>
<td>File Name - $FF 00 00 00 00 00 00 00</td>
</tr>
<tr>
<td>9 - 11</td>
<td>DKN date</td>
</tr>
<tr>
<td>12 - 14</td>
<td>Last BACKUP date</td>
</tr>
<tr>
<td>15</td>
<td>File type - $82</td>
</tr>
<tr>
<td>16 - 17</td>
<td>Directory record size - $003F</td>
</tr>
<tr>
<td>18 - 20</td>
<td>First directory of directories sector</td>
</tr>
<tr>
<td>21 - 22</td>
<td>Next record offset - $02F4</td>
</tr>
<tr>
<td>23 - 25</td>
<td>Last directory of directories sector</td>
</tr>
<tr>
<td>26 - 27</td>
<td>Bad Allocation Unit Count</td>
</tr>
<tr>
<td>28 - 39</td>
<td>Unused</td>
</tr>
<tr>
<td>40 - 41</td>
<td>Seek/Read errors</td>
</tr>
<tr>
<td>42 - 44</td>
<td>First disc sector that can be allocated</td>
</tr>
<tr>
<td>45 - 47</td>
<td>Last disc sector that can be allocated</td>
</tr>
<tr>
<td>48 - 50</td>
<td>First Allocation Unit Bit Map sector</td>
</tr>
<tr>
<td>51 - 53</td>
<td>Last Allocation Unit Bit Map sector</td>
</tr>
<tr>
<td>54</td>
<td>Fill character - $00</td>
</tr>
<tr>
<td>55 - 57</td>
<td>Number of records per sector - $303132</td>
</tr>
<tr>
<td>58 - 59</td>
<td>Unused</td>
</tr>
<tr>
<td>60</td>
<td>DKN Version Number</td>
</tr>
<tr>
<td>61 - 63</td>
<td>Disc Label</td>
</tr>
</tbody>
</table>

DIRECTORY DESCRIPTION RECORD

<table>
<thead>
<tr>
<th>POSITION</th>
<th>CONTENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 8</td>
<td>File Name - $FF xx xx xx 00 00 00 00</td>
</tr>
<tr>
<td>9 - 11</td>
<td>Create date</td>
</tr>
<tr>
<td>12 - 14</td>
<td>Update date</td>
</tr>
<tr>
<td>15</td>
<td>File type - $84</td>
</tr>
<tr>
<td>16 - 17</td>
<td>Record size - $003F</td>
</tr>
<tr>
<td>18 - 20</td>
<td>First record sector</td>
</tr>
<tr>
<td>21 - 22</td>
<td>Next record offset</td>
</tr>
<tr>
<td>23 - 25</td>
<td>Last record sector</td>
</tr>
<tr>
<td>26 - 27</td>
<td>Allocation Unit Count</td>
</tr>
<tr>
<td>28 - 45</td>
<td>Unused</td>
</tr>
<tr>
<td>46 - 48</td>
<td>Key sector delete chain sector</td>
</tr>
<tr>
<td>49 - 50</td>
<td>Record delete chain offset</td>
</tr>
<tr>
<td>51 - 53</td>
<td>Record delete chain sector</td>
</tr>
<tr>
<td>54</td>
<td>Keysize - $08</td>
</tr>
<tr>
<td>55 - 57</td>
<td>Last key sector</td>
</tr>
<tr>
<td>58 - 60</td>
<td>First Key sector</td>
</tr>
<tr>
<td>61 - 63</td>
<td>Top key sector</td>
</tr>
</tbody>
</table>
DIRECTORY FORMAT
10X/20X/30X/40X SERIES OPERATING SYSTEMS

CONTIGUOUS FILE

<table>
<thead>
<tr>
<th>POSITION</th>
<th>CONTENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 8</td>
<td>File Name</td>
</tr>
<tr>
<td>9 - 11</td>
<td>*Create Date</td>
</tr>
<tr>
<td>12 - 14</td>
<td>*Update Date</td>
</tr>
<tr>
<td>15</td>
<td>File Type =$02</td>
</tr>
<tr>
<td>16 - 17</td>
<td>Record Size</td>
</tr>
<tr>
<td>18 - 20</td>
<td>First Record Sector</td>
</tr>
<tr>
<td>21 - 22</td>
<td>Next Record Offset</td>
</tr>
<tr>
<td>23 - 25</td>
<td>Last Record Sector</td>
</tr>
<tr>
<td>26 - 27</td>
<td>Allocation Unit Count</td>
</tr>
<tr>
<td>28 - 53</td>
<td>Unused</td>
</tr>
<tr>
<td>54</td>
<td>Fill Character</td>
</tr>
<tr>
<td>55 - 57</td>
<td>Number of records/sector</td>
</tr>
<tr>
<td>58 - 63</td>
<td>Unused</td>
</tr>
</tbody>
</table>

*New items in the directory format

OBJECT/STANDALONE FILE

<table>
<thead>
<tr>
<th>POSITION</th>
<th>CONTENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 8</td>
<td>File Name</td>
</tr>
<tr>
<td>9 - 11</td>
<td>*Create Date</td>
</tr>
<tr>
<td>12 - 14</td>
<td>*Update date</td>
</tr>
<tr>
<td>15</td>
<td>File type =$10 - Object</td>
</tr>
<tr>
<td></td>
<td>File type =$30 - Standalone</td>
</tr>
<tr>
<td>16 - 17</td>
<td>Record Size</td>
</tr>
<tr>
<td>18 - 20</td>
<td>First Record Sector</td>
</tr>
<tr>
<td>21 - 22</td>
<td>Next Record Offset</td>
</tr>
<tr>
<td>23 - 25</td>
<td>Last Record Sector</td>
</tr>
<tr>
<td>26 - 27</td>
<td>*Allocation Unit Count</td>
</tr>
<tr>
<td>28 - 42</td>
<td>Unused</td>
</tr>
<tr>
<td>43</td>
<td>*Version Number</td>
</tr>
<tr>
<td>44 - 51</td>
<td>*Source File Name</td>
</tr>
<tr>
<td>52 - 53</td>
<td>*Message Address (Standalone only)</td>
</tr>
<tr>
<td>54 - 55</td>
<td>Execution Start Address</td>
</tr>
<tr>
<td>56 - 57</td>
<td>Common Length</td>
</tr>
<tr>
<td>58 - 59</td>
<td>Buffer Length</td>
</tr>
<tr>
<td>60 - 61</td>
<td>Local Length</td>
</tr>
<tr>
<td>62 - 63</td>
<td>Program Length</td>
</tr>
</tbody>
</table>

*New items in the directory format
DIRECTORY FORMAT
10X/20X/30X/40X SERIES OPERATING SYSTEM

KEYED FILE

<table>
<thead>
<tr>
<th>POSITION</th>
<th>CONTENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 8</td>
<td>File Name</td>
</tr>
<tr>
<td>9 - 11</td>
<td>*CREATE date</td>
</tr>
<tr>
<td>12 - 14</td>
<td>*Update date</td>
</tr>
<tr>
<td>15</td>
<td>File type - $04</td>
</tr>
<tr>
<td>16 - 17</td>
<td>Record Size</td>
</tr>
<tr>
<td>18 - 20</td>
<td>1st Record Sector</td>
</tr>
<tr>
<td>21 - 22</td>
<td>Next Record Sector Offset</td>
</tr>
<tr>
<td>23 - 25</td>
<td>Last Record Sector</td>
</tr>
<tr>
<td>26 - 27</td>
<td>Allocation Unit Count</td>
</tr>
<tr>
<td>28 - 45</td>
<td>Unused</td>
</tr>
<tr>
<td>46 - 48</td>
<td>Key Sector Delete Chain Sector</td>
</tr>
<tr>
<td>49 - 50</td>
<td>Record Delete Chain Offset</td>
</tr>
<tr>
<td>51 - 53</td>
<td>Record Delete Chain Sector</td>
</tr>
<tr>
<td>54</td>
<td>Key Size</td>
</tr>
<tr>
<td>55 - 57</td>
<td>Last Key Sector</td>
</tr>
<tr>
<td>58 - 60</td>
<td>First Key Sector</td>
</tr>
<tr>
<td>61 - 63</td>
<td>Top Key Sector</td>
</tr>
</tbody>
</table>

*New items in the directory format

SEQUENTIAL FILE

<table>
<thead>
<tr>
<th>POSITION</th>
<th>CONTENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 8</td>
<td>File Name</td>
</tr>
<tr>
<td>9 - 11</td>
<td>*Create Date</td>
</tr>
<tr>
<td>12 - 14</td>
<td>*Update Date</td>
</tr>
<tr>
<td>15</td>
<td>File Type - $01</td>
</tr>
<tr>
<td>16 - 17</td>
<td>Record Size</td>
</tr>
<tr>
<td>18 - 20</td>
<td>First Record Sector</td>
</tr>
<tr>
<td>21 - 22</td>
<td>Next Record Offset</td>
</tr>
<tr>
<td>23 - 25</td>
<td>Last Record Sector</td>
</tr>
<tr>
<td>26 - 27</td>
<td>Allocation Unit Count</td>
</tr>
<tr>
<td>28 - 63</td>
<td>Unused</td>
</tr>
</tbody>
</table>

*New items in the directory format
DIRECTORY ENTRIES

KEYED                      SEQUENTIAL

1-3                         1st DATA SECTOR
TOP KEY SECTOR              UNUSED
4                            UNUSED
5-12                         FILE NAME
FILE NAME                   FILE TYPE-04
13                           FILE TYPE-01
14-16                        LAST DATA SECTOR
LAST DATA SECTOR            LAST DATA SECTOR
17-18                        RECORD SIZE
RECORD SIZE                 RECORD SIZE
19-20                        AU COUNT
AU COUNT                    AU COUNT
21-23                        LAST KEY SECTOR
LAST KEY SECTOR             UNUSED
24                           KEY SIZE
KEY SIZE                    X
25-26                        DELETE CHAIN OFFSET
DELETE CHAIN OFFSET         X
27-29                        DELETE CHAIN SECTOR
DELETE CHAIN SECTOR         X
30-31                        NEXT RECORD OFFSET
NEXT RECORD OFFSET          X
32-34                        FIRST DATA SECTOR
FIRST DATA SECTOR           X
35-37                        FIRST SECTOR OF
FIRST SECTOR OF             X
34-48                        FIRST KEY AU
FIRST KEY AU                X
34-48                        UNUSED
UNUSED

SECTOR CONTROL BYTES

1-756                        USER DATA
USER DATA                   757-759
757-759                      DISC NAME-APPEARS IN TOP LEVEL DIRECTORY
DISC NAME-APPEARS IN TOP LEVEL DIRECTORY
SECTORS AND ALL TOP LEVEL KEY
SECTORS AND ALL TOP LEVEL KEY
SECTORS.
SECTORS.
760                           KEY LEVEL-LEVEL OF SECTOR. FOR KEY AND
KEY LEVEL-LEVEL OF SECTOR. FOR KEY AND
DIRECTORY SECTORS.
DIRECTORY SECTORS.
761-763                      UNUSED
761-763                      UNUSED
764-766                      FORWARD LINK-EVERY DATA SECTOR LINKS TO THE
FORWARD LINK-EVERY DATA SECTOR LINKS TO THE
NEXT DATA SECTOR. IN KEY SECTORS
NEXT DATA SECTOR. IN KEY SECTORS
THE 5th KEY SECTOR OF AN A.U. WILL
THE 5th KEY SECTOR OF AN A.U. WILL
POINT TO THE NEXT KEY A.U.
POINT TO THE NEXT KEY A.U.
767-768                      OFFSET-GIVES THE NEXT AVAILABLE BYTE IN THE
OFFSET-GIVES THE NEXT AVAILABLE BYTE IN THE
SECTOR FOR DATA ENTRY. ALL INFOR-
SECTOR FOR DATA ENTRY. ALL INFOR-
MATION IN THE SECTOR BEFORE THE
MATION IN THE SECTOR BEFORE THE
OFFSET ADDRESS IS GOOD DATA.
OFFSET ADDRESS IS GOOD DATA.

KEY ENTRY

1-2                          DATA OFFSET-STARTING ADDRESS WITHIN A DATA
DATA OFFSET-STARTING ADDRESS WITHIN A DATA
SECTOR FOR THE RECORD ASSOCIATED
SECTOR FOR THE RECORD ASSOCIATED
WITH THIS KEY.
WITH THIS KEY.
3-5                          SECTOR NUMBER OF NEXT SECTOR OR DATA. A LEVEL
SECTOR NUMBER OF NEXT SECTOR OR DATA. A LEVEL
0 KEY SECTOR WILL HAVE THE SECTOR
0 KEY SECTOR WILL HAVE THE SECTOR
NUMBER OF THE DATA SECTOR FOR ITS
NUMBER OF THE DATA SECTOR FOR ITS
KEY. ALL KEY LEVELS ABOVE 0 WILL
KEY. ALL KEY LEVELS ABOVE 0 WILL
MERELY POINT TO THE NEXT LOWER KEY
MERELY POINT TO THE NEXT LOWER KEY
LEVEL TOWARDS 0.
LEVEL TOWARDS 0.
6                            UNUSED
6                            UNUSED
7                            KEY
**IOU 25 CLOCK**

Qantel Model 2901

**WRITE CONTROL INSTRUCTIONS**

\[1xyy9d\]  
\(X = \text{DEVICE ADDRESS}\)  
\(YY = \text{CONTROL BYTE}\)

<table>
<thead>
<tr>
<th>CONTROL BYTE</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>02</td>
<td>SET TEST</td>
</tr>
<tr>
<td>09</td>
<td>LOAD TIME OF DAY</td>
</tr>
<tr>
<td>0C</td>
<td>READ INTERVAL</td>
</tr>
</tbody>
</table>

**STATUS IN INSTRUCTIONS**

\[4xyy9d\]  
\(X = \text{DEVICE ADDRESS}\)  
\(YY = \text{STATUS BYTE}\)

<table>
<thead>
<tr>
<th>STATUS BYTE</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>WRITE BUSY</td>
</tr>
<tr>
<td>08</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>INTERRUPT</td>
</tr>
<tr>
<td>20</td>
<td>TIME CORRECT</td>
</tr>
<tr>
<td>40</td>
<td>N/A</td>
</tr>
<tr>
<td>80</td>
<td>N/A</td>
</tr>
</tbody>
</table>

MAX. PWR. REQUIRED

-12v = .01A  
+5v = .86A
IOU

UNBUFFED TEN KEY

MAX. PWR. REQUIREMENTS
-12v = .16A
+5v = .33A

ADDRESS SWITCHES 0-F

TEN KEY MODULE UN-BUFFERED

WRITE CONTROL INSTRUCTIONS
1XY9D  X = DEVICE ADDRESS  YY = CONTROL BYTE

FUNCTION

BYTE

00  RESET TONE
01  SET TONE

STATUS IN INSTRUCTIONS
4XY9D  X = DEVICE ADDRESS  YY = STATUS BYTE

STATUS

BYTE

MEANING

00  BUSY
02  N/A
04  END
08  SERVICE REQUEST
10  N/A
20  FLAG TWO
40  FLAG THREE
80  N/A

10-11
### WRITE CONTROL INSTRUCTIONS

<table>
<thead>
<tr>
<th>1XY9D</th>
<th>X = DEVICE ADDRESS</th>
<th>YY = CONTROL BYTE</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>CONTROL BYTE</th>
<th>FUNCTION</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>00</td>
<td>RESET SIGNAL</td>
<td></td>
</tr>
<tr>
<td>01</td>
<td>SET SIGNAL</td>
<td></td>
</tr>
<tr>
<td>04</td>
<td>RESET ALLOW TERMINATION INTERRUPT</td>
<td></td>
</tr>
<tr>
<td>05</td>
<td>SET ALLOW TERMINATION INTERRUPT</td>
<td></td>
</tr>
</tbody>
</table>

### STATUS INSTRUCTIONS

<table>
<thead>
<tr>
<th>4XY9D</th>
<th>X = DEVICE ADDRESS</th>
<th>YY = STATUS BYTE</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>STATUS BYTE</th>
<th>MEANING</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>READ BUSY</td>
</tr>
<tr>
<td>02</td>
<td>N/A</td>
</tr>
<tr>
<td>04</td>
<td>END</td>
</tr>
<tr>
<td>08</td>
<td>SERVICE REQUEST</td>
</tr>
<tr>
<td>10</td>
<td>INTERRUPT</td>
</tr>
<tr>
<td>20</td>
<td>FLAG TWO</td>
</tr>
<tr>
<td>40</td>
<td>FLAG THREE</td>
</tr>
<tr>
<td>80</td>
<td>N/A</td>
</tr>
</tbody>
</table>

MAX. PWR. REQUIRED
-12v = .16A
+5v = .68A
+26v = .35A
FILFIL or BFILFIL EXAMPLES

*I,FD,F=0,C=08000 (RETURN) or (TRANSMIT)
*O,RD,F=0,C=08000 (RETURN) or (TRANSMIT)
Copies everything (8000 sectors) bit-for-bit from the fixed disc to the removable disc. By exchanging the FD and RD copy is from the removable to the fixed. The D is the disc address.

*I,FD,F=00000,C=08000 (RETURN) or (TRANSMIT)
*O,MB,R=7688,B=7680 (RETURN) or (TRANSMIT)
From fixed disc (FD) to mag tape (MB) in blocks of ten sectors.

*I,MB,R=7688,B=7680
*O,RD,F=0,C=08000
From mag tape to the removable disc.

*I,RD,F=xxxxx,C=yyyyy
*O,LF,R=768
Prints (on device LF) everything from the removable disc (RD) starting at address xxxxx, and continuing for yyyyy number of sectors.

*I,CE,R=80
*O,LF,R=80
Prints from punch cards to device LF.

FORMAT

*I, RD, R=nnn, B=nnnn, S=nnn, F=nnnnn, C=nnnnn
I = Input or FROM; O = output or TO
R = Removable (disc); F = Fixed (disc)
L = Lineprinter; C = Card reader
M = Mag tape
O to F = Device address (2nd digit of 2nd expression)
R = Record length (R= 768 if not specified)
B = Block length
S = Number characters from each record
F = Starting sector (disc only)
C = Number of sectors (disc only); must be 5 digits.

Defaults:
Record length - 380 for Cards; 380 for 111 disc. Output record length can not be shorter than input (S).
Block length - To record length. Can not be more than 380 for 111 or 768 for 6+6,3+3, or 114.
Input length (S) - T record length. Used for limited number of sectors.
Number of sectors (C) - Must have 5 digits. C in the output will terminate when reached, regardless of C in the input.