PDP-X Technical Memorandum # 10

Title: Progress 7-17-67 (Architecture)

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I. Seligman

Index Keys: Architecture
Progress

Distribution
Key: C

Obsolete: None
Revision: None

Date: July 17, 1967
The following areas have been completed and documented:

1. A uniform documentation system has been established for PDP-X (index attached) as described in PDP-X Technical Memorandum # 1.

2. The basic system architecture has been completed. This is contained in Technical Memorandum # 6.

3. The processor structure has been defined including:
   a. Central Processor organization.
   b. Basic instructions set.
   c. Priority interrupt system.

   These are described in "PDP-X Processor Description."

4. The basic IO structure has been completed including:
   a. IO Bus
   b. Teletype Input and Output
   c. Paper Tape Reader and Punch
   d. Light Duty Card Reader
   e. Small XY Display
   f. Interprocessor Buffer.

   These are described in "PDP-X Processor Description" and in the various Technical Memorandum.

This design incorporates suggestions from many of our better customers and has been reviewed by key in-plant personnel, Gordon Bell and Ivan Sutherland.
5. The following areas remain to be completed:

a. Protection
b. Detailed Model Specifications
c. Memory Bus
d. Multiprocessor Communication
e. Major Peripheral Controller Structure
f. Simulator.
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