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Electronics / October 14, 1976
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Monolithic Memories
Highlights

Cover: Computer provides better X-ray image, 89
Medical diagnosticians are hailing computerized axial tomography, a new X-ray imaging technique that provides a high-resolution display of a horizontal “slice” of the body. Organs obscured in the single plane of conventional X rays stand out clearly with CAT scanners.

Cover is by Art Director Fred Sklenar.

Naval test system draws flak, 65
A general-purpose automatic test system for Naval avionics has drawn fire from the fleet as a defective concept. VAST, intended as a total support system, has serious operational and maintenance problems, an official report charges.

Microprocessor monitors chemical data, 104
A microprocessor-based data-acquisition controller that monitors chemical instrumentation uses a modular approach to hardware and software design for a simple, flexible system. This article is another in the series, “Microprocessors in action.”

Power-supply choice is crucial, 107
Sophisticated electronics equipment makes greater demands on the power supply. In selecting this increasingly important component, equipment manufacturers choose between three types of supply and decide whether to make or buy.

And in the next issue . . .
The annual technology update . . . Electronics’ 1976 achievement award.
One of the hottest buzzwords in medical circles these days is CAT—which stands for computerized axial tomography. And with a name like that, it's a good thing that the technique has a catchy acronym. What's more, to an increasing number of patients, it's a good thing that there even is a technique to go with the name. Says Haim Zaklad, who wrote the article that starts on page 89, "it can probably be said that CAT, the reconstruction of a transaxial section of the human body by means of X-rays, represents one of the major feats of technology in medicine."

Zaklad, of Israel's Elscint Ltd., adds: "CAT promises to revolutionize diagnostic medicine, the essence of which is a considerable enhancement in the quality of health care delivered to society. Further, the increasing tendency of government to become financially involved in national health-care expenditure is bound to induce the physician to practice more conservative medicine, primarily based on better diagnostic procedures."

"The present inefficiencies of health-care delivery stem from hospitalization and crisis medicine with ensuing high expenditures. Consequently, the need for high-quality diagnostic inspection, delivered on an out-patient basis, is expected to open the market for new sophisticated diagnostic instruments."

As is fairly common among technological advances, CAT took off rather slowly, but once it was proven out, the race was on to cash in on the technique. Again in the words of Zaklad:

"Towards the end of 1973, the medical community, particularly the neurologists, were astonished to see the structural details in man available from the first CAT head-scanner from Britain's EM1 Ltd. However, the basic idea of X-ray transaxial tomography was proposed as early as 1963 through a patent granted to W. H. Olendorf. The story is told that Olendorf arranged the tracks of his son's toy train in a circle, placed an object in the circle and mounted a radiation source and a detector on the train in such a manner that the source and detector were diametrically opposite each other. By letting the train run while taking the signal from the detector, sufficient data were generated to allow for a crude reconstruction of the object."

"In the same year, A. M. Cormack showed a mathematical technique suitable for the determination of a variable gamma-ray absorption coefficient in a two-dimensional region from observations made outside the region. The first CAT scanner was designed by G. N. Hounsfied of EM1. With the publication, a decade after Olendorf's patent, of its performance in diagnosing brain tumor and lesions the machine became an instant success. This development was the start of a new era in which some 20 companies, producing both head and whole-body machines, are competing. The cost of a scanner may reach $600,000. The market size is estimated to have the potential of $1 billion in the next decade."
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Readers' comments

Feest boosted, Schulke panned

To the Editor: The letter in the Sept. 2 issue by Thelma Estrin regarding the leadership, experience, and abilities of the various candidates for the presidency of the Institute of Electrical and Electronics Engineers was very interesting.

Giving Robert Rivers credit for his work on professional activities was aptly contrasted by the editorial on p. 12 ("A slow start for IEEE's professional activities") and the article on p. 67 ("Professional program gets few cheers"). If the professional-activities program is to be credited to Rivers' leadership, we do not need him.

With one exception, the other candidates, all of whom are on the board of directors, are then responsible for the present financial situation of the IEEE and for the engagement of Maj. Gen. Herbert Schulke (ret.) as the inept manager. The IEEE is where it is now because of this type of "leadership."

As a working engineer, my motto must be: Elect Feest; can Schulke!

Gordon Edwards
Phoenix, Ariz.

Correction

Bell Laboratories manufactured the cables used in Bell's experimental fiber-optic telephone link [July 22, p. 43], not Western Electric Inc. and Corning Glass Works. Western Electric made all the fibers in one cable and 75% of those in the other. The remaining 25% came from Corning.

The average loss of all fibers in the primary test cable was 6 decibels per kilometer, which was 2 dB/km less than the design goal specified by Bell Labs.

Using special low-loss splices and selected low-loss fibers from one of the cables, Bell achieved an average loss of 4.5 dB/km over a 10.9-km line; with a total acceptable loss for the system set at 50 dB, no regeneration was required under a distance of 10.9 km.

We thank M. I. Schwartz of Bell Laboratories, Norcross, Ga., for providing us with the information to correct our article.
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<table>
<thead>
<tr>
<th>Op amp category</th>
<th>What BiMOS contributes</th>
<th>RCA device</th>
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<tr>
<td>General Purpose</td>
<td>Wide applicability. Low cost.</td>
<td>CA3140 CA3130</td>
</tr>
<tr>
<td>FET Input</td>
<td>Lower device cost. Reduced circuit cost.</td>
<td>CA3140 CA3130</td>
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<tr>
<td></td>
<td>Large input voltage range: capability of swinging to 0.5 V below rail.</td>
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</tr>
<tr>
<td>Wideband</td>
<td>High slew rate with low ringing.</td>
<td>CA3140 CA3130 CA3100</td>
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<tr>
<td>4.5 to 70 MHz</td>
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<tr>
<td>Micropower</td>
<td>Strobability.</td>
<td>CA3130</td>
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<tr>
<td>down to 1.5 mW</td>
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<tr>
<td>High Current</td>
<td>Eliminates driver stage. Low device cost.</td>
<td>CA3130</td>
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<tr>
<td>up to 22mA</td>
<td>Rail-to-rail output swing.</td>
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RCA. Full house in Linear ICs.
News update

Since its first installation in a dog-food factory (Electronics, Oct. 16, 1975, p. 26), American Laser Systems Inc.’s infrared closed-circuit TV transmission system has found plenty of other uses. Not only is the industrial market for the product “so large it’s hard to address it,” boasts Duncan B. Campbell, president of the Goleta, Calif., firm, but another development has turned up.

A national news-gathering agency, which he won’t name, has paid to bring American Laser’s system, which is based on an IR optical carrier, up to broadcast industry standards. Improvements include tighter differential phase and gain, flat frequency response, and better transient response. The company is selling the system to television stations as short-range transmission links for the new electronic newsgathering minicamera and recording equipment. Stations are buying “a number of units” which are cheaper than comparable microwave transmission equipment, Campbell says. His firm should sell about $500,000 worth of both versions this year, he estimates. They cost $4,000 to $6,000 per copy.

Rome Air Development Center, the Air Force’s semiconductor watchdog, will soon request proposals for reliability studies of large-scale integration and microprocessors and associated support chips. The center, which last year suffered a temporary cutback in funds for outside reliability studies (Electronics, Oct. 16, 1975, p. 25), “is in really great shape for fiscal 1977,” says David F. Barber, head of the Reliability Branch. The branch was able to reacquire most of its fiscal 1976 money, about $1.3 million, out of RADC’s existing funds and reestablish five threatened outside study programs. Among them were programable read-only memories, and complementary-MOS semiconductors on sapphire. “Funding appears to be stable through fiscal 1980,” Barber says. “We don’t see any problems with funds stabilization. In fact, there probably will be a slow increase.”
The CA3080 variable op amp is the first differential-voltage input, current output op amp. Like a transistor it has a control input—one that lets you vary not just voltage but also power, bandwidth, slew rate, input current and output current. It can be programmed and/or signal modulated to select the optimum gain, speed, bandwidth and power. And the output can sink or source current.

It puts the designer in complete charge.
This wide range of operation gives you unusual design flexibility. You can create simplified versions of present designs ... or take unique approaches to new designs. You have linear gain over a 6-decade range to work with. A 50V/μs slew rate. Power levels adjustable from below 1 μW up to 30 mW. And if you need more than 1 mA output, the CA3094 has integral Darlingtones to provide 300 mA peak output.

In short, you can “shape” the variable op amp to fit the job—and create big circuit savings.
How much does all this flexibility cost? Amazingly little: 55¢ for the 3080E and 65¢ for the 3094E, at the 1K level.
For data sheets and application notes on these op amps, contact your local RCA representative. Or RCA.

Write: RCA Solid State. Box 3200, Somerville, N.J. 08876; Sunbury-on-Thames, Middlesex TW16 7HW, England; Ste. Anne de Bellevue H9X 3L3, Canada; Fuji Building, Tokyo, Japan.

RCA. Full house in Linear ICs.

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You must have your entries in by January 15th. A panel of impartial experts will pick the top 10 designs. A drawing will be held to determine the winners. And we'll announce the names of the winners by March 1, 1977.

In the meantime, you've got some very important things to think about. Like what color you want. Do you want a silver body with black interior? Red with saddle? Blue with tan? How about a gold racing stripe along the side?

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Several Electronics editorials have raised questions about what role the educator should play in influencing the supply of engineers. The following comments, which form a rebuttal to points made in some of those editorials, are by David J. Comer, professor of electrical/electronic engineering, California State University, Chico, Calif.

In defense of the educator

Over the last six years, I have read with interest and concern about the plight of the electrical engineering profession. We have seen cutbacks and recessions lead to thousands of unemployed engineers with particularly serious effects on the over-40-year-old engineer.

This is a complete turnaround for a profession that in the last few decades has been on the right side of the supply-demand curves for manpower. Since 1970, the supply and demand curves have drawn uncomfortably close together, allowing even minor economic fluctuations to move the demand curve either above or below the supply curve.

In the concern to solve this problem, many proposals have been made. These range from limiting enrollment of engineering schools and establishing a viable professional association, such as the American Medical Association, to unionization of the engineer with strikes or militant tactics as the modus operandi.

The engineering educator is now becoming accustomed to hearing complaints that he is self-serv ing because he does not represent the true career situation to the prospective engineer, presumably causing many changes in major to other fields. However, some points pertaining to these criticisms have not been emphasized in the past.

When a student elects to major in electrical engineering, he is generally rather serious about preparing for a specific career. This is not true in many other cases, especially in the liberal arts, where students tend to have a very ill-defined career objective in mind as they begin their college work.

When a freshman student seeks career advice, most engineering teachers are conscientious enough to review the employment situation over the last few years. I generally start by relating the story of an ex-student, one who graduated in 1965. This academically average student had interviews with 17 firms, receiving 13 job offers. Then I point out that this situation has changed considerably and that our best students now have to hustle to get a handful of job offers. I mention that our poorer students have been able to get jobs, but in recent bad years have often had no more than a single offer, and this after several interviews at the campus placement center. Some have had to go in person to various firms before securing a job. I then quote the average starting salary of the electrical engineer, as indicated by the College Placement Council.

I usually discuss the need for an engineer to study on his own throughout his career to avoid obsolescence and discuss the topping out of salaries for those who shun management. If the student is interested, I discuss the broadness of the field and the possible areas of his employment, drawing on my own full-time industrial work, my consulting, and my summer employment experiences.

Now let us assume that after this discussion which can last an hour, the student decides to change majors. His next questions logically relate to other four-year programs.

What are the starting salaries and demand for these areas?

A truthful answer is that the only other four-year program that approaches the engineer's starting salary is business administration, and the demand is also comparatively high in this field. But beyond that single field, no other area even approaches the demand and starting salary of the engineer.

Neither the liberal arts major, the school teacher, the mathematician, nor the physicist can expect to receive the number or dollar amount of offers that the engineer expects. The psychology student, the history major, the English major, the agriculture student and others often end up changing fields upon graduation. In good times or bad, the engineer has been at the top.

What other four-year program should the faculty member recommend?

The possibility of medicine or dentistry is often brought up along with the monetary advantages of these professional fields. Self-employment in fields not requiring a college education, such as home building, are sometimes suggested, but ultimately the student makes his own decision.

Why should engineering be one of the few fields that experiences an undersupply of talent?

All other fields with the exception of those such as law or medicine have fewer jobs than people trained in these areas. Is engineering sacred to the extent that there should be no competition for jobs? Perhaps this is why we have so few engineers in public office, in government service, or in nontechnical administrative jobs. There have always been enough technical jobs to absorb all graduates. Competition for jobs may upgrade the profession and nudge some capable people into other worthy professions.

As an educator, I cannot apologize because many mathematically minded students choose engineering—at least not until someone can demonstrate a better choice. Is it the educator who is self-serving or is it those that would divert capable students into less opportune fields to preserve their own job security?
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**People**

Quick growth, is Sinnott's plan for Data Systems group

"Perkin-Elmer wants to achieve a significant position in the information-processing industry in a relatively short period of time," says Daniel Sinnott, vice president and general manager of the newly formed Data Systems group of Perkin-Elmer Corp. of Norwalk, Conn. To this end, Sinnott has the resources of minicomputer maker Interdata Inc. of Oceanport, N.J., tape and disk-drive producer Wangco Inc. of Los Angeles, and printer-terminal maker Terminal Products of Randolph, N.J. These are the three subsidiaries of Sinnott's new group, a consolidation of the computer-related companies acquired in the last two years by Perkin-Elmer.

**Total supplier.** "To be one of the surviving companies in the early 1980s, we need to be a total supplier of products to the information-processing market. This we'll do by adding other products to what we presently have, either by building them ourselves, by acquisition, or by both," asserts the 42-year-old executive who had helped guide Interdata, which he founded in 1966, to sales of $23.5 million a year.

The Data Systems group kicked off a new fiscal year in August after its units posted combined revenues of more than $70 million in the previous year. "We have some exciting growth plans for the next three fiscal years," says Sinnott, who doesn't argue with industry estimates that the group will more than double its revenues by the end of fiscal 1979. "Most of the growth will come out of Interdata," he adds, "but we're also looking for growth rates above industry standards in other fairly select areas."

**Plans.** To meet this objective, Sinnott is considering both internal growth and acquisitions. "We have excellent tape- and disk-drive capabilities, so there's no need for us to buy them. But line printers—there's a talent we don't have and one we might go out and buy."

A software house is being considered for acquisition, as well as a semiconductor manufacturer. However, the group may develop a facility to build its own semiconductor memories for use in its products.

And don't be surprised if the semiconductor project grows beyond supplying memories in-house. Sinnott says, "If we get into that business, we may serve the semiconductor-memory market through its own marketing channels. Right now, it's just one of the areas where we're doing our homework to be one of the survivors."

Persuasion part of Wolff's art at MIT's electronic lab

"Find good people, give them money and support; then pray." That's the broad philosophy of research embraced by Peter Wolff, the new director of the Research Laboratory of Electronics at the Massachusetts Institute of Technology. He considers his principal role is that of persuader, and that researchers must be encouraged to communicate with each other—hardware and software people most of all.

Woff's laboratory in Cambridge, Mass., provides an umbrella under which faculty members, a research staff, and students from 10 academic departments at MIT conduct research in three broad disciplines—quantum physics, astrophysics and plasma...
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People

dynamics, as well as communications and engineering sciences. Wolff was most recently director of the MIT Center for Materials Science and Engineering and head of the solid-state and atomic physics division of the physics department.

He points out that even though electrical engineering "is the heart of the [research laboratory], the electrical engineering faculty is split into hardware and software people, and it's not easy to get researchers in these different disciplines to talk to each other. I've got no authority over them, but I can be a catalyst. I can persuade them to bring together things that should be together."

Close cooperation is needed, for example, in a project involving a $10,000 microprocessor-based machine that converts text into speech to aid the blind. One of the challenges, putting the proper accents and emphasis in the audio, involves developing software algorithms based on linguistics knowledge and building hardware that functions according to these algorithms.

In quantum electronics, Wolff again considers that his task is to foster good communications. MIT has a good reputation in optics, he says, and it's his job to bring together "the theoretical people with the optical-device people, and to form ties between the physicists and the engineers who use optics as a device. All the pieces are here to be put together—probably the best and broadest academic program in optics, which touches a lot of national problems, including communications and weapons."

Another responsibility he accepts is to ensure that his laboratory attracts and encourages young researchers. In this endeavor, Wolff feels he has a little more leverage than simply being a persuader. He can influence who is chosen for the research staff by accepting the research proposals he feels are important. "A younger person has all-out devotion to his early projects," Wolff maintains, "so one of the most valuable things an organization like ours can do is to get good young people started."
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Signetics readies first circuits using multi-level logic

Expect a major announcement from Signetics Corp. by the end of the year concerning the first practical implementation of multi-value, or multi-level logic, until recently considered only a theoretical possibility. Rather than binary logic based on the absence or presence of a signal, multi-level logic can discriminate between any number of levels. Thus, a logic system based on multi-levels can be fabricated, increasing information density per given area 4, 8, or 10 times without any substantial changes in present processing. The company’s first effort will be a four-value “quad-logic” family (0, 1, 2, 3) compatible with binary logic (0, 1) circuits. Circuits based on octal (8) and decimal (10) logic are being evaluated. Multi-value devices would potentially cost less per gate, but, more important, would provide logic flexibility unattainable with binary systems.

The Signetics development is based on two years of investigation into threshold logic using bipolar techniques like emitter-coupled logic and integrated-injection logic. Similar work is reported being pursued by Texas Instruments, Fairchild Camera & Instrument Corp., and Philips.

TI, N.Y. firm to swap licenses on semiconductors

A small Long Island firm, Standard Microsystems Corp. of Hauppauge, whose president holds a basic MOS patent, is reaping the first fruits of its campaign to benefit. It now has access to the arsenal of semiconductor processes used by Texas Instruments Inc. as a result of its worldwide cross-licensing agreement with TI. The agreement covers each firm’s semiconductor technology patents and patent applications on file or applied for over the next five years. Principal patents included in the exchange are the basic IC patents issued to Jack Kilby of TI and those dealing with high-density, high-speed n-channel MOS structures, for use in 4-k and 16-k random-access memories and microprocessors, issued to Standard Microsystems president Paul Richman.

The pact with TI, which includes an undisclosed amount of cash to be paid by TI over a four-year period, is part of an overall patent-licensing program under way at Standard Microsystems, says Richman. He declines to identify the other firms with which discussions are being held; however, both Fairchild Camera & Instrument Corp. and Intel Corp. are believed to be vulnerable since both firms have strong n-MOS RAM and microprocessor programs that use the isolated MOS process.

Color CRT display aims at S-3A

Loral Corp.’s Electronic Systems division in Yonkers, N.Y., and Lockheed-California Co. of Burbank, Calif., have developed a color tactical display they are proposing for use in the U.S. Navy’s S-3A antisubmarine warfare aircraft. The system is the first airborne color display to be developed for an antisub plane. It consists of a beam-penetration cathode-ray tube that uses a multilayer phosphor to produce three colors—red, green, and yellow—with two tones for each, so as to provide six codes for establishing target identification and priorities. Scheduled for flight tests in late 1977, the display also uses a Loral-built 16-bit microprocessor.

Intel to offer digital compandor

Intel Corp. is pushing into a market outside its traditional computer and timepiece sectors: telecommunications. It will soon begin shipping samples of a single-channel companding pulse-code-modulated coder/decoder (codec) system. Where competitors such as Signetics Corp., National
Texas Instruments Inc. is preparing an entry into the 3-digit panel meter business with an integrated-injection-logic LSI chip set for market introduction by the end of the year. TI is going in two directions with the basic chip, using it as an a-d converter either for digital panel meter or for microprocessors, depending upon the associated interface circuitry, says H. Dean Toombs, vice president and director of engineering.

Like a proprietary 12L chip for DPMS developed by Analog Devices Inc. of Norwood, Mass., in conjunction with RCA Corp.'s Solid State division in Somerville, N.J. (see p. 34), the TI chip needs few external parts — a light-emitting-diode display, some transistor digit drivers, and a few capacitors and resistors — to implement a DPM.

Although TI won’t comment, it’s not difficult to imagine the big semiconductor supplier selling the finished DPM, the way Fairchild Camera & Instrument does, or supplying uncased modules as is National Semiconductor. TI now makes the IC, the displays, and all the rest of the DPM parts as well.

The Boeing Co. has begun tests of airborne displays that will show Air Force B-1 bomber crews computerized images of hostile aircraft and missile sites, radar sites, or other threat information so that the crew-member operating the defensive subsystem can initiate countermeasures. The 16 displays were developed and delivered to Boeing, the avionics integrator for the Rockwell B-1, by Sanders Associates, Nashua, N.H., under terms of a $1.2-million contract. There are two types of CRT displays: a unit that graphs threats within a radius of 25 to 200 miles of the bomber and an alphanumeric display that depicts dialogue between the defensive-subsystem operator and the aircraft’s computer.

A programmable graphic-display terminal incorporating dual microprocessors to substantially shrink it was unveiled this week by the Computer Graphics division of Sanders Associates, Nashua, N.H. Use of the bipolar microprocessors — one a graphics controller and the other replacing a minicomputer used in earlier Sanders systems — allows the Graphic 7 to fit on a desk top. It has all the graphics capabilities of larger Sanders units, however, and will sell for some 25% less than its predecessor, the SA 500. A typical system will consist of the single-box terminal controller, 21-inch CRT display and keyboard, and 8,192 words of 16-bit semiconductor memory. Price is $32,800.

Sanders engineers chose the bipolar microprocessor for its speed and because the unit, Monolithic Memories Inc.'s 6701 4-bit slice, allows them to use their own instruction set. The Graphic 7 is aimed at a variety of commercial and military applications ranging from computer-aided design to flight simulation and training. It can be connected to any local or remote standard host computer.
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New computer to compete against IBM's 370/158

Emulation unit built by National Semiconductor will be marketed by Itel Corp and sell for half IBM's price

The IBM 370/158 has acquired a twin and a rival—a large-scale digital computer built by National Semiconductor Corp. The challenger is not only a direct emulation of the 370/158 but runs on IBM's own software.

The machine is being marketed by Itel Corp., San Francisco, which has been leasing to end users IBM central processor units as well as IBM and non-IBM add-on memories and plug-compatible peripherals.

Heretofore, only Amdahl Corp. has cashed in on the existing 370-series software with its System 470 V/6 computing system, a higher-performance replacement for the top-of-the line 370/168 [Electronics, Nov. 28, 1974, p. 39]. National/Itel's 158 also achieves higher performance—a minimum of 25% improvement in throughput, according to a source at Itel. But it is priced 50% below IBM's 370/158 price, which can range upward from $2 million. Targets for Itel, are about 1,000 370/158s already in place and the others to come.

Microprogrammed. The 158 emulation uses a microprogramming technique developed originally by Digital Scientific Corp. of San Diego, Calif. In 1970, Digital introduced the Meta 4, a microprogramed 16-bit processor emulating IBM's 1130 and 1800 system. Since then it has been developing several computers to replace more advanced IBM systems. For example, it had a working prototype of a 370/145 central processing unit, which never reached the production stage.

Work on the 370/158 emulation began as an in-house development at Digital Scientific about two years ago. National bought rights to the machine in December 1975. It funded a development group in San Diego under the name Exsysco, which now has been absorbed as a subsidiary.

The first full operational prototype system was qualified about a month ago. It is reported that Itel has ordered at least 25 of the machines, which rely in part on high-speed Schottky transistor-transistor logic. National expects to deliver the first machine by January.

A large part of Itel's business is in package leasing, in which it leases an entire system to an end user. It is believed that Itel will offer the National machine on the same basis. The firm already markets several of National's add-on memory systems to IBM end users, as well as disk and tape products manufactured by independent plug-compatible suppliers.

Thus, this move by Itel appears to be a logical step for a company with an existing end-user marketing organization already selling IBM replacement products. It might be easier for Itel than for others to convince potential users to relinquish the "security blanket" of buying IBM. Of course, it will have to convince the user that its service and maintenance organization will be as responsive as IBM's.

Reorganization. In a parallel move, National is re-organizing its Memory Systems and Microcomputer Systems groups to reflect this new venture. Memory Systems' general manager Dave Martin, will head a new Computer Systems group, which includes add-on and original-equipment memory systems, the Exsysco effort, and small computer systems. The last of these incorporate the microcomputer-card-and-systems effort previously under William Baker, general manager of the Microcomputer Systems' group, which will now concentrate on development and production of microcomputer components.

Commercial

California tests set for next year of Aerospace Corp. anti-hijack system

The Law Enforcement Assistance Administration believes it has an electronic deterrent to truck hijackers and cargo thieves. The effectiveness of the package will be determined next year when its developer, Aerospace Corp., begins an $800,000 year-long field study in metropolitan Los Angeles using 40 trucks travelling over a 400-square mile area. The company put together the security system over 3½ years under an $815,000 contract.

According to LEAA estimates,
cargo thefts run at an annual rate of $1.5 billion, most of it in trucking. An Aerospace survey shows that, on the average, the loss per truck engaged in urban pickup and delivery runs to $1,950 a year. Thus, for it to be economically attractive, Aerospace concludes its system should not cost more than $1,000 per truck each year.

"Two separate technologies will be tested in the upcoming program," explains Robert Kennel of Aerospace's Washington office which is managing the program for LEAA. One system employs an amplitude-modulated phase-lock technique that measures changes in position. The other is an "electronic signpost" system that provides absolute position. According to Kennel, one system can be employed to backstop the other. But the user's choice will depend upon the geography of the area where it is used.

Radio stations. The phase-lock technique relies on three commercial a-m radio stations whose normal broadcasting frequencies have been synchronized via high-precision frequency-control units. Thus, signals from the three stations in Los Angeles, KFI, KNX and KPOL, "create a stable hyperbolic electronic navigational grid across the city," says Lester Shubin, development program manager for LEAA's National Institute for Law Enforcement and Criminal Justice in Washington. This is much like the master-slave stations of a Loran system. "As a vehicle moves across the city, its transceiver picks up the a-m signals from the three stations, compares phase differences and relays the comparison back to a base-station computer. The computer, in turn, matches the information to the pre-stored (hyperbolic) grid for an accurate determination of the truck's location."

However, because the phase-lock system does not measure absolute position, and radio contact may be lost from time to time as a vehicle passes through a tunnel or under high-tension wires, the signpost system is added to update position. It consists of small transmitters mounted, like signposts, on utility poles along the truck routes. Each transmitter, which Kennel says costs $150-$200, radiates a unique identification code over a limited area. As a truck passes, its receiver picks up the coded transmission and retransmits it to a base station.

"The (base station) monitor can tell that the truck just passed the intersection of, say, Fifth and Jones streets," Shubin explains. "If the truck were to be hijacked and go off-route, the monitor would soon pick it up."

Sensors. A computer at the base station processes the data and interprets truck location, displaying information on a cathode-ray-tube terminal. It also monitors sensors built into the truck for determining such things as whether the driver's seat is occupied, when a door is opened, or whether someone is in the cargo compartment. There's also a foot-operated alarm that can be covertly operated, and a magnetic movement sensor to signal if the vehicle moves.

Somewhere between 300 and 600 signpost transmitters will be used in the upcoming tests, Kennel says. The exact number has yet to be worked out. In an earlier pilot test completed in Los Angeles in June involving six trucks and 25 square miles, 331 transmitters were used, placed about 1,000 ft. apart. But it was found the area could be covered with transmitters much further apart.

Aerospace's Kennel believes that the a-m phase-lock approach will probably be preferable "because it is cheaper" than the signpost alternative. Phase lock "is accurate within 600 ft. and works 95% of the time," he says, which is good enough. He expects contract awards for the hardware and upcoming tests will be made in two months.

Consumer

Small $39.95 solar-cell power package provides current for consumer electronics

So far, the public has met solar cells mainly in novelty items—executive toys and expensive digital watches. But the cells could turn up in portable radios and calculators, too, now that a small Chicago-area firm is marketing a low-cost photovoltaic power system that not only converts solar energy to electricity but stores it as well.
charged battery pack is able to compensate for the 0.3 v drop across output up to 6 v, another is used to deliver more than 50 mA at 0.4 v.

Solar power. Multiple-jack adapter plugs M-7's solar cell power pack into variety of gear. Two-pronged plug in middle of power cord is used to reverse polarity of output.

two are added for margin.

"It's that margin that takes care of the solar cell's temperature problem (output decreases as temperature rises) as well as days that we don't see full sun," Ignatius says. It also gives the panel the extra power to run 12-v radios.

Audio IR headsets do well in Germany

Ever since infrared systems for transmitting sound beams from a television receiver to a headset were first demonstrated two years ago, there's been much speculation in West Germany over other applications for this wireless sound-transmission technique. Now that more than 100,000 infrared headsets have been sold in West Germany since they went to market last summer, new consumer applications are already coming to the fore—sooner than most observers had expected.

Of this trend, there was plenty of evidence at the Photokina 1976 photographic-equipment show in Cologne and at the Hifi 76 audio-equipment exhibition in Dusseldorf held earlier this month. At Hifi 76, headsets for picking up stereo sound-modulated IR signals from audio equipment were introduced by several German producers, including Akg GmbH, Munich, and Eugen Beyer, Heilbronn. These headsets are expected to sell as well as did the monophonic types for reception of TV sounds after their market debut last year.

Private sound. Behind such optimism are, first of all, the advantages that wireless sound reception provides. Without any cables to bother them, listeners may walk freely about a large room without missing a sound because the IR rays reflect from every surface and spread throughout the room. Further, the headset wearer can listen to a program without disturbing other people in the room.

Besides such selling points, a big

Infrared audio a lead balloon in U.S.

Television and audio equipment makers in the United States have been thoroughly unenthusiastic about using infrared devices for wireless sound transmission. To date, no producer has given IR a tumble, although there has been some laboratory work.

In May Zenith Radio Corp. demonstrated Concept 4X, which was a futuristic four-channel stereo unit that featured infrared wireless transmission. The two front speakers were wired to the audio amplifier and in turn transmitted IR signals that drove the two rear speakers. The firm has since put Concept 4X on the back burner.

Looking to sell infrared diodes for consumer products, Texas Instruments this spring showed a possible IR earphone design that could be adapted by an original-equipment manufacturer. But it has not attracted much real interest. In addition, U.S. television makers have just about ignored IR for remote-control tuning, preferring to go along with ultrasonic transmission.

The main reason for this lack of interest is the cost. Price is much more of a factor in the U.S. market than in West Germany, and manufacturers here have tended to stay with the well-accepted and lower-cost wired audio headphones and ultrasound TV remote controllers.
marketing boost comes from the standards that German audio-equipment makers agreed on relatively early in the game. Since, for stereo sound transmissions, the two carrier frequencies used by all producers are 95 kilohertz and 250 kitz, the infrared headsets from any company may be used with the infrared transmitter from any other firm. Klaus Goschmann, an official at the German Electrotechnical Industry Association in Frankfurt, points out, "The unfortunate standards chaos that exists in quadraphony has been avoided in wireless sound transmission."

Separated. At present, the IR transmitters come as a separate unit. The LD241 infrared-emitting-diode from Siemens AG, for example, is a gallium-arsenide device with a total radiated power rating of typically 10 milli­watts at 100 milli­amperes. Efficiency is from 6% to 7%. In a stereo sound-transmission system, a number of such diodes may be needed to provide the power for an average-size room. The system from AKG, for example, has nine diodes per channel. However, Siemens is developing a diode with twice the efficiency of the LD241 and much higher output power.

At the receiver, Siemens, for one, offers the BPW 34, a silicon photodiode with a 3-by-3-millimeter chip, a photosensitivity of better than 50 nanometers per lux, and capacitance of less than 40 picofarads at 3 volts. A typical stereo system would have two diodes per channel. Some companies integrate the diodes into the headset, while others put them in a cigarette-pack-size unit that the listener wears around the neck. Audio frequency range is quite broad—from 20 to 20,000 hertz.

Built-ins. In the future, producers may decide to build the IR transmitter right into the stereo equipment. The units shown by equipment makers at the Dusseldorf show are about half an inch high, and the transmitter can thus be almost hidden from view. For operation, the unit is merely plugged into a power outlet and connected to the existing loudspeaker or headset jacks in the stereo equipment. The transmitter then radiates the modulated IR beams throughout the room.

Most infrared headsets have volume controls for both ears, as well as a control to switch the headset to monophonic TV-sound reception. Powering the IR receiving diodes in the headset is a small built-in rechargeable battery. More elaborate models have both a rechargeable and a dry battery. The headsets are priced at about $120, roughly the same as a high-quality wired headset. The IR transmitter costs about $150, but future versions made of integrated circuits will cost less.

Communications

Single chip has a-m transceiver

Dick Tracy's wrist radio may not be that hard to build now that most of a two-way radio has been crammed onto a single silicon chip. Buoyed by the boom in citizens' band communications, a small seven-year-old custom-chip designer in Cupertino, Calif., Lithic Systems Inc., has produced such a chip with all the active functions needed for an a-m transceiver. It could be used in such things as alarms, telemetry equipment, two-way walkie-talkies, and cordless telephones.

Few extras. All that's needed are tuned-circuit elements that set the operating frequencies, a crystal for the transmitter's master oscillator, another for the receiver's local oscillator, an antenna, and a speaker.

With an eye to the wrist-radio application, Lithic has designed the chip to operate directly from a 2.7-volt battery of the kind used in digital watches. Usually, circuits for portable personal communications have been designed around larger 9-v batteries, but the lower voltage at the current level needed for linear operation allows longer battery life. It also allows closer spacings on the monolithic circuitry and hence a smaller overall chip size, in this case, 70 by 100 mils.

According to Robert Hirschfeld, president of Lithic Systems, the chip could even fit easily into a watch. "The substrate now used in those..."
The new Airpax process monitor combines digital readout with analog setpoints.

Only from Airpax

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Circle our number on the Reader Service Card and we'll send you the full story, along with a model map that makes it easy to choose the specs that make the '78 right for your application. Whatever you choose, we can ship it in three to four weeks.

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Electronics / October 14, 1976

Circle 33 on reader service card 33
watches has more than enough empty space to include our chip plus the needed tuned elements that can be put into hybrid form," he asserts. The transmitter draws about 40 milliamperes of current and provides upwards of 60 milliwatts of radio-frequency power. In the squelched mode, the receiver draws only 1.5 mA.

The transceiver chip, the LP 2700, contains separate transmitter and receiver circuitry as well as the transmit/receive logic circuits. It also has the circuitry for the two crystal-controlled oscillators. Standard bipolar technology is used for all circuits. However, the receiver design incorporates a unique automatic-gain-control amplifier that puts a lot of gain in a small space yet assures a wide dynamic range.

**Op amp.** The AGC is built around an operational amplifier in which the dynamic range of the AGC is equal to the open-loop gain of the amplifier—about 100 decibels. The closed-loop feedback of the circuit drops the gain to unity to prevent distortion on strong received signals—often a problem with conventional AGC.

Instead of using the crystal-controlled oscillator, the chip could also work with a separate phase-locked-loop frequency synthesizer to provide the channel frequencies for citizens' band radio, Hirschfeld points out.

Producing a practical wrist transceiver will take more than just dropping the chip, along with some tuned elements, into a watch case, however. The radio will also have to be fitted out with a small speaker and an antenna that's efficient yet doesn't get in the way of the user.

The speaker problem could be solved in several ways. "We could use the plastic front of the watch case as part of the transducer mechanism or let the sound radiate out of the side edges of the case using an electromagnetic transducer," says Hirschfeld. "Or we could build the transducer into the wristband."

A suitable antenna is more difficult. One approach that seems promising, says Hirschfeld, embeds an antenna wire in a plastic wristband.

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**Instruments**

**FL creates $39 digital panel meter**

Is there finally a low-cost digital alternative to instrument-grade analog panel meters? Maybe so, with the new 3-digit, low-power digital panel meter being put on the market for $39 (in 100-piece quantities) by the Instruments and Systems group of Analog Devices Inc. in Norwood, Mass.

Designated the AD2026, the DPM is built around a single custom integrated-injection-logic chip developed in conjunction with RCA Corp.'s Solid State division in Somerville, N.J. All told, the large-scale-integrated chip helps reduce the electrical parts count to 14. The display uses half-inch-high light-emitting diodes.

Says Robert Boole, modular instruments product director at Analog Devices, "[The DPM] was designed from the outset to achieve two specific goals: a price that would convert analog-meter users to digital meters for precision measurement instruments, and reliability consistent with the experience of analog-meter users."

**Original equipment.** Analog Devices is targeting the AD2026 for use in original equipment such as medical and analytical instruments and those used to measure process variables such as temperature, flow, pressure and strain. These markets use about 2 million analog units annually and "$65 DPMs don't make sense," Boole says. He believes the new digital panel meter could "quickly attract upwards of 10% of the estimated $50 million market" for instrument-grade $10—30 analog meters "because of other advantages of the digital meter"—for instance, its 0.1% accuracy, compared with the 2% of its analog competitors, its equal or better resolution, and its direct-reading display and smaller size.

Further, the DPM can be scaled with a simple resistive divider to read out in any engineering units, whereas the analog meter's readout in engineering units is fixed on its front template. With a 3.4-by-2-inch front panel, the new DPM occupies less space than an analog meter with a typical 3.5-inch scale. And the DPM's depth—only 0.7 inch—is
If switching regulator control is all work and no play, boy, do we have news for you.

**MC3420 Switchmode* Regulator Control**

It used to be a dull, tedious job putting together reference, oscillator, PWM, phase-splitter and dual alternating outputs from numerous components to form a regulator control circuit. Not to mention hours of design time.

The MC3420 Switchmode regulator control circuit has changed all that.

Now all you do is plug it in and you’ve got all the functions needed to regulate the simplest to the most complex constant frequency switching power supply.

- It’s virtually all things to all designs.
  - It has a power supply voltage range of 10V to 30V.
  - It’s capable of 2 to 100 kHz operation and can be slaved to others like itself for synchronization.
  - Its 0 to 100% dead-time comparator is unique.
  - Its outputs are open-collector type with a saturation voltage of 0.5V @ 40mA and can block 40V.
  - It features an inhibit input and has options for independent control of one output for implementing a symmetry correction control loop.

Best of all, it’s priced at just $5.75 (100 up), a pittance compared to what it cost in parts and time before.

Now it’s so easy and simple to control single and double-ended supplies, transformer-coupled dc-to-dc converters, transformerless voltage doublers and polarity converters et al you’ll wonder what happened to all the hard work. We did it for you.

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Electronics / October 14, 1976 Circle 35 on reader service card 35
Much less. It weighs under 2 ounces, works from a 5-volt source and dissipates less than 0.75 watt.

**Density advantage. Why **12L**?

While Analog Devices' designers could surely have used standard complementary metal-oxide-semiconductor technology for the digital side of the chip's converter, they turned to the more speculative **12L** approach because it offers three times the density of C-MOS (total chip size is slightly over 10,000 mil²). It also offers the opportunity to use the same linear bipolar process in the digital as in the analog portion which includes the voltage current converter, current source, and comparator, explains Lew Smith, the senior engineer responsible for the project.

Injection logic also has the lowest speed-power product of any circuit technique—three times lower than standard C-MOS—so that, for the same speed, it's possible to operate the converter portion of the chip at a third of the power dissipation.

Analog Devices is not alone in choosing **12L** for its DPMs. Texas Instruments, Fairchild Semiconductor, Motorola, National Semiconductor, Signetics and RCA are all evaluating it for DPMs, sometimes in competition with a C-MOS effort. Siliconix, on the other hand, has chosen the C-MOS route for its new 3-digit panel meter chip.

**Mass-produced.** Because of its small parts count, the Analog Devices meter can be mass-produced from start to finish. Production flow starts when 15 separate printed-circuit patterns are plated onto a 12- by-12-inch glass-epoxy circuit board called a pallet. Components for all 15 DPMs are then inserted automatically on a high-speed assembly line. After wave soldering, the pallet of DPM boards move to a testing station where each DPM is subjected to 32 in-circuit and 50 functional tests. The same test station also runs 216 tests on the **12L** chip.

After testing, the 15 DPMs on the pallet undergo a 168-hour burn-in. Then the pc boards are removed individually from the pallet board and snapped into mounts in a special case. Analog Devices calculates the mean time between failure at more than 250,000 hours.

**Careers**

**EMC to continue demand forecasts**

Despite criticism that its studies of manpower demand actually contribute to an oversupply of engineers, the Engineering Manpower Commission seems determined to disseminate data that, by its own admission, have limited validity. The EMC, the research arm of the Engineers Joint Council that is supported by 36 professional societies, at its latest meeting turned back all proposals to discontinue its demand projections or to modify its method of making them public.

About two-thirds of EMC's 41 members have met to hear an interim report assessing various demand surveys from its ad hoc committee on supply and demand. The committee was formed initially to investigate charges by the National Society of Professional Engineers that the studies endanger engineering careers [*Electronics*, March 4, p. 67]. The report was to have been an informal response to the charges and to serve as a guide in determining the limitations of demand surveys [*Electronics*, Sept. 16, p. 75].

**Delay.** However, not only did the EMC overwhelmingly turn down motions calling for it to stop making supply and demand forecasts, but it also declined to postpone making projections until a valid model was developed. And it put off making a formal report until next month's meeting. The delay was attributed to illness and workload of committee members, but it was viewed by critics as further foot-dragging.

"EMC is still in the demand business," says David Reyes-Guerra, executive director of the Engineers' Council for Professional Development and chairman of the ad hoc committee. The interim report, he notes, came up with the conclusion that "no one can really project demand. The models available, at best, provide a 1-2 year accurate predictive value."

Reyes-Guerra, taking a middle ground in the controversy, says the problem with demand studies is that "people who know little about their caveats are using them as guides. Publishing demand data whose rationale is not known is something we just don't want to do."

Grumman Aerospace Corp.'s Art Gilmore, EMC's chairman, says: "Although there are lots of models around, all of them are highly controversial. We aren't confident that any validated model could be developed in a reasonable time. But that doesn't mean we can't print what people from a couple of hundred manufacturers indicate as to how many they intend to hire over a period of time."

However, the group has decided to review its policy on demand studies. But for now, Gilmore adds, "EMC is still free to do studies of demand information that is completely qualified." That qualifier, he says, is being worked on and will be in a policy statement due at a Nov. 16 meeting.

**Criticism.** Robert A. Rivers, president of Aircom Inc. of Union, N.H., and an IEEE representative to the EMC, criticizes the rejection of his proposals to get out of the forecasting business. "This indicates a complete disregard on their part for the need to stop promoting engineering enrollments and the tremendous opportunities in engineering."

EMC's interim report indicates that
Hand-held calculators have become standard tools for the busy businessman, investor, accountant, engineer, shopper—for everyone. No wonder they’re so popular.

But there’s one limitation. The “display only” machines provide no record of your entries and the results. Try adding up a column of figures. Most people have to do it twice to be sure they haven’t hit a wrong key. And—there is no permanent record for the files.

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You can select the 8-digit C-716, or 12-digit C-717, C-717X and C-718 circuits. Or you can even add a display with our C-719 printer-display interface circuit. All work with popular low cost print mechanisms.

Any way you figure it, General Instrument Microelectronics is the place to come for all kinds of calculator chips. Especially, the kind that help people figure for the record. Write or call General Instrument Microelectronics, 600 West John Street, Hicksville, New York 11802, Tel: (516) 733-3107.

We help you compete.
U.S. Air Force F-15 Eagle fighters recently downed two simulated MIG-25 Foxbats in tests at Eglin AFB, Fla. Equipped with Hughes APG-63 radar, the McDonnell-Douglas-built Eagles took on jet drones simulating the high-performance MIGs. The first Eagle launched a Sparrow missile with a dummy warhead at a drone moving at Mach 2.7 at 71,000 feet. The missile passed within lethal range of the target. The second Eagle, with live missiles, found and destroyed the mock MIG-25 at 68,000 feet and Mach 2.7.

A precision Ground Laser Locator Designator, for guiding laser-homing missiles like Maverick and Hellfire or cannon-launched guided projectiles to their targets, is undergoing field testing at the U.S. Army's Redstone Arsenal. GLLD (pronounced "glid") is a 50-pound, tripod-mounted device built by Hughes for use by ground troops. It combines high-power optics with a viscous-fluid-damped tracking unit, providing the accuracy to work against rapidly moving distant targets. GLLD emits a narrow beam of invisible light to the target. The guided missile senses the reflected beam and thereby homes unerringly on the target for a direct hit. Range and beam information can be sent to the artillery battery for use with conventional artillery.

For the first time, NASA will have direct correlated measurements of Venus's atmosphere as a result of the Pioneer-Venus twin missions in 1978. The program will use two spacecraft, the Orbiter and the Multiprobe. Data from the Multiprobe, to be launched in August 1978, will be compared with measurements taken remotely from instruments aboard the Orbiter, to be launched in May or June 1978. One large and three small probes will be launched before the Multiprobe spacecraft enters Venus's hot, dense atmosphere. Major objective of the mission is a detailed investigation of Venus's atmosphere and clouds. Hughes is designing and building the two spacecraft and integrating the scientific payload for NASA's Ames Research Center.

Hughes needs systems-level engineers: Sonar Systems -- to develop PM/FL at systems and hardware level; to develop/conduct test of advanced sonar systems..... Torpedo systems -- develop overall advanced systems.....Programmers -- design experience in real-time graphic display, sonar data processing, and/or hardware simulations. AN/UYK-20 computers and CMS-2 highly desirable. Requirements: BS or higher degree, U.S. citizenship. Please send resume to: Engineering Employment, Hughes Aircraft Company, P.O. Box 3310, Fullerton, CA 92634. An equal opportunity employer.

New products from Hughes include a traveling-wave tube designed for use in satellite earth terminal transmitters and capable of more than 400 watts of CW RF output power over the frequency range of 5.925 to 6.425 GHz; design and construction of the PPM-focused, metal-ceramic tube is based on Hughes' high-power CW and space-qualified communication tubes.....a new series of mechanically-tuned Gunn-effect oscillators designed to meet requirements as parametric amplifier pumps, local oscillators, and transmitter sources; the new units feature inherently low AM noise characteristics, compactness, and ease of operation; they are factory adjusted to one of eight specific center frequencies in the 18- to 60-GHz range.
News briefs

Crystal growing process tailors materials
Scientists at Bell Telephone Laboratories in Murray Hill, N.J., have successfully used a new crystal-growing process, called molecular beam epitaxy. They believe the process could permit tailor-making materials with specific, built-in electronics, optical and mechanical properties. A new crystalline material—a monolayer crystal—was assembled atomic layer by atomic layer by the process, which allows the chemical composition of each layer to be individually controlled. The synthetic crystal, not found in nature and never before prepared in the laboratory, has the same average composition as crystals used in fabricating light-emitting diodes and tiny lasers now under investigation for future Bell telecommunications systems.

Interactive fiber-optic cable TV to begin tests in Japan
An interactive fiber-optic cable television network will begin two-way tests on November 15 with 300 subscribers near Osaka, Japan. The Optical Video Information System will do such things as answer requests for TV programs and data, send facsimile transmissions, provide computer-assisted instruction, allow for cashless transactions, TV shopping and reservations, and read utility meters automatically. Three Japanese companies involved in the project are Fujitsu Ltd. (control center and digital data interfaces), Sumitomo Electric Industries Ltd. (optical fiber transmission system), and Matsushita Electric Industries Co. (studio equipment and subscriber terminals).

The system comprises three fiber-optic trunk cables, each containing 36 fibers and connecting the head-end facility to a subcenter. The subcenters have a video switch and control equipment and up to 14 distribution cables, each containing 24 optical fibers that will fan out to serve 12 subscribers. The final subscriber drop thus allows for two-way transmission between user and head-end. In addition to homes, fiber-optic cables will be connected to schools, hospitals, and town halls to allow programs that originate at those sites to be transmitted to the head end for further distribution to the subscribers.

CDC wins F-18 standard computer award
Control Data Corp.'s 480 computer has been chosen by the Naval Air Systems command for its standard airborne computer—Stacom—to be known as the AN/AJK-14(V) [Electronics, April 1, p. 40]. A $6.5 million Stacom contract calls for 150 pre-production models, with an option for 350 more, for use as the central computer on the Navy's new F-18 fighter. The selection of a winner from competitors IBM Federal Systems division, Lear Siegler, and initial favorite Sperry Univac was postponed for two months when the office of H. Tyler Marcy, assistant Navy secretary for research and development, ordered Stacom competitors to benchmark their systems against Univac's AN/AJK-20 already in inventory. As winner of the first Stacom competition, CDC stands to have its system picked for use in the antisubmarine helicopter program known as Lamps—for light-airborne multipurpose system. In addition, Navair could acquire as many as 800 F-18 fighters over the next several years, pushing AN/AJK-14 procurements for that program alone up to 1,600 systems on the basis of two per plane.

long-term models for determining demand shouldn't be used publicly since the future is determined by policy decisions that haven't been made yet. However, Rivers adds, "the majority of the commission seems to want to generate this baloney. The predominant attitude of the group is that they don't want to stop these projections."

One of the supporters of Rivers' proposals, Hans C. Cherney, a personnel administrator at International Business Machines Corp. in Poughkeepsie, N.Y., and vice chairman of the IEEE's U.S. Activities Board, asserts that the vote "highlights exactly what the problem is with EMC and the EJC. They do not represent the interests of the
Building blocks for the in-circuit/functional test system you need.

The system you use to test printed-circuit assemblies should be tailored to your specific needs and readily expandable to handle your future requirements. When you specify FAULTFINDER® FF101 test systems, you have the building blocks you need to do the job.

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Circle 40 on reader service card

Solid state

C-MOS voltages range to 150-200 v

Complementary metal-oxide-semiconductor technology, with a number of high-volume applications beckoning, is breaking through its traditional small-signal barrier and entering the high-voltage domain.

Researchers at Stanford University's Electronics Laboratory in Palo Alto, Calif., have developed two separate transistor fabrication techniques that allow construction of high-voltage C-MOS driver and switching circuits with source-to-drain breakdown voltages in the 150- to-200-volt range. Threshold voltages of 5 to 15 v make them compatible with standard low-voltage, low-power C-MOS logic.

What this makes possible, say the researchers James D. Plummer and Gerald A. May, are linear and digital C-MOS circuits that can operate off a rectified 120-v ac line.

New realm. Switch in the high-voltage C-MOS structure designed by Stanford's James D. Plummer consists of double-diffused n-channel and one or two p-channel transistors. A quad analog switch array has been built to handle 300 milliamperes at 200 volts.
First, with 3 rows of contacts on .100 centers, Viking's unique Nordic 2-piece P/C board connectors and I/O I.C. panel plugs get a lot more contacts into a lot less space.

Second, our unusual polarizing system lets you key each mating pair to prevent cross mating with adjacent connectors of the same type. You can stack a series of Nordic connectors next to one another in cramped space and not worry that they might be cross mated.

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Electronics / October 14, 1976

Circle 41 on reader service card 41
Electronics review

strap MOS capacitor connected back to its source for positive feedback, enabling the control logic to drive the high-voltage output devices.

The maximum voltage the circuit can switch, May says, is limited by the breakdown of the pre-charge transistor in the trio. So in the fabrication process, the low-doped, deep p-well diffusions on the C-MOS chip are used as the source-drain junctions of the high voltage structure to obtain breakdowns of about 75 to 80 V. To increase the breakdown to 200 V, a negatively-biased field plate [Electronics, Dec. 11, 1975, p. 30] surrounds the drain junction of the p-channel precharge transistor. The technique was first used, May says, in an 8-channel tactile-display-screen driver IC for a blind reading-aid. It is being developed further for an implantable artificial ear [Electronics, Feb. 20, 1975, p. 38].

Double-diffused. In Plummer's approach, double-diffused n-channel (D-MOS) structures substitute for standard n-channel transistors. "The high-voltage capability of the device is a result of the n-minus drift region between the p-channel and the n-plus drain contact diffusion," he says. "The gate extends over this region as a field plate to reduce surface fields and increase the breakdown voltage."

The high-voltage p-channel structures that result consist essentially of two p-channel transistors in series, which effectively divide the applied voltage making possible breakdowns ranging from 150 V to above 200 V. Threshold voltages are 5 and 12 V respectively. Plummer's technique is currently being implemented in a quad analog switch for a medical ultrasonic imaging system.

Computers

HP's small system woos volume users

Hewlett-Packard, in a move to garner more business from software-oriented systems houses, is changing
New from Texas Instruments:
An authoritative guide to understanding microprocessor software...from the beginning. Only $12.95.

A working knowledge of microprocessor software is essential to mastery of microprocessors. And acquiring such knowledge is now simplified with TI's new text, Software Design for Microprocessors.

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Using our tester, non-technical operators can inspect hundreds of boards daily. The operator merely puts the board on our test fixture and pushes the start button. If the board is good, that's it. If the board fails, precise rework instructions are automatically printed. Typical test time: 5 to 30 seconds!

Summing up the advantages — TROUBLESHOOTER:
- Tests a wide variety of products
- Detects and pin points single or multiple defects on an entire assembly in seconds
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THE MIRACLE OF SILICON GULCH

In the beginning there was the Op Amp. And it was good. Unfortunately, it had high bias current and was slow. So, we created the LM108, with Super Beta. Super Beta reduced the high bias current. But it was still slow. So we developed the LM118. A beautiful thing, with feed-forward, large BW and slew. Well, that made it faster. But it left us with the bias current problem all over again.

Then a long silence enveloped the Op Amp world. Everyone waited. There was even some talk that linear was dead.

Months passed. Then, a wondrous event. National succeeded in putting Bipolar and JFET together on a single chip.

Hallelujah!

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Electronics / October 14, 1976
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And other terrifically good stuff like the LF 13331 family of analog switches (with no latch-up or static blow out problems) and the LF 352 instrumentation amplifier (combining low input current and excellent linearity), and the LF 398 sample and hold amplifier (a delightfully pleasant combination of noise, acquisition time and price numbers).
And what they all have in common is that they’re the least expensive way to get the kind of performance they offer.
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FBP Series compact dimensions — shorter than FWP fuses — save equipment space.

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THE QUALITY LINE

Electronics/October 14, 1976
Democratic presidential nominee Jimmy Carter has dismayed supporters and gladdened critics in the telecommunications industry by apparently changing his views on an issue to meet the needs of an immediate audience. The contrast came in Carter's replies to questions on the currently hot topic of competition in telecommunications before two different groups. Appearing before a meeting of consumer activist organizations sponsored by Ralph Nader in Washington, Carter was asked about the American Telephone & Telegraph Co. antitrust suit by the Justice Department and the AT&T-backed consumer communications reform legislation that would restrict U.S. telecommunications competition. Carter at first confessed unfamiliarity with the subject, but then went on to call the suit "a good move in the right direction," adding that he sees no current need for "any corrective legislation."

In a later letter responding to an inquiry from Glenn E. Watts, president of the Communications Workers of America, which supports the Bell bill, Carter said he has "not precluded all possible legislation in this field," and labeled the preliminary hearings on the legislation as "the first step toward a comprehensive review of the direction of our telecommunications policies for the long term." Again, he prefaced his comments with the observation that he had not yet "been briefed on this problem."

Defense outlays for command, control, and communications will soar by 125% in the six fiscal years between 1976 and 1982, according to a new Electronic Industries Association market forecast. Spending on research, development, and procurement will jump to $2.7 billion annually by 1982 from the present level of $1.2 billion. At the same time, the EIA estimates that operating and support costs will remain virtually static, permitting R&D and procurement spending to rise to 54% from the present level of 36% of the Defense Department's total annual investment in command, control, and communications.

The EIA forecast, by Will Gray of Honeywell's Aerospace and Defense group and Jim Lee of Hughes Aircraft Co., identifies interoperability between tactical command and control system as an urgent first priority within the Defense Department, with emphasis on increased standardization and consolidation of data links, greater use of standard electronic modules, and standard computers [Electronics, Dec. 25, 1975, p. 52].

Applications for type-acceptance and certification of new 40-channel citizens' band transceivers are drowning the Federal Communications Commission laboratory in the rush by manufacturers to make the January market. Thirty-seven companies had submitted applications for one or more models by October, the FCC says. Familiar names include: Channel Master, E. F. Johnson Co., General Electric, Hy-Gain of Puerto Rico, Pace Communications, Pioneer Electronics, and RCA, as well as such offshore producers as Kyodo Communications, Matsushita Communications Industrial Co., Matsushita Electric, Sharp Electronics, and Toyota Motor Sales. Among the merchandisers filing were: Layafette Radio & Electronics, Montgomery Ward, Radio Shack, Sears Roebuck & Co., and Western Auto Supply.
Washington commentary

Plunging EFT into the political pit

The promised revolution of the nation's banking system by electronic funds transfer was relegated to the status of creeping evolution by the U.S. Supreme Court on Oct. 4. By refusing to review two Federal appeals court rulings that computerized customer banking communications terminals in shopping centers, supermarkets, and other sites off bank promises constitute "branch banks," the Supreme Court let the earlier judgments stand. Thus point-of-sale bank terminals are subject to Federal regulations limiting the number of branches that may be established by a national bank.

At stake initially, from the electronics viewpoint, is an average outlay of $25-35,000 per national bank on terminals alone. Overall, for all types of banks and including communications, switching, automated bank clearinghouses, and retail point-of-sale tie-ins, the potential hardware market is enormous—one preliminary estimate puts it at more than half a billion dollars within the decade.

What now? Bankers agree they must now turn to the 95th Congress that convenes next January as their court of last resort. "It is now clear that a legislative remedy will be required," sighed J. Rex Dewey, president of the American Bankers Association. The ABA is "disappointed with the decision which will allow discrimination to continue in the implementation of EFT services for bank customers," Dewey declared, and "will develop new strategies" to counter the challenge.

ABA's dismay

With the U.S. Comptroller of the Currency on their side, bankers had anticipated a Supreme Court review and hoped for a favorable judgment. Thus they were dismayed when the refusal came down on the third day of the ABA annual meeting in Washington.

After the initial shock had subsided, most of the ABA members and some of the EFT technologists among them began expressing concern about the prospects of getting a favorable judgment from the new Congress. National bankers recognize that the financial community is divided on the issue of off-premises terminals and that they face some powerful opposition in the political arena from state-chartered commercial banks and the state banking commissions that generally support them. State banks, fearful of being overwhelmed by the bigger national banks with multiple remote EFT terminals, are determined to maintain the economic status quo if they can.

Savings and loan associations present another and perhaps more powerful lobbying threat to national banks anxious to exploit EFT systems. "The S&Ls like to cast themselves in the role of the good little guys against us big greedy bad guys," mused one national banker from New England at the ABA meeting, "and they can be very effective in pulling it off." The fact that S&Ls are unaffected by the court ruling against remote terminals, he added, "is doubling their determination. They are expanding like crazy."

Two more years?

If the division within the banking community prolongs a legislative remedy for the EFT problem by a year, the National Commission on Electronic Funds Transfer threatens to make it two. Though the big 32-member body now shows signs of beginning to function effectively—particularly in its survey of EFT equipment makers on the issue of national standards—it is still handicapped by its slow start, thanks to President Gerald Ford's failure to appoint any commission members for almost a year following its creation [Electronics, Aug. 21, 1975, p. 10]. Congress is unlikely to take any action whatsoever on EFT before getting the commission's report, so bankers and the computer companies anxious to serve them seem to have no choice but to sit and wait until next year for a report that was first mandated for delivery no later than next month.

This sad state of affairs could easily permit Donald I. Baker to become smug, if he wished. For it was Baker, the knowledgeable and perceptive U.S. assistant attorney general for antitrust who warned the EFT community of just such a turn of events more than a year ago [Electronics, March 20, 1975, p. 50]. On that occasion, he likened tomorrow's promise of electronic funds transfer to that of cable television yesterday.

"About five years ago," he recalled, "everyone looked on cable the way we now look at EFT—as the wave of the future. Unfortunately, cable ran into the broadcasters and their regulators. The same thing can happen to EFT. It can be loaded down with legal restrictions to protect existing interests and with expensive obligations to serve the dreams of social engineers. If this happens, it is likely to lose the cost-effective advantages it now has and to become a relatively minor factor in the muddled future."

Baker's words are more chilling today than when delivered 20 months ago. Now they are closer to becoming true. —Ray Connolly
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<td>6.0 to 35 volts DC (50 volts on request)</td>
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<td>Temperature Range:</td>
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Digital module decodes dialing tones
for older phone exchanges

The spread of push-button telephones has been limited by the basic inability of electromechanical exchanges to convert the multi-tone frequencies into the series of pulses required to complete a call. Conversion circuitry has, of course, been installed in some exchanges, but it has been a patchwork of discrete components, inductors and filters that are mounted on a printed-circuit board.

Now, General Instrument Microelectronics is introducing a metal-oxide-semiconductor chip that does away with inducting coils and much of the discrete supporting cast. The AY-5-9800, employing a speedy digital tone-acquisition technique, can detect and decode digits as fast as they can be keyed in by a pushbutton phone, a considerable improvement over existing analog hybrid modules, according to Peter Rush, product manager, microprocessors and memories. In fact, it does its job as quickly as 15 milliseconds, well within the new international standard of 40 milliseconds.

Advantages. The chip also offers the expected benefits over hybrid modules of smaller size, lower current drain, cheaper assembly and final price. What’s more, the circuit module needs no factory adjustments. It gets all its timing signals from a single external 1-megahertz clock. Then, too, to meet various markets, the AY-5-9800 has seven mask-programmable options of center frequencies, tone accuracies, tone-acquire and tone-release criteria, one-tone or two-tone signaling, output-pulse length, and choice of output code for computer applications or 16-bit fully decoded output for telephone-exchange applications.

The company, now sampling the chip, targets a big potential market in North America and Europe. One exchange could absorb thousands because each incoming line requires a tone detector-decoder. But the chip-based module costs only $50, half that of the competing analog hybrid modules, according to James P. Smillie, telecommunications product manager. Moreover, GIM, which supplies 45% of Europe’s MOS phone circuits, sees a market in private exchanges where multi-frequency dialing is used internally. This intended market also would need the company’s AY-5-9100, which converts the keypad output into pulsed line output for the slower exchange switches.

In multifrequency phone-signaling systems, each digit is represented by a simultaneous burst of one high and one low tone, four of each being required. These can be paired in 16 high-low combinations to yield 10 digits plus six control functions needed by data applications. A problem with the analog modules is that they need eight narrow-bandpass filters to separate the tones. Besides the high component count, these filters, to achieve adequate tone discrimination, must have high filter delay, which means that tone bursts of less than 60 milliseconds can remain undetected.

GIM’s digital tone-detection technique, developed at its Glenrothes, Scotland, plant, simplifies things so that the incoming tone burst need only be separated into high-tone and low-tone groups. Thus, fast wide-bandpass filters can be used. The high and low tones then are processed by frequency-recognition logic. Each frequency is squared by a Schmitt trigger, then frequency-divided to produce a uniform mark-to-space-ratio pulse, which can be handled by conventional logic circuitry.

Each tone period is counted by a timer clocked by the external 1-megahertz clock, which can drive several modules. If the period fits any of the four tones, the tone is indicated by an appropriate register. Further circuitry, including a programmable logic array and a mask-programmable timer, ensure a proper signal, one that is undisturbed by noise.

It’s Tokyo’s turn this year

Nearly 330 companies, including about 70 foreign firms, will exhibit their products at Japan Electronics Show ’76, which will be held Oct. 22 through 27 in two exhibition halls at the Tokyo International Trade Center (Harumi Fairgrounds) alongside Tokyo Bay. This year, show officials say, more companies will occupy more exhibit space than last year in Osaka, where about 210 companies were represented.

Sponsored by the Electronic Industries Association of Japan, the annual show is held alternately in Tokyo and Osaka. The Tokyo show is usually slightly larger than the Osaka version, but improvement in the general economy is also one of the factors behind the pronounced upswing in exhibitor interest this year. Nevertheless, the total number of participants is not expected to bounce back to the level of the 1974 Tokyo show, where 280 Japanese companies and 152 foreign exhibitors signed up.

The components and parts section will be specially strong this year, occupying about half of the total space. In the industrial-products section, microprocessor applications will be featured. Stereo products, usually exhibited in the consumer-products section, will be seen this year at the International Audio Fair, scheduled for the same period in an adjacent exhibition hall. Other consumer products, including television sets, will be displayed in the consumer section as usual.

Electronics / October 14, 1976
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Circle 54 on reader service card
Applying an electronic "watermark" to conventional magnetic tape, EMI Ltd. has developed a tamper-proof security system for the growing market in credit and bank cards. Applicable, too, to transportation tickets, currency, and passports, EMI's proprietary process encodes a magnetic stripe by changing the direction of transverse areas of the magnetic fields on the tape. The special trick, which took 10 years to develop, is to do this while the plastic film containing the iron oxide particles is still hot and fluid. Once the plastic is cooled, the encoded data, which has a density of 50 bits per inch of track, is so embedded in the tape that no surface electromagnetic tampering can alter it. Having already sold a 350,000-card security system to the British Government, EMI is talking with British Airways and the International Air Transport Association, as well as eyeing a vast U.S. market for its tape and recording-head readers.

An automatic unit for checking defects in LSI masks has been developed by Nippon Jido Seigyo Ltd. Despite an operating speed called 10 times faster than with optical comparators, it can pick up defects that are skipped by optical systems. The key to the unit's operation is a flying-spot scanner, which works with two microscope-type optical systems spaced to view identical features in adjacent unit cells on same photomask. Any differences in the two cells is picked up and their locations stored. Comparison of one of the two cells with a third then give an indication of which cell is bad. What's more, an operator can indicate the locations of defects, and the unit automatically shows an enlarged image of each defect area on a 9-inch cathode-ray-tube monitor. The price tag of the defect inspection system is about $120,000 in Japan. Delivery is said to be about 6 months after an order. The system, for use with masks based on transmission of light, is not designed for working with reflected light from semiconductor wafers.

A management team representing Control Data France will visit the People's Republic of China this month in an attempt to further interest the Chinese Government in the purchase of a computer network to process oil-exploration information. Control Data, in collaboration with France's Compagnie Générale de Géophysique, has been negotiating the sale of a Cyber 172 computer, which is intended to be installed in Peking to enable the Chinese to better estimate the extent of their reputedly substantial oil reserves. However, the Paris-based international coordinating committee for exportation to Eastern countries has not yet approved the sale, and industry sources speculate Peking may soon look elsewhere for a similar system. The main objector to the sale, according to observers, is the U.S. Department of Defense, which feels the Chinese might employ the Cyber 172 for other purposes.

AEG-Telefunken has reported success in building terrestrial solar generators consisting of large-area polycrystalline silicon solar cells. Using a proprietary silicon material supplied by West Germany's Wacker-Chemitronic, the 10-by-10-centimeter cells exhibit an efficiency of better than 10%. While it won't put a price on these prototypes, AEG-Telefunken says...
International newsletter

it has reached an important milestone towards the goal of generating electrical energy from sunlight for less than $1 per watt.

Sanyo launches lithium battery line

A series of lithium energy cells will be placed on the market soon by Sanyo Electric Co. Designed to match requirements of a variety of miniature electronic equipment, their main selling point is their high energy density—typically four times that of silver oxide cells, five to six times that of alkali cells, and 10 times that of standard dry cells. What’s more, the nominal terminal voltage of 3 volts reduces number of cells required when voltages higher than the 1.5 v of most other cells are needed.

The first in the series, the LF-1/2 W has a diameter of 24.5 millimeters and a length of 2.8 mm—half as long as standard W-size cells. But 200 milliampere-hours are packed into its 4-gram weight. Two of these will power a new Sanyo 6-mm-thick calculator with a liquid-crystal display to go on sale next month. Price per cell in Japan is $1.75. Applications for other cells in the series, which use a lithium negative electrode and manganese dioxide positive electrode, include electronic watches, electronic cameras, hearing aids, tape recorders, communications equipment, and other cordless equipment.

West Germany’s Grundig plans hi-fi expansion

After a relatively slow startup period, West Germany’s high-fidelity equipment market is now entering a phase of strong expansion. That’s the assessment of industry observers at the HiFi 76 exhibition held in Düsseldorf last month. As officials at Grundig AG see it, the 1976 and 1977 West German hi-fi market will rise by 8% to 10% a year, and not before the mid-1980s do they expect that market to reach a saturation level of 80%. To cash in on this market potential, Grundig is considerably expanding its hi-fi equipment production capacity. The Nuremberg-based company already is West Germany’s biggest TV producer.

Interference suppression cables pushed by France’s LEAD

At least three U.S. companies, including one major automobile manufacturer, will be vying for American licensing rights to French-developed ignition cables that suppress interference. The companies anticipate that the Federal Communications Commission will introduce legislation that will impose lower noise emission levels and require a new type of ignition cable. The Grenoble-based research and development firm Laboratoire de’Electronique et d’Automatique Dauphinois in a joint venture with its French licensee Electricfil Boucicord will display the cable, based on frequency selective absorptive wires and capable of attenuating signals up to 1 gigahertz, in Detroit later this month.

Long-life lithium cell powers Siemens pacemaker

West Germany’s giant Siemens AG has joined the small number of companies that are offering pacemakers powered by a lithium-iodide battery, a type of battery with a useful life of up to 10 years. Delivering pulses that are 5.2 volts in amplitude and only 0.5 millisecond in duration, the company’s new lithium pacemakers consume very little current, so that the batteries last longer. The titanium-housed units are produced at Siemens-Elema, the West German firm’s subsidiary in Sweden, and come in various sizes—from small units weighing only 25 grams and intended for babies to regular-size versions for grownups.
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Excellent variable persistence means a bright, sharp trace you'd expect only on a non-storage scope. The result is a bright display of fast, low-duty-cycle repetitive signals.

Auto erase/Auto store. In Auto erase you adjust the number of displays per second up to 10 seconds. After that, it's all automatic, which means you simplify your set-up time and you eliminate smeared displays of digital data. It's a powerful tool for capturing those elusive glitches in data streams.

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For measurement convenience, the 1741A has a selectable 50Ω input in addition to the standard 1 MΩ input. A 5X magnifier permits two channel measurements as low as 1 mV/div. to 30 MHz, without cascading. The 1741A is priced at $3,950*.

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WE SELL PRODUCTIVITY.
VAST causing headache for Navy

$750 million, 15-year program to deploy general-purpose avionic test station 'has proven defective,' says report

by Bruce LeBoss, New York bureau manager

For three quarters of a billion dollars, should a government agency get what it ordered? The U.S. Navy is beginning to think so. It has spent upwards of $750 million and 15 years to develop, build, and implement a general-purpose automatic-test-equipment (ATE) system that was billed as the solution to its pressing problems of avionics testing. But now the fleet seemingly finds itself sinking in an ocean of problems surrounding the operation and maintenance of its primary equipment, the versatile avionics shop test (VAST) system developed by Harris Corp.'s PRD Electronic division in Syosset, N.Y.

VAST is intended as a total maintenance-support system aboard aircraft carriers and at shore installations. The concept for the system, designated AN/USM-247, was a general-purpose test station that is programmable and controlled by a computer. Such a system, it was reasoned, should be able to supplant the many kinds of support equipment used in the fleet. It was also to improve avionics turn-around time and reduce the space, manpower, and training required to operate support equipment.

But despite the fact that VAST has been operating for 1.1 million hours, the concept "has proven to be defective," says the recent "Report on Navy Issues concerning Automatic Test, Monitoring and Diagnostic Systems and Equipment," prepared at the request of the Assistant Secretary of the Navy for R&D.

Users in the fleet are frustrated, the report says. "Despite promises, they do not see the situation improving. From their vantage point, the opposite appears to be happening. Increasingly complex testers and software, still failing to do the job, are being furnished to them."

Frustrations. While the serious problems in the operation and maintenance of the Navy's support equipment involves virtually all aspects of ATE, the report calls VAST the "primary air-Navy tester" and says it "exemplifies the promise and the problems of ATE utilization." Typical of the problems cited are: ATE that doesn't perform as intended; is not reliable and causes a maintenance problem itself, and cannot be, or is not, properly used. Also, the report says the equipment has not fulfilled its mission: improving fleet readiness at reduced cost and manpower.

Explains Art Morrow, PRD's vice president, programs, "The problem is one mainly of management: management of the test-program tapes, of the carrier shops, and of the avionics configuration. Prime-weapons contractors wrote the test programs wrong, or there were changes in the avionics and the guy writing the test-program tapes was behind the group coming up with the changes, so the test programs were behind in diagnostics." Also, Morrow says that the failure rate of some of the avionics was "much higher than expected" and, as a result, VAST was so overworked that it couldn't be used for testing everything it was supposed to.

Morrow's view is shared to a large degree by Michael Myles, ATE section head at the Naval Air Systems Command in Washington. Says Myles, "The report reflects problems related more to the management of the VAST system than to..."
Probing the news

problems with VAST system hardware." When the Navy first began tracking the reliability of the hardware, the average mean time between failures was 24 hours. "It's now better than 160 hours as the result of corrective action that is still going on, primarily in the form of improvements to the test program," says Myles.

A dramatic example of the ATE problem is the support of the F-14 fleet-defense fighter, S-3A antisubmarine-warfare plane, the E-2C early-warning and control aircraft, and certain government-furnished equipment, notes the report. Because of limited workload capacity and the cost and time required for generating test software, many test requirements are being shifted from VAST to other equipment already in the inventory or being procured.

For example, Grumman Aerospace Corp. in Bethpage, N.Y., builder of the F-14 and E-2C, has contracted to supply the Navy with 25 computer-automated testers, designated CAT IID. They will be used for upwards of 200 F-14 and E-2C shop-repairable assemblies. Similarly, the Navy has ordered from Lockheed-California Co. of Burbank, builders of the S-3A antisubmarine plane, some 22 hybrid automatic test systems (HATS) to test modules.

Grumman also has an order to deliver a CAT IID system to PRD to develop test-program sets for select assemblies on PRD's own VAST system. Says Ed Stroud, F-14 support equipment manager at Grumman, "We're building support equipment for the support support system. "He asserts that the problem is the support of the F-14, A-7E, and some weapon-replaceable assemblies on other module testers such as HATS and CAT IID," says Myles. "This represents a change in the basic philosophy of using one system, VAST, for testing both. We found the workload is so great on VAST that it is practical to devote it primarily to weapon-replaceable assemblies."

Thus far, the Navy has ordered 88 VAST stations, of which approximately 75 have been delivered. The latest purchase is an initial $3.2 million contract for three systems to support new avionics on the A-7E trainer aircraft. In addition, the Iranian Air Force has purchased and received seven VAST stations to support its F-14 aircraft.

The problems with VAST today are that the weapons systems being supported have extremely complex electronics, says Jim Zing, an engineer at Lockheed's Automatic Test System division and developer of S-3A test-systems software for both HATS and VAST, "The avionics is super-complicated and difficult to understand," he claims. "VAST itself is a tough system for the operator to learn. People underestimated the training required for the Navy whitehats [service technicians] to learn how to run complex electronics on a complex support system."

Navair's Myles agrees that VAST requires highly trained maintenance technicians who must be more skilled than technicians for the previous manual ground-support equipment. However, the operator of VAST "can't just be a button-pusher. It requires additional training in trouble-shooting the system when it isn't working properly. Operator training on VAST is greater than anticipated, but we're using less people to do the equivalent job."

Training. The training related to VAST "is the overwhelming shortcoming" of the system, asserts Kamill R. Hilberth, a former PRD employee on the VAST program and now, as president of GT&T Industries Inc. of Panorama City, Calif., an ATE consultant to three VAST users. But Hilberth also points to deficiencies in test-program sets. "If VAST or the test setup is not acceptable, or if the unit under test has been tampered with, the test result will often become misleading."

The fact that VAST has met many of its objectives, although not to the degree intended, leads PRD vice-president and general manager Thomas H. O'Brien to assert that the program "is still pretty viable. We don't see its level of business going down for the foreseeable future."

The fact that the Navy would specify VAST for the new F-18 is, says O'Brien, "an indication that it really achieved what the Navy started out to do—have a system that could take on new aircraft without going to a completely new support system." He asserts that the capability to switch from one aircraft to another without a big initial investment for special support equipment, has saved the Navy hundreds of millions of dollars.

Control check. Navigation control unit from an S-3A antisub plane is tested by a technician using the USS Kennedy's VAST.
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Electronics / October 14, 1976
Japanese tangle in home-VTR contest

Videotape players from Victor and Matsushita blunt Sony lead as battle lines are drawn over price and cassette time

by Gerald M. Walker, Consumer Electronics Editor, and Charles L. Cohen, Tokyo bureau manager

Competing strenuously to establish the first beachhead with home-video-tape-recorders, an arena deserted by U.S. companies in favor of video-disk players, the leading Japanese consumer-product manufacturers have launched their new models in the market. The sudden rash of announcements, so typical of these companies, indicates that their products are getting closer together in prices, but not in designs that could lead eventually to some degree of compatibility.

Before the leader or leaders are clearly established, there will be more in-fighting and specification juggling. There are four entries now:

- Sony Corp. late last month came out with a stripped-down version of its BetaMax, priced at about $800, compared to about $1,000 for its SL-7300 video-cassette deck introduced last July. Sony also has cut the price of its one-hour tape cassette from about $16 to about $13.
- Victor Co. of Japan (JVC) earlier in September introduced its new VHS videotape recorder, priced at $890. Its two-hour cassette will sell for $20 when the product hits the domestic market some time this month.
- Matsushita Electric Industrial Co. Ltd. this summer began selling the VX-2000 for $730. Its single video head and cassette is an unusual configuration. Maximum playing time is 100 minutes for its $26 cassette.
- Toshiba and Sanyo, joint developers of a fourth system, came out in June with a new model V-Cord II capable of playing two-hour cassettes and priced at $1,150 and $1,220. The cassette sells for $24.

Hitachi Ltd., which is strong in video-tape-recorder technology, has indicated it will not introduce another home system, but prefers to cooperate with “whatever of the four systems offers the best opportunity to reduce the selling price substantially,” a company spokesman puts it.

Hitachi says that its dealers are clamoring for a VTR, and there’s a good chance that the company could get a unit built on an OEM basis by Victor—in the same way JVC now supplies its U-Matic players—on the shelves in time for the year-end sales. Other companies without such strength also want to offer VTRS to their dealers, so there is intense pressure on the competitors to show up best early in the race.

Advantages. According to initial reactions in Japan, JVC and Sony appear to have the best chance, but Matsushita has the largest dealer network in the nation and enough marketing clout to put its VX-2000 across. Prospects for Sanyo and Toshiba now appear to be fading. In part, that’s because their two-hour playing time is achieved by a skip-field technique that diminishes picture quality.

Of course, it’s possible that two different systems could coexist, to use a favorite Japanese term. Matsushita, for instance, is likely to market both its own recorder and the VHS designed by JVC. In addition, Sony, through its prompt response to the JVC announcement, has served notice that it will fight hard to make Beta-Max, the first home VTR on the market, a winner.

What market do these firms expect? This year, it’s not very spectacular—about 200,000 units could be produced, most in the last quarter, and half destined for export. Next year, the producers estimate that 200,000 to 400,000 units may be sold, so a safe bet would be 300,000. At the outset, export sales to the United States represented around 60% of Japanese production, but today it’s closer to 50%. Next year, because of the lower prices, the percentage could shift again, this time in favor of domestic sales. The next surge in sales probably won’t

<table>
<thead>
<tr>
<th>COMPARING VIDEO-TAPE RECORDER</th>
<th>JVC</th>
<th>VHS</th>
<th>Sony BetaMax</th>
<th>Sanyo/Toshiba V-cord</th>
<th>Matsushita VX 2000</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tape speed (mm/s)</td>
<td>33.35</td>
<td>40</td>
<td>73.87</td>
<td>52.1</td>
<td></td>
</tr>
<tr>
<td>Relative speed (m/s)</td>
<td>5.8</td>
<td>6.9</td>
<td>7.73</td>
<td>9.09</td>
<td></td>
</tr>
<tr>
<td>Weight (kg)</td>
<td>13.5</td>
<td>20.5</td>
<td>17</td>
<td>20</td>
<td></td>
</tr>
<tr>
<td>Dimensions of cassette (mm)</td>
<td>188 x 104 x 125</td>
<td>156 x 96 x 25</td>
<td>156 x 108 x 25</td>
<td>213 x 146 x 44</td>
<td></td>
</tr>
<tr>
<td>Max. recording time (min)</td>
<td>120</td>
<td>60</td>
<td>120</td>
<td>100</td>
<td></td>
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</tbody>
</table>
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Frequency Extension Options

<table>
<thead>
<tr>
<th>Frequency Range</th>
<th>Prescaler</th>
</tr>
</thead>
<tbody>
<tr>
<td>520 MHz</td>
<td>Covers frequency range of 50 to 520 MHz, using a scaling ratio of 4. Sensitivity is 15 mV rms (AGC). Maximum allowable input is 5 V rms (fuse protected). VSWR less than 2:1 into 50 ohms for levels less than 1 V rms.</td>
</tr>
<tr>
<td>1000 MHz Prescaler</td>
<td>Covers 50 to 1000 MHz using a scaling ratio of 8. Sensitivity is 15 mV rms, and maximum allowable input is 5 V rms (fuse protected). VSWR less than 2.5:1 for levels less than 1 V rms.</td>
</tr>
<tr>
<td>1250 MHz Prescaler</td>
<td>Covers 50 to 1250 MHz using a scaling ratio of 8. Sensitivity is 20 mV to 1000 MHz, increasing to 40 mV rms at 1250 MHz. Maximum input 5 V rms (fuse protected), and VSWR less than 2.5:1 for levels less than 1 V rms.</td>
</tr>
</tbody>
</table>

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Time Base Options

<table>
<thead>
<tr>
<th>TCXO</th>
<th>Oven-Stabilized</th>
</tr>
</thead>
<tbody>
<tr>
<td>Frequency</td>
<td>10.00 MHz</td>
</tr>
<tr>
<td>Aging Rate (constant temperature)</td>
<td>&lt;±3x10^-7/mo. &lt;±1x10^-7/mo.</td>
</tr>
<tr>
<td>Temperature Stability: 20°C-30°C</td>
<td>±2x10^-7 typ. ±3x10^-9 typ.</td>
</tr>
<tr>
<td>0°C-50°C</td>
<td>&lt;±5x10^-7 &lt;±1x10^-9</td>
</tr>
<tr>
<td>Line Voltage: (±10% change)</td>
<td>&lt;±5x10^-9 &lt;±3x10^-9</td>
</tr>
</tbody>
</table>

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Probing the news

come until the makers reduce the price below $700, to about the price in Japan of an 18-inch color-TV receiver, which now starts at $480.

Prospects. That point is some years away. In the meantime, a fairly substantial market is expected to develop at present prices. As for a standard VTR compatible with decks from other manufacturers, that's a long way off, and it will depend largely on how dealers and consumers react to the present line-up.

Nevertheless, all the contenders fear harm may result from confusing potential customers. "Perhaps," Yutaka Ikeda, general manager of Toshiba's TV-receiver division suggests, "none of the present helical-scan systems will be the best. There may be a better chance for a different approach, such as a fixed-head player."

Besides the electronics in a VTR, there are also mechanical considerations. The dimensions of the cassette are also a factor because people will expect to keep libraries of recorded television broadcasts. JVC's entry has a cassette measuring 188 by 104 by 25 millimeters to provide two hours of playing time. Victor's slow tape speed of 33.35 millimeters per second and small drum diameter of 62 mm help conserve tape. But they combine to give a tape speed relative to video head of only 5.8 meters per second, which makes it necessary to use sophisticated circuit techniques to get good reproduction.

Although Sony has a one-hour tape now, it claims that technology for a two-hour version is well in hand, although no product is imminent. Matsushita promises by the end of the year a two-hour tape that will play on VX-2000s.

Sony attributes the lower price for the single-touch BetaMax SL-7100 in part to production savings resulting from increased output of the BetaMax 7300 and in part from the removal of a couple of frills from the original. Essentially, the new video-cassette player is the same, except audio-dubbing and pause features have been removed.

The firm expects to increase manufacturing from the present 10,000 units a month to 15,000 units a month in April when additional production facilities go on line. Sony asserts that even though it is losing market share, there is an increase in total sales.

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DC voltage measurement and dc ratio are standard features. DC voltages are measured over five ranges, with resolution between 1 µV and 10 mV and a basic accuracy of ±0.001% (10 ppm) for 24 hours, 20°C ±1°C. Starting with the lowest range, a maximum display of 312.5 mV is possible with a resolution of 1 µV. Displays on the lower ranges are in volts, followed by an exponent display of −3.

Two types of ac measurement options are available for the 8500A. While only one can be installed in the instrument at a time, removing one option and installing the other requires a minimum of time and/or operator training. At power up or after reset, the front panel displays whether the averaging converter, true rms converter or neither is installed in the instrument.

The Averaging Converter (Option -01) measures up to 1000V ac on four ranges with a bandwidth from 30 Hz to 100 kHz and accuracies up to ±0.05% + 5 digits. The True RMS Converter (Option -09) measures up to 100V ac on four ranges with a bandwidth from 10 Hz to 300 kHz and accuracies up to ±0.1% + 15 digits.

Resistance measurements can be made on eight ranges from 1Ω full scale to 100 MΩ full scale with the Ohms Converter (Option -02). Basic accuracy from 100Ω to 1 MΩ is ±0.003% + 1 digit, with resolutions up to 100 µΩ obtainable.

Both ac and dc current can be measured with the Current Module (Option -03) provided one of the ac options is installed in addition to the basic dc. Current measurements to 1A can be made with sensitivity to 1 nA. Accuracies to ±0.03% + 10 digits, for dc readings and to ±0.06% + 8 digits for ac readings. Bandwidth of the 100 µA through Amp ranges is 30 Hz to 10 kHz. For the 1A range only, the bandwidth is 30 Hz to 3 kHz.

Guaranteed accuracies for the 8500A measurement options are based on 90 days, 18°C to 23°C.

Three Remote Interface options are available with the system. Only one of the three may be installed at a time; however, one can be easily exchanged for another when the top cover is removed. This allows the instrument to be used with more than one interface system, requiring only that additional modules for the desired interfaces be obtained.

The IEEE Standard 488-1975 Bus Module (Option -05) provides an eight-bit (one byte) parallel interface. The Bit Serial Asynchronous Interface Module (Option -06) interfaces the 8500A to systems using either RS232B, RS232C, or Current Loop interface. Selection of type and Baud rate is made with bit switches accessible through an entry.
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Port on the rear panel. And the Parallel Interface Module (Option -07) provides a 16-bit duplex register interface compatible with mini-computer and microprocessor systems.

A non-volatile calibration memory module stores correction factors desired from a standard input during CAL mode operations. It can also be used to compensate for long-term drift, eliminating the need for manual adjustments or trips to the calibration laboratory. This al-

lows the operator to remove power from the instrument or the system to suffer a power failure without loss of automatic calibration factors. The battery permanently installed on the module will keep power on the memory to retain the stored data in excess of 90 days after removal of power.

Service capability is one of the strong points in the 8500A program. Of course, extensive overload protection has been designed into the instrument. But should problems develop, most of them can be handled in the field by using the available service aids. An extender card, a bus monitor, a test module, and a static controller, together with diagnostic programs and the microprocessor control should handle 60%-80% of most troubleshooting problems.

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Electronics abroad

Polish plant ready to roll

Power semiconductor facility, set up in $9.7 million deal by Westinghouse, starts production in November

by Howard Wolff, Associate Editor

Late next month, the first production thyristors will roll out of a plant 20 miles south of Warsaw in Poland—and Donald A. Walczak of New Alexandria, Pa., will sit back, take a deep breath, and begin thinking about a vacation.

Walczak has been a virtual Pennsylvania-to-Poland commuter since the spring of 1974. That's when the Westinghouse Electric Corp. team he heads as project manager began fulfilling its $9.7 million contract with the Polish government to design, equip, and put into operation a power-semiconductor plant. And before that there were two years of negotiations.

The deal, the first sale of technology by an American company to Poland, is the largest technology sale ever made by Westinghouse. The company is so pleased with results that its representatives are shuttling among Eastern European capitals looking for similar business. Nothing is yet in hand, but the governments of Bulgaria, Rumania, and Czechoslovakia have been sounded out. As Walczak explains: "It takes so long to determine what they want; everything is tied in to five-year plans."

The Rumanians already have a plant equipped by an American company—a tuning-diode facility that started manufacturing devices last summer. It was outfitted by ITT and has licenses and knowhow from Intermetall GmbH, the German member of the ITT Semiconductor group. And in Bulgaria, a selenium-rectifier manufacturing facility has been set up by ITT's Components group in Europe.

Warsaw is eager. But Poland, of all the Eastern bloc nations, appears to be making the strongest effort to import Western technology. The figures back them up. Of Unitra's total production, about 28% is based on foreign licenses. And in semiconductor products alone, the figure is much higher—around 65%. As for integrated circuits, Japanese technology is used by the big Polish combine mainly in linear circuits, and French knowhow—from Thomson CSF—primarily in digital types.

The Unitra numbers are a good indication of Poland's drive away from agriculture and toward industrialization, because electronics is leading the drive. Unitra, with 1975 volume of about $1.1 billion, is the largest single electronics producer in Poland, accounting for half the output. Unitra executives expect the tempo to pick up and forecast an

Builders. Donald Walczak, left, was project manager as Westinghouse designed and built a power semiconductor plant for Poland. Maurice C. Sardi, right, is general manager of Westinghouse's Semiconductor division. Poland paid $9.7 million.
annual growth of 15% to 20% for some years to come [Electronics, July 10, 1975, p. 68].

Westinghouse officials are careful to note that their success in making the sale and delivering the product to the Poles is not necessarily transferable to other electronics industries. The power semiconductor industry, they point out, is unique. It's a difficult business in which to make money, they say, because it requires a heavy investment in research and development and equipment. But from a manufacturing standpoint, the market is a relatively low-volume one, so that to recover the investment a company like Westinghouse must either price its products high or sell its technology. And since the competition for that relatively small market is stiff, costs can't be covered through sales alone. The result is, in the words of one official, "If you can do better by selling technology than product, then you do it." Still, even the Westinghouse officials agree that it's not a good business decision to sell technology if you can sell product.

**Competition.** Their experience brings up two big questions: one for the company in particular, and one for American electronics firms in general. The first is the question of putting a potential competitor in business, specifically, in Poland's case, one whose government can marshal national resources of money, time, and labor almost to any lengths.

No problem, says Maurice C. Sardi, Semiconductor division general manager. "The benefits of selling technology are sufficient to offset the disadvantage of setting up competitors," he says. The way Sardi explains it, "when negotiating the contract, it was necessary to freeze the technology at a particular point four years ago. But since then, Westinghouse has been continuing its own R&D; we are now four years smarter. I can sell technology through, let's say, phase one. But it would take the same resources to get to phase two as it took to get from zero to one, and there are no option clauses in the contract under which we would provide the next phase to Poland. Moreover, the Poles want to advance themselves. They now have the core, but how they perpetuate it is their problem."

The division's operations manager, Mickey Turner, adds, "It takes a long time to learn the semiconductor business, and it will take many years for the Poles to get good yields. We just showed them how."

But perhaps the larger question is one of exporting a precious American resource—technology—to a nation that's part of a bloc that could one day face the U.S. in a hot, as well as an economic war.

Sardi concedes that there has been some concern that Westinghouse's sales might lift the Poles to the level of Western power-semiconductor makers. However, he says, "Either we believe that we in the U.S. have the resources, talent, and systems to apply technology better than anyone else or we don't. That's what's important: the ability to apply the technology. After all, I can give you a Rolls Royce, but if you can't drive it, you can't enjoy it. We, as Americans, simply can't live under a bushel basket."

The Westinghouse team agrees that one of the pleasant surprises it encountered while building and setting up the plant was the cooperation it got from the Polish government. "They assigned talented people to us," says Sardi, "and the work proceeded in an atmosphere of mutual confidence and trust. There was never any feeling that anyone was trying to put something over on anyone else. The Polish people are hard negotiators, and they held the line hard at getting what they paid for, but there was no attempt to go beyond that." The result is that the project was completed right on time, says Walczak, and Westinghouse's profit from the fixed-price contract met its projections.

The major difficulty was the unavailability of certain materials, particularly metals. Two years were spent seeking local materials that would meet Westinghouse's specifications because the Poles were loath to use hard currency to purchase material outside the country. But the biggest lesson that the Westinghouse officials learned from the project, they agree, is to have one man in charge of the project from the first day. That man was Walczak.

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Probing the news

Military electronics

PDP-11 chosen as tactical base

DEC’s commercial architecture to be used for new military computer family in competition with IBM, Interdata

by Ray Connolly, Military Electronics editor

The search is over. After a year’s examination and deliberation, the Army and Navy have agreed on Digital Equipment Corp.’s PDP-11 architecture as the base on which they will attempt to build a new family of software-compatible military computers for the 1980s. Known as MCF—for military computer family—the program is a joint effort led by the Army Electronics Command at Fort Monmouth, N.J., with support from the Naval Air Systems Command in Washington and Naval Research Laboratory.

Selection of the PDP-11 architecture over IBM’s System/370 and Interdata Inc.’s 8/32 provides a significant boost in the military computer market for the Maynard, Mass., manufacturer of commercial machines. The selection came only days before ECOM selected United Technologies Corp.’s Norden division to develop an artillery system using PDP-11 technology (see “Norden’s new artillery: PDP-11”).

The selection of DEC’s commercial architecture by a committee of representatives from more than 25 Army and Navy organizations “demonstrates that Alexandre Dumas was right,” observed one Defense Department monitor of the program: “Nothing succeeds like success.” But more than that, it confirmed earlier military concessions that tactical computer systems—with their expensive and incompatible software packages—have failed to keep pace with commercial developments.

More surprising was the choice of Itel Corp.’s Applied Technology division in Sunnyvale, Calif., to carry out a year-long system-implementation study. “Itek’s computer expertise is not well known outside of ‘spook’ circles,” says one official, referring to the company’s classified work for government security agencies. The Itel award is only for $600,000, and reflects elimination of fiscal 1977 Navy funds for the MCF program. The Navy is struggling to hang in, however, with uncommitted prior-year money, but at a lower level. As it stands now, the bulk of MCF’s funds—and direction—is from the Army. And part of MCF is the Navy’s former all-applications digital computer (AADC) program.

Processor due. With the selection of the PDP-11 computer family architecture, the program expects to have an instruction-set processor, or ISP, available by year’s end. By that time also, Army and Navy staffers will have selected prior-generation military computers that may be backfitted with MCF software, and processor specs for older computers will be prepared and validated. The specs of the PDP-11 family, plus those for the earlier military machines, Coleman explains, “will be basic input data for subsequent MCF implementation planning.”

Along with the plan for system implementation, a parallel plan for support software will be procured from at least two contractors to permit a military choice for an integrated support software system. Initially, that system will consist of PDP-11 support software, of course, but it will be gradually augmented through efforts of the Pentagon and its contractors. The software plan will define the system and its major components; estimate their relative cost-effectiveness; consider alternative acquisition methods; estimate return-on-investment for each, and recommend a preferred method.

Norden’s new artillery: PDP-11

Digital Equipment Corp. has won another victory with September’s $6.2 million award by the Army’s Electronics Command to United Technologies Corp.’s Norden division in Norwalk, Conn. The contract is for engineering development models of a new artillery computer for use by individual gun batteries. Norden’s new battery computer system (BCS) will be a militarized version of DEC’s PDP-11 system. DEC licensed Norden to militarize its small, highly successful commercial machine about a year ago.

Of the eight bids received by ECOM for the new system, Norden’s entry became a finalist along with those of Litton Data Systems, Van Nuys, Calif., and Teledyne Brown Engineering, Huntsville, Ala. Should Norden’s version prove successful in field tests, it could spell bad news for Litton’s Tacfire program now used by the Army for artillery automatic fire control. Adoption of the Norden system for computation of artillery-fire trajectories and rates at the battery level by individual units would free them of dependence on the larger, division-level Tacfire system and presumably reduce requirements for Tacfire hardware and interconnection. The battery is the artillery’s counterpart of the infantry company.
Probing the news

Computers

Curve is upward for Britain’s ICL

by William F. Arnold, London bureau manager

On Oct. 1, International Computers Limited began its new fiscal year in a buoyant mood. Once a stodgy amalgamation of various computer entities, the British company over the last several years has transformed itself by some bold strokes, big government aid, and a few key managers imported from Sperry-Univac into an aggressive company whose new emphasis befits the first word of its name and not the last.

Now among the two or three largest computer companies outside the U.S., the firm plans to pull more than half of its annual sales in the new year from exports—up from less than half this past year. Executives foresee growing continental markets, especially in West Germany, France, Sweden, and the Netherlands. And they wouldn’t be surprised if the new fiscal year’s performance approximates the rise of the one now closed. When all the sums are finally totted up for the period just ended, ICL is expected to show profits on total sales of around $500 million—up comfortably from the approximately $432 million a year ago.

The buoyancy seems justified. The firm’s New Range of large mainframe machines launched two years ago is now selling better than expected after some initial software teething problems, although sales remain mostly in Britain. Despite French opposition, the company is confident that it will grab an $8 million deal to supply its largest computer, the 2980, to the European Economic Community, with possible later add-ons. The small-computer 2903 series is attracting customers in Europe and the U.S. And the first products from Computer Peripherals Inc., the joint venture with NCR Corp. and Control Data Corp., are beginning to emerge.

But the company’s new international thrust is best shown by the bold stroke earlier this year when it grabbed the Singer Business Machines division’s profitable international business at almost distress-sale terms [Electronics, April 12, p. 55]. With a speed uncommon to large companies, especially British concerns, ICL plucked off for a mere $2 million down payment Singer’s overseas customer base and the option to market and manufacture as much of the firm’s equipment overseas as it wanted. Even if it takes all of the estimated $70 million international assets, ICL will have four years to pay it off.

What the deal gave was a crack at Singer’s range of hardware—the point-of-sale equipment, the 1500 intelligent terminal, and the System 10 small computer system. Though there’s some overlap between the System 10 and the 2903, the new gear fills gaps in ICL’s lineup. More importantly, the deal enlarges the company’s customer base by a third to about 5,000 users and increases its overseas business by 50%.

Under terms of the deal, ICL begins the new fiscal year having chosen what it wants. According to Peter V. Ellis, director of the worldwide marketing group, beginning in February, the company will begin making Singer’s System 10 in Britain, as well as continuing production in Singer’s former factory in Utica, N.Y. The 1500 will be made in Utica, probably not in Britain, but will be sold abroad by ICL. The point-of-sales series “we’ll continue with either in the U.S. or the U.K.,” he says. The firm also plans a follow-up terminal to the Singer 920 in 1977.

How important is the Singer deal to ICL? “We have a 15-million-pound business in France,” says Ellis. “With Singer it becomes a 25-million-pound business.” With Singer, the companies will be able to set up trading operations in new territories, such as Italy, Spain, Norway, Finland, Portugal, and Latin America. Holding on to Singer’s customer base, especially getting the customers to trade up into ICL equipment, might be difficult, but Ellis concedes no problems.

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Probing the news

alone. In 1977 it plans to extend the New Range downward by introducing the 2950 for applications in retail distribution and data-base systems in manufacturing. The new mainframe will have 20% lower prices and 25% less throughput than the 2960 introduced this year. New software products and elaborations on the 2903 series are planned, too.

Basically, as Ellis points out, ICL is a systems company that doesn't make every bit of hardware it needs. For example, it buys Digital Equipment Corp. processors for some of its data-entry systems. The philosophy extends even to the research level, according to Edwin S. Mack, director of product development. In explaining why magnetic bubble memories "can't win," he quotes three essential rules: don't invest in them, other devices or processes are faster; if they work, buy in; if not, forget about it.

Waiting for CCD. Consequently, ICL expects to stay with standard 16,384-bit random-access memories for the next several years until charge-coupled devices prove themselves in performance and price. Mack admits however, to tinkering with a nonrevolving drum memory made from standard CCD chips. Motorola's standard MECL 10-K logic will remain in the big machines, for a while, too, although new logic can be added modularly.

But ICL does watch new technology. "What really turns me on are holographic memories," Mack enthuses, though he says they're years away. Also, "we're playing with thick film, of course." Part of the firm's thinking says that in a few years' time special-purpose computers will become more important. So, the company is developing associative processing and associative memory techniques, built around a distributed-array processor for threedimensional problem-solving such as weather prediction, and a content-addressable memory for fast text retrieval, such as for telephone directory inquiries. And, it's working on marrying that to a computer-voice system for possible use by airline reservation networks.
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Imagine viewing a horizontal slice of a living human body without harming the subject. A detailed reconstruction of just such a view is possible with computerized axial tomography, a marriage of computer and X-ray tube that gives physicians a revolutionary diagnostic tool.

While conventional X-ray procedures are better than such invasive techniques for diagnosis as surgery, they are far from perfect in their ability to locate an abnormality precisely or, for that matter, even to determine that an abnormality exists. In conventional radiology, pictures are obtained by subjecting the patient to radiation from an X-ray tube acting as a point source and recording the attenuation of the rays through the thickness of the patient's body on a single film (Fig. 1).

This may result in overlapping of anatomical features so that some information may be hidden from view. What's more, the film detects less than 1% of the projected X-rays, which limits the resolution of differences among soft tissues. Sometimes a gas or liquid is injected to increase contrast within the body. But these techniques can be painful and are not without danger to the patient's life.

Computerized axial tomography replaces many of these invasive procedures and provides a reconstructed three-dimensional image of the inspected organs with a resolution greater than has ever been possible without surgery (Fig. 2). This is achieved by viewing the patient via X-ray imaging from numerous angles, mathematically reconstructing the detailed structures, and displaying the reconstruction on a cathode-ray tube (Fig. 3).

The first CAT scanner, which was designed for brain examination, was introduced by the United Kingdom's EMI Ltd. in 1973 (see "How it works," p. 92). The whole-body scanner (Fig. 4), a rather new entry into the field, is intended for the examination of the chest cavity.
1. **Overlap.** In conventional X-ray imaging, like that used in common chest X-rays, two internal organs in the same transaxial plane are projected to the same place on the film. Their images will overlap.

2. **Cross section.** By applying computerized tomography to the head, a cross-sectional view of the brain and sinus cavities can be obtained. The dark areas show where air is trapped in nasal cavities, and the white areas correspond to the skull bone, and limbs, and is particularly useful for viewing the abdomen.

**Scanning the body**

In the basic scanning process (Fig. 5a), a collimated X-ray beam passes through the body, and its attenuation is detected by a sensor that moves on a gantry along with the X-ray tube. The tube and detector move in a straight line, sampling the data 180 times. At the end of the travel, an 1° tilt is made, and a new linear scan begins. The assembly travels 180° around the patient’s couch.

This procedure results in 32,400 independent measurements of attenuation. The data generated can satisfy the solution of equations containing that number of unknowns. This is sufficient for the system’s computer to produce an image measuring 180 by 180 pixels (square dots of light whose intensity varies to reflect the attenuation). But the large number of independent measurements would require a typical scan time of 5 minutes.

A major effort in the design of new CAT systems has been reducing this scan time. The shorter it is, the smaller the chance the patient will be injured by the X radiation. In addition, short scan times permit a higher patient throughput, greatly desired by hospitals that are spending over a half-million dollars for one system. Moreover, shorter scans reduce the chance of blurring in the reconstructed image caused by the movement of the patient or of internal organs.

**Putting the picture together**

As important as short scan times are short reconstruction times. It is the total of these two imaging periods that truly limits throughput, for the patient remains on the couch throughout the entire imaging procedure. With the 5-minute scan time of the earliest scanners, patient throughput amounted to two per hour, at best. Efficient mathematical treatment in the computer, as well as an analog-to-digital converter for data manipulation, can reduce calculation time to 10 seconds per scan, so that total imaging can be reduced to less than half a minute.

One technique reducing total scan time makes use of a fan-shaped segment of the X-ray beam and ten or more detectors (Fig. 5b). At the end of the linear scan, the gantry tilts 10° or more, instead of the 1° step of the simpler scanner. Total scan time drops below 10 seconds.

Yet another method for cutting scan time is to use a beam covering the width of the body, completely eliminating the linear travel of X-ray tube and detector (Fig. 5c). However, a much larger number of detectors, perhaps several hundred, are necessary.

**Sensing the signal**

Generally, CAT scanners use high-atomic-weight scintillators, such as those containing efficient radiation detectors like sodium iodide or calcium fluoride. All scanners use photomultipliers as noiseless gain stages to convert the scintillation light into a direct current. A problem with the detection material is their production of after-glow components in the scintillator and the photomultipliers.

Another problem is the high cost of the detectors. At present, a scintillator coupled to a photomultiplier tube, plus the associated peripheral circuitry, may cost more than $200. If faster scans require the number of the detectors to be increased to several hundred, their price becomes a major part of the system’s cost. An array of 300 detectors, for example, may cost $60,000, or more than 10% of the total selling price of the most expensive CAT scanners.

Before reconstructing an image, the linear X-ray attenuation coefficient, μ, must be determined at each pixel. This coefficient is a measure of the stopping power...
3. Taking a slice. A computer can reconstruct a sectional view, or transaxial slice, from X-ray projections via a detector that takes the place of X-ray film. Internal-organ projections don’t overlap.

of tissues for an X-ray exposure. The quality of the reconstructed image is a matter of the differentiation between \( \mu \) at different points and of the size of each pixel. Both of these factors warrant some discussion.

The differences in \( \mu \) of the various body tissues are slight (see the table). Typically, the CAT scanner can measure differences in \( \mu \) of less than 1%. An important factor in the measurement is the spectrum emitted by the X-ray tube. It is determined by the maximum accelerating voltage applied between its anode and cathode. For a peak voltage of 130,000 electronvolts, the weighted center of the spectrum is in the vicinity of 70 kiloelectronvolts. Although \( \mu \) is highly sensitive to changes in the energy of the incident beam, the average energy of 70 kev yields meaningful results.

It turns out that typical tissue contains mostly elements of low atomic weight. At the photon energies used in CAT scanners, the dominant effect controlling interaction of these photons in tissue is the Compton effect, in which the impact of an X-ray photon with an electron is accompanied by a transfer of energy and a drop in the X-ray’s frequency.

The loss in energy is proportional to the density of electrons, which results in linear relationship between tissue density and alterations. So the differential attenuation coefficient \( (\Delta \mu) \) is well correlated with the increase in specific gravity above that of water (see the table). Hence, the image presented as a result of computerized tomography can be regarded as the mapping of densities.

For a given element of tissue of thickness \( \Delta X \), the radiation leaving this thickness is related to the input radiation \( I_o \):

\[
I = I_o e^{-\mu \Delta X}
\]

For a tubular segment of tissue of length \( L \), the X-ray leaving the tissue, \( I \), is expressed as a line integral of the absorption of \( I_o \):

\[
I = I_o \exp (- \int_0^L \mu (x,y) \, dy)
\]

The profile of the line resulting from a straight-line scan of the patient is called the shadow function. It is proportional to the total attenuation of the ray. As defined in Fig. 6,

\[
g_\theta (x_i) = -\ln I/I_o = \int_0^L \mu (x_i, y) \, dy
\]

As the detector measures \( I \), an electronic log circuit or a reference table stored in the computer files takes a logarithm. The problem is reduced to reconstructing the function of the attenuation coefficients \( \mu (x,y) \) from the measured projections.

The distribution of frequencies in the X-ray beam traveling different lengths (the center of the body versus the edge) shifts because the low-energy components of the beam are stopped more easily. Since the energy of the detected photons is not measured, information about the changes is lost.

Adding errors

But the computation of \( \mu \) assumes some mean energy that in reality is not constant, so errors are introduced in the form of image artifacts. Earlier solutions involved surrounding the head with a water bag so that the beam always travels the same distance. This is not suitable for the thorax and abdomen, and at present, this effect is corrected mathematically, as discussed below.

The importance of pixel size in determining the quality of the image can be made clear with an example. To reconstruct an image of 256 by 256 pixels requires sampling each projection at 512 intervals. For a scan traverse of 25 centimeters as with a large adult abdomen, the resulting pixel size is 1.0 millimeter. This is about ten times worse than the resolution on conven-
How it works

Since the 1972 introduction of the first scanner for computerized axial tomography by EMI Ltd., the market for such devices has grown to almost $80 million. Sales will reach $100 million by 1979, estimates Creative Strategies Inc., a market-research firm based in San Jose, Calif. The market is so large that more than a dozen companies have taken aim at it by designing and introducing CAT scanners [Electronics, Dec. 25, 1975, p. 33].

There are two basic types: head scanners and whole-body scanners. Machines of both types resemble the block diagram of the EMI CT1010 head scanner below, although body scanners differ in some aspects because of their more complicated task.

On both types, the X-ray beam passes through the body for detection by scanners. Analog outputs from these detectors go through signal-conditioning circuitry that amplifies, clips, and shapes the signals. A relatively simple analog-to-digital converter than prepares the signals for the computer.

In some systems, the computer controls all the operations of the scanner, including X-ray-tube positioning and multiplexing of the a-d converter channels. In other systems, the computer is aided by hard-wired logic for X-ray control and for signal processing. In some cases, as in Elscint's Scan-ex, more than one computer is employed to share these tasks. In every system, the computer performs some of the mathematical functions described in the text, and formats data for display.

Whole-body scanners must accommodate a wider range of body sizes and tissue densities than brain scanners, and must cope with the movement of internal organs, as when the patient breathes. Thus, they must have much lower scan times and higher resolution, as well as being larger to accommodate the entire body.

Reconstructing a slice

The basic equations for reconstruction from projections were treated as early as 1917 by Johann Radon, an Austrian mathematician. They have been elaborated since then for applications in microwave astronomy, for mapping microwave solar activities, electron microscopy, for an understanding of the three-dimensional structure of a body, and nuclear medicine, in which the distribution of isotopes within a body is desired. The reconstruction methods can be classified into three major techniques:

- Back projection, which is analogous to a graphical reconstruction.
- Iterative methods, which implement some form of algebraic solution.
- Analytical methods, where an exact formula is used. Two of these are filtered back projection, which incorporates the convolution of the data and Fourier filtering of the image, and two-dimensional Fourier reconstruction, a more direct technique.

The method of back projection without any further processing is simple and direct, but it suffers from image artifacts. The subject (Fig. 7a) consists of a point of finite size. The three projected rays generate profiles on the projection planes. To reconstruct the point, these profiles are back-projected in the form of rays perpendicular to the image planes, and the three resulting patterns are added algebraically. The intersection of these rays reconstruct the point, but the rays are also retained in the image as artifacts in the form of dim lines, and the point itself is transformed into the shape of a star (Fig. 7b).

Although iterative methods have been used in reconstruction tomography, they tend to require long compu-
5. Scanning. The earliest CAT machines made a linear traverse before taking a 1° rotation (a). Using a fan-shaped beam and an array of detectors (b), larger steps can be taken and the process speeded up. If the fan is large enough, (c), no transverse motion is needed.

A spatial filter would be able to remove the blurring artifacts. This is accomplished by convolving the shadow function with a filter so that each point in the projection has a negative value, instead of 0, at every point other than its proper place in the projection. The resulting profiles are then back-projected and added (Fig. 8).

The net effect is such that the data of each pixel has a contribution near 0 in areas beyond the pixel, and the pixel itself receives positive contributions from the projections. The negative portion of each shadow function cancels out image artifacts that otherwise would be caused by other functions.

Mathematically, this can be presented by writing the Fourier inversion formula of \( M(u,v) \) which is the two-dimensional Fourier transform of \( \mu(x,y) \) in polar coordinates:

\[
\mu(x,y) = \int_0^\pi \int_0^{2\pi} \exp[2\pi i(x \cos \theta + y \sin \theta)r] \frac{M(r, \theta)}{r} dr d\theta \tag{1}
\]

Substituting

\[
t = x \cos \theta + y \sin \theta
\]

\[
\mu(x,y) = \int_0^\pi Q(t, \theta) d\theta \tag{2}
\]

The integral inside the square bracket in (3) can be recognized as the inverse Fourier transform of the function \( M(r) \) at the point \((t, \theta)\). By the convolution theorem, it is equal to the convolution of \( g_0(x) \) with a filter \( \Phi(t) \) such that \( F(\Phi(t)) = |t| \), where \( F \) denotes the Fourier transform. Thus,

\[
\mu(x,y) = \int_0^\pi Q(t, \theta) d\theta = \int_0^\pi g_0(t-\tau, \theta) \Phi(\tau) d\tau = g_0(t) * \Phi(t) \tag{4}
\]

\( Q(t, \theta) \) is the filtered projection and equations (2) and (4) define the back-projection process.

A more direct method, Fourier reconstruction, was first introduced by Ronald N. Bracewell in 1956. The advent of the fast Fourier transform in the middle 1960s made this method a powerful tool, because it reduces the number of repetitive calculations. Special-purpose, high-speed computers for performing the fast Fourier transform are now available.

The two-dimensional \((u,v)\) Fourier transform of the attenuation-coefficient map, \(\mu(x,y)\) is:

\[
M(u,v) = \int_0^{2\pi} \int_0^{2\pi} \mu(x,y) \exp[-2\pi i(ux + vy)] dx dy
\]

At \(v = 0\), a line in the Fourier plane is

\[
M(u,0) = \int_0^{2\pi} \mu(x,0) \exp[-2\pi iux] dx
\]

where \(g_0(x) = \int_0^{2\pi} \mu(x,y) dy\) is the shadow projected at \(\theta = 0\).

The Fourier transform of any projection \(g_0\) is equal to the function \(M(u,v)\) on the line in the \((u,v)\) plane which passes through the origin in direction \(\theta\).

In the direct method, many projections in angles such

| Measured Linear Attenuation Coefficient (\(\mu\)) and Specific Gravity of Human Tissue at 60 KeV |
|-----------------------------------------------|-----------------|-----------------|
| \(\mu\) (cm\(^{-1}\)) | \(\Delta \mu\) above water (%) | Specific gravity |
| Water | 0.205 | - | 1.00 |
| Whole blood | 0.214 – 0.322 | ~ 4.3 | 1.034 |
| Red cells | 0.222 | 8.7 | 1.09 |
| Spleen | 0.216 | 5.3 | 1.06 |
| Fat | 0.190 | ~ 7.8 | 0.93 |
| Liver | 0.221 | 7.8 | 1.05 |
| Heart muscle | 0.212 | 3.4 | 1.04 |
| Breast | 0.189 | ~ 8.4 | 0.97 |
| Brain: | | | |
| white matter | 0.216 | 4.8 | |
| gray matter | 0.213 | 3.9 | |
| Meningioma | 0.214 | 4.3 | 1.05 |
6. The shadow knows. The output from the detector produces a shadow function that is proportional to the attenuation caused by varying depths and densities of tissue.

as \( \theta^*, \Delta \theta^* \), and \( 2 \Delta \theta^* \) are measured as the scanner rotates, and their Fourier transforms are computed. The values of \( M(u,v) \) on a set of rays crossing the origin, where \( u = v = 0 \), are thus determined.

By interpolation, the values of \( M(u,v) \) at the Cartesian grid points of the \((u,v)\) plane can be found. The inverse Fourier transform of \( M \) can be taken by a fast-Fourier algorithm and the desired function obtained.

Coming improvements

Although X-ray tubes have been around for many years, their design essentially has not changed. New demands by CAT technology have just started to make a dent in the conservative designs of the X-ray-tube manufacturers. There is a lot of work needed on higher power, power density, and controlling the output-energy spectrum.

With the present price of over half a million dollars each, only major hospitals can justify acquisition. Since prices reflect performance, it is likely that, as the CAT scanner gains acceptance, new markets will be opened to "slow" and lower-priced machines.

Major cost reductions are likely to occur with the advent of new detectors, high-voltage power supplies, and special computing hardware performing on-line reconstruction and display while reducing memory requirements. Some researchers have already presented results promising to reduce much of the computational load by means of reconstruction techniques based on analog or optical methods.

There is considerable research in progress on direct calculation of the effective atomic weight of tissue based on the measurement of attenuation coefficients at different accelerating voltages of the X-ray tube. This, in conjunction with scanning in planes that are not perpendicular to the body's vertical axis, promises to provide additional information on the structure of tissue in man.

7. Artifacts. In reconstruction by adding the back projections produced by the shadow functions, the back-projected rays are added to the reconstructed image as artifacts, or unwanted points. The original circular structure is transformed into a star.

8. Clean-up. By filtering the signals from the detectors so that the contribution just beyond each image point is negative, unwanted images, like the color dot, are eliminated from the reconstruction.
System requirements dictate fiber-optic component parameters

When working with a new technology like optical fibers, it is desirable to treat a communications link as a subsystem and design in what is actually needed, rather than tailoring it to existing components.

by Ray McDevitt, Harris Corp., Electronic Systems Division, Melbourne, Fla.

The most satisfactory way of designing a fiber-optic link, in terms of its overall cost, performance, and reliability, is to let system requirements determine the component specifications. However, as in most new technologies, initial component developments usually determine what system configurations emerge first. But, basing the system configuration on such standard components, which usually reflect what has been done, rather than what can be done or what is actually needed, is not the optimum approach.

Figure 1 details the steps involved in moving from user needs to actual specifications for the optical-fiber cable, transmitters, receivers, and other link hardware. In this scheme, "existing or planned device capability" has the lowest priority and, while important, it should only serve as the initial interaction between the user's needs and existing component capability.

The top priority usually is given to deciding whether to allot one channel to each optical fiber in an all-parallel approach, or multiplex several channels onto each fiber, or to combine the two approaches. If any multiplexing is involved, the next questions to ask are: which type would be more cost-effective, and what degree of multiplexing would be most economical? (Above a certain limit, the more channels squeezed into a fiber, the more stringent become the fiber-optic component specifications and the higher go their prices.) The answers to these questions determine component requirements, which last of all, are traded off against available device capabilities.

In favor of multiplexing

As it turns out, some degree of multiplexing is often desirable in fiber-optic communications links because most users want the freedom it gives them to add extra data channels to a system with relatively little difficulty. In many ground-based systems, for example, the most effective link consists of a combination of parallel data transmission and some sort of multiplexing—usually frequency-division multiplexing in the case of an asynchronous, multi-channel digital-data interconnection.

In fact, FDM is the type of multiplexing generally preferable for fiber-optic links. Time-division multiplexing, although it can be used, could require horrendously complex hardware to handle the variety of data rates commonly met with in these applications. FDM, on the other hand, much more readily accommodates asynchro-
2. Satellite-terminal link. Fiber-optic cables that need no protection from electromagnetic interference and have large bandwidths can drastically reduce the cost of linking ground terminals with control facilities as far as two kilometers away.

Noisy rates, with either phase-shift or frequency-shift keying, and therefore makes it easier to send multiple streams of signals at different rates over the same fiber. This simplicity gives FDM an economic edge over TDM, even after consideration of its need for fiber-optic components with tighter specifications and, therefore, higher prices. For instance, in FDM, the receivers and transmitters may have to meet the equivalent of analog-transmission requirements in their handling of an increased bandwidth and signal fidelity. The fiber cable may also need more stringent specifications to prevent harmonic and intermodulation distortion.

Another consideration is the link’s interface with existing equipment. If this includes much analog voice or video equipment, a key decision is whether to convert analog signals to digital form. Instead of inserting hardware to convert the analog signals to digital for transmission over the fiber-optic link and then converting them back to analog form at the receiving end, it may be less costly overall to use fiber-optic components directly. The savings from not using converters are partially offset by the greater expense of fiber-optic components capable of meeting the heavy demands of analog transmission. In terms of signal fidelity, a signal-to-noise ratio of 40 to 50 decibels is usually needed, while harmonic and intermodulation distortion must be 55 dB down to meet existing standards. For transmitting analog signals over long distances requiring multiple repeaters, direct analog transmission is much less attractive because of the buildup of noise in repeater lines.

Alternatively, to reduce the distortion requirements and hence the price of the fiber-optic components, the analog signals could be frequency-modulated. Technically, this approach is feasible—the increased bandwidth needed by fm formats would fall well within the large fiber-optic bandwidth capability. Economically, the outcome is less certain—the added cost of the fm equipment could outweigh the reduced cost of fiber-optic components.

The new signal formats that have been developed for fiber-optic systems could also prove worthwhile. They differ from conventional wire-transmission techniques in that they exploit the large bandwidth of a fiber-optic link and its freedom from electromagnetic interference. As an example of the possibilities, it is easy in a noise-immune optical system both to send and receive a high-amplitude pulse and to include it in a signal format for synchronization purposes.

Perhaps the best way to illustrate the top-down approach to fiber-optic component specification is with an example. The purpose is to demonstrate that, by starting with user needs, the design will call for a different set of components than would be required if there are no tradeoffs at the subsystem level. For brevity, only one cycle of the design evolution shown in Fig. 1 will be assumed, and the major cost tradeoffs for each approach will serve as the primary evaluation criteria.

Defining user requirements

Satellite terminals are often located some 0.5 to 2 kilometers from a control facility containing the multiple digital and analog data sources. For the one shown in Fig. 2, a broad mix of data rates is required. But of the 20 receive channels, only seven require a data rate faster than 1 megabit per second, and of these seven, the fastest needs a rate of only 6 Mb/s. The remaining 13 channel rates vary from 12 kilobits/s to 960 kb/s. Of the 20 transmit channels, half have data rates faster than 1 Mb/s, the fastest being 4.2 Mb/s, and the other half have

| TABLE 1: COST OF COMPONENTS OF OPTICAL-CABLE LINK |
|-----------------------------------|-------|-------|-------|
| Optical cable (10 fiber channels) | $27/meter | $13.50/meter | $6.25/meter |
| Transmit/receive component sets    | $2,000 | $1,500 | $1,000 |
| Connector sets (installed)        | $200 | $100 | $75 |

Electronics/October 14, 1976
3. Optical-cable link. Multiplexing channels over a single 10-fiber optical cable provides both flexibility and room for expansion. The link can transmit 25 data channels in each direction. The composite FSK/FDM signal covers 100 kilohertz to 28 megahertz.

Cost of the all-parallel approach

For comparison, the cost of a set of transmit/receive modules is set at $2,000. This figure, which applies to quantities of 50 or more, assumes that each of these modules is ready for production without additional work. A full-duplex interconnect requires two fiber-optic channels and two sets of fiber-optic transmitters and receivers. Table 1 shows the price trend assumed for fiber-optic components over the next three years. Using the figures for today, the cost of 20 full-duplex channels for an all-parallel two-km interconnect link is:

- Four 10-fiber cables, with a 10-db/km loss, at $27/meter = $216,000
- Forty connector sets at $200 each when installed = $8,000
- Forty transmit/receive sets at $2,000 each = $80,000
- Two racks at $1,000 each = $2,000
- Six power supplies (±15 volts, 5 v) at $200 each = $1,200

The total recurring cost of such a system, therefore, works out to be $307,200, and the average cost of a simplex channel is therefore $7,600.

However, in planning for a system that is scheduled for production in a year or two, today's component costs may not be realistic. Actual system tradeoffs must be made partly on the basis of the cost of subsystems at the time of production. Accurate prediction of future prices is difficult, but it's safe to assume that they will drop as fiber-optic components mature and more systems are designed.

On the optimistic assumption that cable costs will drop to half of the present $27/m next year and transmit/receive units will price out at $1,500 a set, the cost per simplex channel would fall to $4,380 ($174,200/40). Further in the future, cable costs could again drop by a factor of 2 to $6.25/m and transmit/receive sets could cost only $1,000 a set, and the cost per simplex channel would then be $2,405 ($96,200/40). These figures apply to components having enough capability to close a two-km link at 30 Mb/s per channel with over 6 dB of margin with a bit-error rate of 10^-6.

Multiplexing approaches

The next step is to calculate the cost of a system using multiplexed data channels with the same components and to select a specific multiplexing technique to start the comparisons. In this case, time-division multiplexing is discounted because lack of time synchronization among the interconnect's data channels would make this approach overly complex and costly.

Several frequency-division-multiplexing designs, using various electro-optical components and several subcar-
Carrier-modulation types are possible. The best initial choice is frequency-shift keying (FSK), using noncoherent detection, rather than either phase-shift keying or quadrature PSK. This is because, when properly implemented, noncoherent FSK conserves bandwidth and provides a bit-error-rate performance that approaches that of PSK. Moreover, FSK has been around for some time, with two results: it is very cost-competitive, and practical filters providing over 30 dB of crosstalk rejection are available. It is therefore reasonable to assume that an FSK approach can easily handle the varying data rates needed.

To sum up, the FSK/FDM format allows a number of channels, multiplexed together, to occupy a 30-megahertz electro-optical channel. The bandwidth of such a fiber is normally sufficient for five channels to be multiplexed onto it with enough guard space to make extremely sharp cutoff filters unnecessary. On the other hand, as the channel data rate goes up, fewer channels can be multiplexed. This limit on bit-rate growth is less with the all-parallel approach, since its transmit/receive hardware can handle up to the maximum 30-Mb/s rate on each channel. To get around the problem, FSK systems generally include a few spare parallel channels. In the following analysis, therefore, a 10-fiber cable is used, even though with five channels multiplexed on each fiber, only eight fibers are necessary for a 40-channel system.

The hardware costs of the noncoherent FSK/FDM approach with five channels per fiber, excluding the FSK hardware, breaks down as follows:

- One 10-fiber cable at $27/m (2 km needed) = $54,000
- Ten connector sets at $200/set installed = $2,000
- Ten transmit/receive sets at $2,000 each = $20,000
- Two racks at $1,000 each = 2,000
- Two power supplies at $600 each = $1,200

The total per-simplex-channel costs without FSK is $1,980 ($79,200/40).

Table 2 compares the present and future costs of this and the all-parallel approach. The $1,980 subtracted from the $7,680 cost of each all-parallel channel leaves $5,700 for the FSK/FDM hardware. Since such hardware in 1976 comes to about $2,000 a channel, a saving of more than $3,500 per channel results. If 1977's projected costs (Table 1) for the optical cable, transmit/receive sets, and connectors are used, then each simplex channel's cost drops to $1,155. This amount, plus the FSK/FDM-multiplex cost/channel, still compares favorably with the $4,300/channel cost for the all-parallel system. Three years from now, the cost/channel, not including the FSK/FDM hardware, is estimated at $661. These comparisons point out that if the per-channel FSK/FDM hardware costs $1,700 or less, then even in 1979, the FSK/FDM approach would be preferable, assuming that an all-parallel approach offers the user no needed extra capacity. This is often true, since the capacity of a 30-MHz channel greatly exceeds anticipated bit-rate increases over the next five years.

Obviously, the bandwidth and number of channels that can be multiplexed, the distance covered, and all assumed costs have a big impact when considering system tradeoffs. For example, if a maximum data rate of 6 Mb/s per channel were specified, then the multiplexed approach would use more fiber-optic components making the all-parallel approach more attractive. With an FDM approach, the transmitter circuit would have to be capable of handling the more demanding analog
TABLE 3: DATA-CHANNEL ASSIGNMENT FOR INTERCONNECT FACILITY

<table>
<thead>
<tr>
<th>Fiber</th>
<th>Channel No. 1 100 kb/s</th>
<th>Channel No. 2 500 kb/s</th>
<th>Channel No. 3 2 Mb/s</th>
<th>Channel No. 4 4 Mb/s</th>
<th>Channel No. 5 6 Mb/s</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>100 kb/s</td>
<td>450 kb/s</td>
<td>1.0 Mb/s</td>
<td>3.0 Mb/s</td>
<td>4.2 Mb/s</td>
</tr>
<tr>
<td>2</td>
<td>50 kb/s</td>
<td>Spare</td>
<td>1.0 Mb/s</td>
<td>3.0 Mb/s</td>
<td>4.2 Mb/s</td>
</tr>
<tr>
<td>3</td>
<td>50 kb/s</td>
<td>Spare</td>
<td>900 kb/s</td>
<td>3.0 Mb/s</td>
<td>4.2 Mb/s</td>
</tr>
<tr>
<td>4</td>
<td>12 kb/s</td>
<td>Spare</td>
<td>Spare</td>
<td>3.0 Mb/s</td>
<td>4.2 Mb/s</td>
</tr>
<tr>
<td>5</td>
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requirements and would be more complex and costly than the simple on/off transmission techniques needed with the all-parallel approach.

The point to be made, however, is that unless system configuration tradeoffs are made, optimum components cannot be developed, and specifications for the transmitter/receiver bandwidths, intermodulation and harmonic distortion, and the capability to control bias currents for the different modulation indices would be quite different.

The frequency plan

Once the data rates of all channels are finalized, it is essential to consider the bandwidth requirements of each subcarrier before allocating channel-frequency slots. In determining such a frequency plan, several factors must be considered:

- The lowest-frequency channel should not approach dc; otherwise, large and rather expensive components, such as capacitors and inductors, would be needed.
- The actual position of each subcarrier on the frequency spectrum depends primarily on data bandwidth; the subcarrier frequency must be considerably greater than the data bandwidth.
- In spacing subcarrier frequencies, the harmonic content of adjacent channels must not overlap, and the crosstalk between channels must be held to an acceptable level.

The spacing depends on the characteristics of the filter used to separate channels multiplexed onto the link. In practice, available relatively low-cost four-pole Bessel-type filters are usually adequate. Should it be necessary to pack more channels into the allowable frequency allocation, a sharper cutoff filter could be used. However, these filters are more costly and usually less stable, and, as more poles are added, in-band amplitude and phase response become much more difficult to control.

Final system configuration

Figure 4 shows the frequency plan for the FSK/FDM approach to the design of the optical-fiber-cable link between a satellite terminal and its control facility (Fig. 3). This frequency plan is flexible, and its flexibility provides for growth. Table 3 shows the exact data-channel assignment for the example problem.

The five data channels can handle a wide range of data rates that are multiplexed over a single optical fiber by FSK/FDM. Thus, if one 10-fiber cable is used, the system has the fiber capacity of 25 data channels in each direction—five more than the system needs at present—to allow for future expansion. Identical FSK demultiplex hardware is used for each of the single fiber-optic channels. The composite FSK/FDM signal covers from 100 kilohertz to about 28 MHz, as shown in Fig. 4. Channel-to-channel isolation of more than 30 dB is provided by standard filters, and, besides supplying adequate transmission-channel bandwidth, each optical transmitter and receiver has intermodulation and harmonic distortion down more than 30 dB.

Note, finally, the capacity of the link. Any subchannel can accommodate a digital data rate with bit-error rates of less than $10^{-9}$ up to the nominal non-return-to-zero (NRZ) rate of that channel.

For more information on fiber-optic technology, components, and systems see Electronics, Aug. 5, pp. 81—104, Sept. 2, pp. 94—99, and Sept. 16, pp. 113—116.
Frequency doubler and flip-flop make adjustable phase shifter

by Vladimir Brunstein

A frequency doubler for operation at the voltage levels of either complementary-MOS or transistor-transistor logic can be built with an inverter and a comparator. And if the doubled signal is then fed into a flip-flop, the output has the original input frequency, shifted in phase by an amount that depends on the reference voltage applied to the comparator. This shifter has been used to adjust the phase of the output from a phase-locked-loop device.

In the doubler circuit (a), the rectangular input signal and its inverted form are applied to capacitors $C_1$ and $C_2$. Their triangular ramps, which are 180° out of phase, are added through diodes to produce a sawtooth wave at twice the input frequency. This sawtooth is applied to the noninverting input of the comparator, producing a output at $2f_m$ with a duty factor that depends on the setting of the reference voltage at the inverting input.

Adding an edge-triggered D-type flip-flop to the circuit (b) yields an output signal of frequency $f_m$ phase-shifted with respect to the input signal. Varying the reference voltage $V_{ref}$, the phase shift between the output and the input signal can be set at any value between 0° and 180°. However, the duty factor of the output is always 50%. The frequency limit is set by the frequency band of the comparator used.

The component values shown were used for 60-hertz operation in a circuit that phase-locks the output of an uninterruptable power supply to the ac line. Connecting the output of the power supply back into the line provides a load that is inductive or capacitive, depending on the phase shift that is set by the reference-voltage potentiometer.

**Frequency doubler and phase shifter.** Inverter and comparator ICs make up a simple frequency doubler (a); the duty cycle at the doubled frequency is adjusted by the potentiometer that controls $V_{ref}$. If the doubled frequency is applied to a D-type flip-flop (b), the output is a square wave at the original frequency, but shifted in phase from the input. Phase shift can be varied from nearly 0° to nearly 180° by varying $V_{ref}$. Waveforms shown in (c) illustrate the complete operation of the circuit.
Chopping mode improves multiple-trace display

by C. S. Pepper
IRT Corp., San Diego, Calif.

A chopped mode of signal sampling extends the usefulness and versatility of an oscilloscope display that shows several signals simultaneously. The eight-trace oscilloscope switch described in [Electronics Dec. 25, 1975, p. 75] operates in an alternating mode that uses both beams of a dual-beam scope. One beam repeats as usual, and the second steps through a repeating pattern of eight vertical levels. Each level displays one line of digital data; the result is a nine-channel trace-sequential display.

The sequential mode of sampling is satisfactory for data that is repeated at a sufficiently high rate. But, if data repeats slowly or occurs only once, all of the signals must be sampled at high speed and displayed during a single sweep. The circuit shown in Fig. 1 provides for both the chopped and alternate-sweep modes of signal sampling and display.

In this circuit, the 74S151 is an 8-line digital multiplexer. Inputs A, B, and C pick one of the eight digital signals for connection to the output at pin 5. A 74151 may be used if the faster Schottky device is not required.

The CD4051 analog multiplexer takes its inputs from

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1. Signal traces. Scope displays two, four, or eight digital input signals, timeshared on either a chopped or sequential basis. The digital multiplexer selects individual inputs in cyclic succession, and the analog multiplexer separates their traces vertically. Both multiplexers are driven by a counter that counts pulses from a 555 timer for the chopped mode or sweeps from the scope for the alternate-sweep mode.
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Corning Glass Works, Corning, N.Y. 14830

Corning Electronics / October 14, 1976
2. Fast steps. Staircase waveform positions scope trace in the chopped mode. Each step is 5 microseconds wide.

3. Outputs displayed. The chopped mode produced these waveforms of (a) the four outputs from a 7490 decade counter, and (b) the first eight outputs from a 7442 decimal decoder driven by the 7490. Input pulse rate was 1 kHz.

4. Slow process. Timing diagram of a slow speed controller with total trace length of 0.5 second was photographed by use of chopped mode. Display was triggered from the negative transition of the upper trace.

a resistance-chain divider that establishes a set of eight equally divided voltage levels. These levels appear at the output in the same sequence as the digital signals from the 74S151 because the two multiplexers have common addressing. A 2N2222 transistor, Qb, provides drive power for the analog output, and the digital and analog signals are summed at the output to the oscilloscope.

Addressing is obtained from the 7493 counter. The circuit utilizes a single channel of the oscilloscope, with external triggering from one of the signals or a related source. The Qa, Qh, and Qe outputs provide fast chopping of the data. If a slower chop signal can be used, dropping back to Qh, Qe, and Qb will double the ON time for the same chop frequency.

Switches S1 and S2 provide options of eight, four, or two traces. For eight traces, both switches are in position (a), and for a two-trace display, both switches must be in the grounded (b) position. If only switch S1 is in the (b) position a four-trace pattern, composed of traces 1, 2, 5, and 6, will appear. This can be a useful option because, at times, eight traces are too many, and the switches provide a means of momentarily reducing the clutter. Note that only addressing is changed; a two-trace display spaces the traces the same as the original eight.

A simple 555 timer circuit provides the counter input when switch S3 is set for the chop mode. The values shown will provide a trace-bit time of about 5 microseconds, or a staircase time of 40 µs. The chop waveform is shown in Fig. 2. Each step is 5 µs—fast enough to cover the line breaks in the traces. Since the chop is not in synchronism with the data, surprisingly fast data can be viewed in the chop mode. The trace of Fig. 3a shows the four outputs from a 7490 decade counter with a 1-kilohertz input. Figure 3b shows the first eight outputs from a 7442 decimal decoder tracking the 7490.

The waveforms in Fig. 4 are those of a very slow control system, with a total sweep time of 0.5 second. The sequences shown are all for one single event. The only way to identify these scope traces is by photography—the single sweep goes by much too fast to begin to track the events taking place on the eight traces. The eight-channel switch and Polaroid film make the photography simple.

For operation in the alternate-sweep mode, the scope sawtooth provides the clock input to the 7490 counter. Because the signal level from some scopes is much too high for the 5-volt transistor-transistor-logic counter, the drive circuit with Q2 and Q3 is included. This circuit works well with a 30-V sweep in, but for other voltages, a revision of R17 may be needed.

Three compensation elements are included in the circuit. The first of these is C3, which may require some tweaking to best flatten the top step of the staircase shown in Fig. 2. The second is C2, which eliminates overshoot at the end of each step. The effect of overshoot is to draw a thick trace; the 330-picoFarad value shown may require trimming to produce the narrowest trace and to eliminate ringing. Finally, R1 and C1 should be trimmed to produce the narrowest trace.
Data-acquisition system built modularly around Intel 8080

by Jonathan A. Titus
Tychon Inc., Blacksburg, Va.

Early this year, engineers at Tychon were asked by a U.S. Government agency for an appraisal of what was available to monitor transducer outputs from chemical instrumentation. But when no commercial data-acquisition system could be found to meet the agency’s requirements, Tychon proposed a design for a suitable microprocessor-based data-acquisition controller and won a contract to build it.

The agency wanted the monitoring system to have:
- A flexible and easy-to-change configuration.
- A mix of analog and digital inputs and outputs.
- A serial input/output port with both an RS-232-C interface for modems and a 20-milliampere current loop for teletypewriters and printers.
- A minimum number of controls.
- Complete software.

In addition, an acceptable system had to be simple enough for people who were not hardware or software experts. Yet all the information necessary for making software and hardware changes had to be available to guide those expert enough to do so.

Tychon’s survey of data-acquisition systems turned up none that met all these specifications. Some units would have required extra interfaces and software, while others required data to be in a preset format or within decade voltage ranges. Still others were far too complex or lacked flexibility, though, otherwise, they were well-engineered instrumentation. Even those that were microprocessor-based were not flexible enough because they did not come with fully documented control programs, and this lack would have made them difficult to modify for specific needs.

To obtain the necessary flexibility, Tychon opted for a microprocessor-based modular design. Use of the microprocessor, instead of hard-wired logic, cut design time and kept system cost down to $2,500. The modularity applies to software, as well as hardware, so that both can be easily changed to match a change in application. Yet nontechnical people can easily program and operate the system.

The microprocessor chosen was an Intel 8080, which is in widespread use and is second-sourced. Many peripheral and control chips and a wide variety of software are available for use with it. Around it was built a microcomputer with a bus signal structure to provide hardware flexibility. Thanks to the parallel-wired bus, the microprocessor module and other function cards may be moved from slot to slot, and more memory may be added if needed.

The modular hardware

For the function cards or modules, standard printed-circuit cards were adopted. The dual-width Digital Equipment Corp. size was picked because of the wide variety of function modules being made the same size. A version of the controller using a selection of these cards is shown in Fig. 1.

Besides the central-processing-unit card, available function cards include standard analog input and output cards, standard digital I/O cards, an analog-to-digital converter, a front-panel controller, and an asynchronous serial interface, as shown in Fig. 2.

Each analog input may be equipped with a differential programmable-gain instrumentation amplifier, when necessary, to provide for a wide range of analog-signal levels. The a-d converter is a prepackaged unit, complete with internal analog multiplexers, amplifier, and sample-and-hold circuitry; its resolution is 12 bits, even though many applications call for only 8 or 10 bits.

Among the digital I/O devices is the solid-state-relay interface card with four relays for 110-volt ac control. It is particularly valuable where external controls are needed. A dual 1/O-interface circuit card helps with special digital I/O, since an area of the card is open for construction of special circuits, such as flags, registers, counters, and controllers.

Standard I/O proved preferable to programmable peripheral chips such as Intel’s 8255, which were not used. Though very flexible, they are expensive and need more software than do the logic chips used with standard latched output ports and three-state input ports. The additional software needed by the 8255 is not extensive, but it might be difficult for a user to understand—an unwanted complication.

The front-panel controls posed a problem. The panel could hardly contain all the special hardware needed for

1. Modular. The Tychon data-acquisition controller is a microprocessor-based system with a variety of analog and digital inputs and outputs. Both the hardware and software are modular.
all possible functions. Instead, all front-panel controls and displays are treated as I/O devices under software control. The controls and displays supply information to the 8080 and display information from it. What's more, because their decision-making and control functions are in software, they can easily be added or subtracted from the system as its needs to be changed. Even the functions can be changed. Still, in some applications only an on/off and a reset switch may be needed.

The hardware for the front-panel controller card had, consequently, to be very general in purpose. The card contains both input ports (for switches, keyboards, and push buttons) and output ports (for seven-segment displays, light-emitting diodes, and an audio alarm). It also contains a real-time clock with the time base derived from the 8080's crystal clock.

A hardware clock was preferable to a software-timer subroutine because the former allows for subsequent addition of interrupts. Interrupt-control signals are available, but are not used in most systems. The clock control, like the front-panel controls, is handled by software subroutines that are available to the user.

The standard asynchronous serial-interface module makes it possible to use a teletypewriter or terminal with the data-acquisition controller. This full-duplex, four-wire interface is one of the easiest methods of interfacing to a computer, and it can transfer data as fast as 9,600 bits per second. Its presence means that a similar port could be used on a remote computer to accept data from the data-acquisition controller and return processed data or instructions to it.

In one application, the serial I/O port was used to send data to a computer 1,000 feet away and also to run diagnostic software with a nearby terminal. A software-sensed jumper in the serial-interface plug told the 8080 whether it was supposed to transmit (jumper in) data or run (jumper out) the diagnostic programs. This convenience enables even nontechnical people to test special functions easily.

The diagnostics are used for adjustments to the
3. Subroutine. Typical of the data-acquisition controller's software subroutines is this one for the 16-channel analog-to-digital converter. Note the use of complement instruction (CMA) to replace hardware inverters on the a-d module. Diagnostic software is also available.

amplifier and analog-to-digital converter and for testing all front-panel devices. For instance, with the diagnostic program, a dc voltage reference is fed into the amplifiers so that gain settings and offset adjustments can be made. A similar procedure is used to calibrate the a-d converter. Users of microprocessor-based systems should insist upon diagnostic software, since check-out and repair become very expensive without it.

The modular software

The 8080 software may at first seem complex because it controls so many parameters, including analog and digital I/O, the asynchronous I/O port, the front panel, and diagnostics. However, it is written in easily understandable and usable subroutines, or "modules," like the logical and electronic functions. Even though each data-acquisition controller may be used for a different task, the software is very similar for all.

Usually, standard "off-the-shelf" software subroutines are linked in the correct sequence within the main controller program, but, on occasion, some software may be customized for special applications. This modularity decreases the time and cost of software development. It also means that new modules may be added and languages like Basic or APL may be used. An example of a software subroutine for a 16-channel analog-to-digital converter is shown in Fig. 3.

In a typical application, 16 analog channels are scanned, and the data is formatted and serialized on a user-selected time interval. Only 768 bits of programable read-only memory and 1,024 bits of random-access memory are used. Very little of the RAM is actually used merely a few locations for a stack and some temporary data storage.

Memory within the data-acquisition controller may be expanded in increments of 256 or 1,024 bytes of PROM and 1,024 bytes of RAM to make up the maximum of 65,376 bytes that can be supported by an 8080 system. Data may also be logged locally on paper or magnetic tape, printed on a teletypewriter, or displayed on a terminal.

Although the original intent was not to design a general-purpose microcomputer system around the 8080 microprocessor chip, additional memory would enable editors and assembler programs also to be run in the data-acquisition controller. A D-BUG software package also available for the 8080 system allows a programer to modify the random-access memory for a set point in a chemical process-control system, look in the stack, and examine the register.
Power-supply choice looms large in sophisticated designs

Complex electronics demands more from supply; cost and size become more crucial as other components shrink

by D.J. Blattner, Circuit Design Editor

The power supply is the unsung hero of electronic equipment with its behind-the-scenes contribution. It's often the last part to be specified, almost as an afterthought. But with today's complicated equipment, that may be a mistake. As the electronics grows more sophisticated, it makes greater demands on the supply. And the shrinking of component costs and sizes makes the power supply loom larger in the overall system.

Surveying equipment manufacturers in a variety of fields discloses a growing concern with the type of supply specified. The familiar make-or-buy decision is still important, although specialized demands of some equipment virtually preclude off-the-shelf supplies.

The growth in electronics applications translates into a wide range of circuit and operational demands. For example, logic circuitry is satisfied with 1% regulation and is immune to noise below the switching level. But analog circuitry requires regulation on the order of 0.04%, and it processes noise pickup along with the signal. Another factor is operating efficiency, which involves more than saving power. The greater the efficiency, the smaller the supply and the less the heatsinking. And, of course, regulation and efficiency are only a few of the considerations in choosing the type of power supply.

There are three basic technologies for regulated-power-supply design: ferroresonant, linear, and switching. All three types of supply accept an ac line voltage and deliver one or more regulated dc output voltages, but they differ in the ways that they set and regulate the dc levels (see "How they do what they do," p. 109). The relative advantages and disadvantages of the three types of supply are summarized in the table.

Having decided upon the power-supply type and performance that he wants, the equipment manufacturer is faced with a make-or-buy decision, even with special requirements that could be built outside to the company's specifications by a custom supplier. In some cases the custom product can be less expensive than an off-the-shelf unit, because it delivers only the performance required.

The standard product, designed to meet broader needs, may have capabilities, unnecessary in the particular application, that add to the price tag. For the most part, however, the skill, efficiency, and manufacturing volume of the standard houses allow them to provide a wide range of supplies at unbeatable prices.

An increasingly popular middle ground in the make-or-buy decision on ferroresonant and linear supplies are the submodules sold by power-supply and component manufacturers.
makers. The equipment manufacturer needs only to couple a transformer with a submodule that includes rectifier, regulator, and filter (Fig. 1). Several submodules can be used to provide various voltages, perhaps at different locations for point-of-load regulation. With ferroresonant transformers, the regulating transistor can operate near saturation, and the submodules provide good efficiency, regulation, and economy.

A similar semi-custom procedure applies to switching supplies. Hybrid and monolithic integrated circuits are available for regulating and controlling the dc-to-dc conversion (Fig. 2). The equipment manufacturer selects only the switching power transistors and the magnetic components.

About 80% of all power supplies are manufactured in-house, mostly by large firms. "Generally, a company can think about designing its own power supplies when its volume or above, it has the in-house expertise to keep out of trouble technically, and the return on the money invested is as attractive as from alternative investments," says Chuck Acken, product manager at the power-supply facility of Hewlett-Packard in Rockaway, N.J.

**Making the decisions**

However, a survey of manufacturers of different kinds of equipment discloses that some large firms have good reason to farm out some or all of the power-supply manufacturing. While some kinds of equipment generally may rely on just one of the three types of supply, there are special cases to which another type is better suited. Computers are a case in point. Their makers show strong reliance on switching power supplies, which they usually manufacture themselves. But there are some interesting exceptions.

Five years ago, Digital Equipment Corp., Maynard, Mass., used linear and ferroresonant supplies almost without exception. Now, says Philip Tays, manager of power and packaging systems, DEC is shifting more and more to switching supplies because they’re low in cost and light in weight. And he predicts that, over the long term, they will deliver better mean times between failures. Nonetheless, the company has designed compact ferroresonant supplies, too, such as the one in Fig. 3 for the PDP-8/A minicomputer.

The make-or-buy decision includes consideration of cost, system schedule, reliability, and performance, according to Tays. The vendor’s ability to respond to design changes during development is a substantial consideration too. "We'd like to find off-the-shelf conservative power-supply designs to do advanced functions," he says. "But it doesn't happen often enough."

The company sometimes has supplies built outside to its designs. "Power supplies are heavy and costly to ship," Tays says, "and we may have them built locally to our design for the savings in transportation costs. We don't want to build in-house if we can get good, qualified vendors." However, for a variety of reasons, DEC designs and builds most of its own switching supplies. For one thing, the specifications of a system often change during its development, so that the power-supply requirements can also change drastically and cannot be frozen from the conceptual stage. For another, the system may require a single supply with multiple outputs to serve, say, the central processing unit and memory. "The optimum route in such a case is to design one supply rather than buy a cluster of several," Tays says.

At Data General Corp., Southboro, Mass., staff engineer Dan Clemson says the company has been specifying mostly switching supplies because they offer the compactness and the variety of voltage levels needed. In addition, he says, battery backup is becoming an important consideration for minicomputers, and switching supplies are well suited to battery inputs.

In the Nova 3 minicomputer, introduced about a year ago, Data General wanted the power supply to fit on a single plug-in board (Fig. 4). A linear supply would have dissipated too much power, and the need for close regulation ruled out a ferroresonant supply.

A recent exception to the trend is the Eclipse line, the company's high-end minicomputer. The ferroresonant type is used because the system needs a simple supply of the highest reliability. Of all the systems that Data General has shipped to date, Clemson's guess is that half have switching supplies, with the rest split evenly between linear and ferroresonant.

The firm makes most of its own supplies, he says, because it can make the power supply an integral part of the system and save money. Then too, he adds, "we have a pretty strong buying arm and can buy parts in large quantities to save money. And, by being our own manufacturer, we have only ourselves to blame if we don't have the parts when we need them."

Hitachi Ltd. in Japan builds switching power supplies for its Control Computer 80. Engineer Yasuji Kamata of

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<th>COMPARISON OF CHARACTERISTICS OF REGULATED POWER SUPPLIES</th>
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How they do what they do

In the linear or series-pass supply (a), the ac line voltage is stepped down through a transformer. The low-voltage alternating current is rectified to give an unregulated direct-current voltage about 5 volts higher than the desired output. The extra voltage is dropped in a variable dissipating element (the series-pass element).

This dissipating element is shown in the sketch as a variable resistor, but it actually is one or more power transistors operating in the linear mode; hence the term linear power supply. A control circuit continuously senses the output voltage and adjusts the transistors to maintain the desired level.

The linear supply provides excellent regulation, is reliable, and does not generate electrical noise. But the line-voltage transformer makes it large and heavy, and the heat sink for the pass transistor adds to the bulk. Also, the dissipative mode of regulation makes the linear supply inefficient—about 60% of the input power is wasted in low-voltage supplies—and requires cooling provisions.

If the ac line-voltage goes too high, the output voltage is unaffected. The series transistor merely dissipates extra power. If the input ac line voltage drops below the design value even momentarily, the output voltage drops too, because the low-voltage capacitor that holds the unregulated dc cannot store enough energy to maintain the output against a momentary line-voltage dip.

The switching power supply (b) rectifies the ac line voltage directly, storing energy in a high-voltage capacitor. The high dc voltage is switched at high frequency (around 20 kilohertz) across a transformer and then is rectified to provide the regulated dc output voltage desired. The switches used in the dc-to-dc conversion are actually power transistors, operating in the highly efficient switching mode; hence the term switching power supply.

A control circuit senses the output voltage and provides regulation by adjusting the duty cycle of the transistors—the length of time that the switches are closed to charge the output capacitor. Because this charging is done in pulses and correction for a voltage change may require several pulses, the regulation response is not as fast as in the linear supply—resulting in reduced regulation.

The on-off switching in these supplies can generate electrical noise that may be radiated and conducted to other parts of the system unless special precautions are taken. The operation at high voltage and the greater number of components required make the switching supply more susceptible to failure than a linear supply. But it is lighter, smaller, and cooler, as well as less expensive to operate because of its greater efficiency. Also, the energy storage at high voltage provides some protection against brief drop-off of line power.

The ferroresonant supply (c) is the simplest and most reliable of the three types of regulated power supply. The input ac line voltage is applied to the primary of a transformer, exciting flux in the iron. A resonant secondary circuit causes part of the iron to saturate. A magnetic shunt provides a flux path for the secondary so that the primary flux remains unsaturated. The output secondary coil is across the saturated core, so the output voltage has a square waveform. Therefore the rectified output is fairly well regulated without the need for any external control circuitry. Inherently a current-limiting device, the supply automatically protects against overload.

The resonant circuit also provides protection against dips in line voltage, but is very sensitive to line frequency. So line-frequency stability must be assured.
Hitachi's Omika works says that the vendor-produced series regulators previously used were sometimes larger than the control processor itself. And the linear supply weighed about 150 pounds to about 25 lb for the switching supply. Although the linear's initial cost was somewhat less, the power wasted over a 10-year life by its 30% efficiency would make it more expensive than a 70%-efficient switching supply. Kamata says that series regulators are now being mostly relegated to applications requiring 1 watt or less, because efficiency at these feebler power levels is less important.

While acknowledging that design of switching regulators is difficult and expensive, Kamata says units produced in-house cost less. Also, a single power supply can be designed for perfect electrical and mechanical fit with the computer, something that an independent manufacturer of power supplies is reluctant to provide. Another advantage is that reliability can be assured, which is also difficult to obtain from an independent manufacturer.

Kamata says that the power supply is more difficult to build than the logic circuits in which the division specializes, despite 10 years experience and the assistance of the company's nearby research laboratory. He says switching regulators are becoming much easier to design because of the sensing and control ICs coming from American semiconductor manufacturers.

To protect the memory store of its minicomputers, Ferranti Ltd.'s Automation Systems division in Manchester, England, uses switching supplies. Frank Moss, design manager, says the supplies have to protect the core store against electrical failure and computer startup and shutdown, and the voltage has to track with temperature.

Ferranti makes the power supply that controls and sequences three voltage buses for the Argus minicomputer, as well as other special-purpose supplies. But the firm also buys standard supplies from several makers.

In Paris, CH-Honeywell-Bull technical director Henri Feissel notes a tendency among most European minicomputer makers toward specifying switching supplies. However, the higher cost of such units sometimes is a factor.

His firm paid particular attention to the power supply on the design of the series 60 computer line. One has a 10-kHz switching thyristor, while the other uses a single-phase transistor. The rule of thumb is to use transistors for the first hundred watts and switching thyristors for higher power, he reports.

Cost is key in telecommunications

Initial cost is always important with power supplies, but sometimes it's not the only factor, as the telecommunications industry illustrates. "The cost of the supply has risen to between 10% and 15% of the total cost of a typical system," says Joseph J. Suozzi, head of energy-system engineering at Bell Laboratories, Whippany, N.J. "Therefore, power-supply considerations are an integral part of our system planning."

The key is the overall cost of power from a supply. This includes not only the initial investment, but also the operating cost, cost of the cabinet and floor space, the cost of borrowing money, and so on. Thus operating efficiency and compactness are prime considerations for Bell. That's why it uses switching supplies in most equipment. At power levels up to 10 w, the effects of low efficiency are not as important, so linear regulations can be used.

Standardization of design is extremely important in controlling costs. Donald R. Anselmo, head of power-systems development at the Whippany laboratories, strives for commonality and modularity through the whole line of power supplies. The standard converters in Fig. 5 reflect this approach to the assembly technology of today's supplies.

Bell designs reliability into the supply by derating components and by redundancy. Control of the switching speed, low-inductance wiring, and filtering that dissi-
2. Control for switching supply. This Silicon General Corp. 1524 integrated circuit contains all the control circuitry needed for a push-pull switching-regulated power supply. Replacing 20 or more discrete components, it brings the parts count of the switching-regulated supply down to that of a series-regulated supply and almost reduces design of the switching supply to selection of appropriate power transistors and magnetic circuitry. Plessey Ltd. makes a similar IC, and Texas Instruments Inc. has a device for single-ended operation.

R. F. components pates the noise energy all prevent electromagnetic interference.

In England, Marconi Communication Systems Ltd., Chelmsford, designs the power supply as part of the electronic circuit, according to Ian Alexander, technical director. Generally the firm uses conventional linear approaches, except when switching techniques are necessary to reduce heat dissipation and gain greater efficiency. "But you have to watch the economics of things all the way through the design because high-voltage switching transistors are expensive," Alexander cautions.

Marconi has "certain inhibitions about buying outside, because after all we're in the business of manufacturing electronic equipment," he declares. "We buy only if there is a specific requirement or only if using a subcontractor's module."

Linear supplies have proved to be the answer to the special demands of test equipment. Its manufacturers prefer to buy outside whenever possible, but rigorous operating conditions sometimes force them to build their own.

GenRad's test-system division in Concord, Mass., produces equipment that performs both analog and digital testing. Analog work includes measurements down to the millivolt level and precision instruments in the system that don't tolerate noise well. Linear supplies are used to keep radiated and conducted noise to a minimum, says development engineer Karl Karash, because there is so much analog instrumentation in the systems. For example, the 2230 component test system (Fig. 6) has to test capacitance down to the picofarad level and voltages down to microvolts, "and you can't measure microvolts of signal if you have millivolts of noise," he says.

Another advantage of linear supplies is that they more readily accommodate the inevitable changes that take place during development. In a GenRad test system, changes may occur in the voltage levels to be measured, the current outputs, and the options available in the system. "It's much more difficult to design a switching supply to accommodate these kinds of changes than it is to use a linear supply," Karash maintains.

"It seemed like it took us a long time to get the 2230 out the door, but I'm convinced it would have been a lot longer in development if we had used switching supplies. The parts changes were minimal compared to what would have been required for a switching supply. If a voltage changes slightly, only a capacitor or transformer winding might have to be changed."

The company makes power supplies, but the policy is to buy wherever possible, Karash explains. He says the pattern has changed to buy for large systems in the last few years because of the availability of standard power supplies from vendors.

He likes the proliferation of vendors of the open-frame linear supply because it makes for price competition. "The price doesn't vary much more than 10% among possibly five different manufacturers."

The company has developed a set of rules for buying its linear supplies. They must be available of the

3. Minicomputer supply. Digital Equipment Corp. built this power supply to fit into the 3½-inch-high cabinet housing a complete minicomputer. Smaller coil on ferroresonant transformer is primary; larger windings are secondary and tank coils. The rectified output goes to a regulator board (not shown) to provide 5 V for TTL and for dc-to-dc conversion to +15 V for a teletypewriter or an EIA line driver.
What of the future?

An expanding electronics market means an expanding power-supply market, and Venture Development Corp., Wellesley, Mass., predicts the 1975 power-supply shipments of $2 billion will approach $3 billion by 1978. In-house manufacture will decline slightly from its present 80% share of the total.

Gnostic Concepts Inc., of Menlo Park, Calif., also foresees this strong growth in the power-supply market. It will be generated by the steady overall growth of the electronics industry, by the increasing use of electronics in place of electromechanical controls, and by the greater cost of power conversion compared to such functions as logic and memory.

Manufacturers of electronics equipment are showing an increasing tendency to use switching supplies, says Dr. John M. Salzer, a consultant and an executive associate of Darling, Paterson, and Salzer in Los Angeles. The penetration of switching supplies in the U.S. power-supply market will grow from 8% in 1975 to 19% by 1980. This increasing penetration corresponds to the worldwide trend and represents a very high growth rate.

The major reasons for this growth, according to Salzer, are the availability of better components, reduced overall cost, the increased cost of energy (stimulating energy-conservation programs), convenience of higher-voltage dc bus for battery backup and line-fluctuation protection, and the advent of smaller products (such as microcomputers) that make smaller power units desirable.

4. Tops for logic. Switching power supply for Data General Corp.'s Nova 3 computer is shown as top element in pile of chassis. Logic makers like switching supplies because they save space and run cool. They also can sustain the dc voltage during a dip in ac line voltage or while a battery backup comes on line during a power failure, thus preventing loss of data stored in volatile memories.
5. **Standard converters.** These dc-to-dc converters provide low-voltage power in Bell System telecommunications equipment. Their input power levels are (from left) 250, 150, 80, and 35 watts, respectively. To minimize cost, all units use the same structures, circuit boards, components, hardware, and cases. The transformer windings are chosen to provide a fixed output voltage (5.0 or 5.2 V or whatever is required) with a fixed input of 24, 48, or 140 V. Regulation is by pulse-width control. The conversion efficiency is 75 to 90 per cent.

that “people tended to think they could build their own cheaper than they could buy them. That turned out to be fallacy, so we’ve tended to buy lately.”

At the Environmental Engineering division of Interstate Electronics, Anaheim, Calif., reliability and prompt delivery remain the principal consideration of project designer John Kracik. “We want something that’s not going to give us trouble,” he says. Reliability is particularly important for the division’s adaptive data-reporting systems for marine oil-drilling rigs. Since variations in wind, waves, and current are critical for the rig’s operations, the reporting system must perform flawlessly around the clock.

The system uses two linear supplies, a 15-v unit for driving low-pass filters and a 28-v model for powering the sensors. Kracik has found a serviceable off-the-shelf supply from a dependable nearby manufacturer and is not inclined to change. “As long as the supplies work and we have zero problems, we’ll stick with them.”

Interstate’s own reliability testing requires a full-load 24-hour burn-in of each power supply, then random turning off and on about a half-dozen times as the system is kept running until delivery time. By this time, any problems would have shown up, he says.

Operating conditions created a make-or-else situation when the Eastman Whipstock division of Petrolane Inc., in Houston, Texas, needed a power supply for a down-hole oil-well surveying instrument. The supply had to fit inside a 11/2-inch diameter pipe, to provide 9 W at 28 V from inputs varying between 6 and 18 V, and to operate at any temperature from −20°C to 175°C. High reliability was a must, because down-time on an oil rig costs $3,000 per hour.

“We couldn’t buy such a supply anywhere,” says chief engineer Gordon Richardson, “so we built one ourselves. For compactness and low heat generation we used a switching-supply dc-to-dc converter, with the transistors operating at a small percentage of their rated dissipation. Our brassboard prototype for environmental testing survived the temperature tests and an impact of 2,000 g, and one hundred production units have been operating successfully in the field for over a year.”

Sometimes systems will have a mix of power-supply
7. Looking into the power supply. The supply for IBM's new 46/40 Document Printer uses a ferroresonant transformer to produce six different dc voltages, four of which are further regulated by series-pass transistors. Printed-circuit boards eliminate point-to-point wiring, thus simplifying assembly. The lower horizontal board holds rectifiers and filter capacitors. The higher horizontal board has connections to series regulators, fuses, and output connectors. The vertical circuit board holds the control circuitry for the regulators.

Types. A case in point is the Japanese wide-area traffic-control systems built by Matsushita Communication Industrial Co. It uses a switching supply in the traffic-control center, which is equipped with a minicomputer, and linear supplies for the local controllers of the traffic lights.

The firm changed from linear to switching for the central supply in 1975, says chief engineer Koji Kurimoto of the data-equipment and control-systems department. The linear supplies used just met system requirements, but they were large and ran at more than 60°, even with fan cooling. So there was a good chance of power-supply failure if the fan failed.

The smaller switching supplies are more efficient and run at about 40° when the fan is operating. They can be over-specified without undue cost in size or operating expense, so they are generally run derated to about 80% of capacity for improved reliability.

The linear supplies for the local traffic-light controllers are made by the firm, and parent company Matsushita Electric Industrial Co. makes the general-purpose switching supplies adopted for use in the traffic-control center. In-house production of the linear supplies is attractive because the quantities are in the thousands.

The buyers of office equipment almost never care what supplies power to their machines, so long as they work efficiently and reliably and don't take up a lot of space. "After the electrical requirements, cost and reliability determine which technology is used," says Jay Kinnard, power-systems project engineer at IBM's development laboratories in Austin, Texas. But these requirements can be translated into different specifications for different machines.

"For office machines, such as magnetic-card typewriters or copiers, we use ferroresonant supplies if possible," Kinnard says. "There high reliability is particularly important in products that are to be used by the general public, and their cost is low."

The newly announced 46/40 Document Printer, for example, derives its dc power from a ferroresonant supply (Fig. 7). A couple of voltages require better regulation than the ferroresonant type provides, so linear regulation is added to those lines. But the rest of the sources are just rectified transformer outputs.

Where size or weight limitations rule out ferroresonant units, IBM may go to the switching supply. The electronic Selectric Composer is a good example. Because it is housed in a typewriter-like cover, the size of the power supply and the heat dissipation had to be minimized. Therefore a small, efficient switching supply was used.

The Memory Typewriter is the same size as the Composer, but has less stringent power requirements. It uses a lower-cost linear supply that is more compact than a ferroresonant unit.

IBM is an in-house producer of power supplies, not so surprising considering the size of the company. "We build our own power supplies for reasons of cost, reliability of components, integration of the supply into the system design, and time saving," Kinnard says.
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Clip-on monitor unit displays count in IC

by John Okolowicz

An integrated decoder/display, mounted on a standard test clip that fits onto a dual in-line package provides a quick means for monitoring the state of an integrated-circuit counter. This monitor is brighter than the LED-chip monitors now on the market, and can be custom-made to suit any purpose.

In the setup, the pins that correspond to the outputs of the counter to be monitored are wired to the inputs of the on-chip driver. The enable line (pin 5) is then tied to ground so that the chip always displays the latest state of the counter.

For each different type of counter or latch to be monitored, a new assembly must be made. However, by mounting a socket on the DIP clip, instead of soldering a display directly to the clip, a variety of monitor clips can be made without requiring a large number of display ICs.

The figure shows a Hewlett-Packard 5082-7340 hexadecimal decoder/light-emitting-diode-display IC mounted on a DIP clip; H-P 5082-7300 or 5082-7302 numeric displays may also be used.

This concept works best when the monitored counter can be single-stepped so that successive intermediate states are displayed. However, as long as the states are displayed for a sufficient time to be observed, any clocking arrangement is adequate.

This display technique provides a quick visual check of counters or latches with important outputs that need to be constantly monitored. The concept may be extended to monitoring of bus addresses or data-bus lines by using more than one display wired to a DIP clip with 24 or more pins and wiring only the first three inputs of each display so that an octal output format is displayed. Alternatively, all four inputs of each display may be used for a hexadecimal display.

Power supply’s VC product sets interrupt capability

by C.A Watson Jr.
E-Systems Inc., Greenville, Texas

It’s easy to calculate how long a power supply can continue to provide its regulated output voltage during interruption of the primary power source. The voltages and the storage capacitance in the supply determine this. Interrupt capability (also called holdover, holdup, or carryover time) is important for preserving data in volatile memories during momentary power interruptions or while standby power goes on line during an outage.

The holdup time is estimated and tradeoffs in its optimization are evaluated by the expressions given here for the capacitive-storage supply shown in the figure. The capacitor may be only large enough to reduce ripple to within the range of the voltage regulator, or it may be larger.

The input voltage must be equal to or greater than the sum of the desired regulated load voltage plus the minimum drop in the regulator, and the input current to the regulator is the sum of the regulator current plus the
load current. For most efficient regulators and normal loads, the current drawn off by the regulator is small compared to the current to the load, so \( I_{in} \) is nearly equal to \( I_{load} \).

To estimate the interrupt capability of a typical 5-volt, 1-ampere supply in which the minimum dropout voltage of the regulator is also 5 v, let

- \( I_{in} = I_{load} = 1 \, \text{A} \)
- \( V_{load} = 5 \, \text{v} \)
- \( V_{reg} = \text{minimum drop across regulator} = 5 \, \text{v} \)
- \( V_{in} = 15 \, \text{v} \)
- \( C = 1,000 \, \text{microfarads} \)

If the primary power source fails, the capacitor discharges through the regulator into the load until its voltage drops to \( (V_{reg} + V_{load}) \). The load current remains constant, and therefore \( I_{in} \) remains constant, so the discharge is linear with time. The interrupt capability in seconds is therefore given by

\[
t = \frac{(V_{in} - V_{reg} - V_{load})C}{I_{in}}
\]

where the units are volts, amperes, and farads. For this example,

\[
t = \frac{(15 - 5 - 5) \times (1 \times 10^{-3})}{1} = 5 \times 10^{-3} = 5 \, \text{milliseconds}
\]

In other words, the storage capacitor is drained at a rate of 1 v/ms until the minimum voltage for the regulator-load combination is reached and normal voltage across the load can no longer be maintained.

The price paid in input power for this 5-ms capability is the product of the load current and the excess voltage, \( V_{ex} \), above the minimum required for the regulator and load. In this case, \( V_{ex} = 15 - 10 = 5 \, \text{v} \), so the extra power required for the 5-ms capability is \( 5 \, \text{v} \times 1 \, \text{A} \), or 5 watts.

The energy stored in the capacitor is

\[
E_C = \frac{1}{2} V_{in}^2 C = \frac{(15^2 \times 1 \times 10^{-3})}{2} = 0.1125 \, \text{joule}
\]

If it is necessary to have 50 ms of interrupt capability, rather than 5 ms, the \( V_{ex}C \) product must be multiplied by 10 (if the load current and voltage-regulator requirements are not changed). The two extreme solutions are to multiply \( V_{ex} \) by 10, so that \( V_{in} \) is 60 v, or to increase \( C \) to 10,000 \( \mu \text{F} \).

If \( V_{in} \) is raised to 60 v, the energy stored in the capacitor is

\[
E_C = \frac{(60^2 \times 1 \times 10^{-3})}{2} = 1.8 \, \text{j}
\]

The power price of the 50-ms capability is \( V_{ex}I_{in} = 50 \, \text{v} \times 1 \, \text{A} = 50 \, \text{w} \).

If the voltage is unchanged, but capacitance is multiplied by 10,

\[
E_C = \frac{(15^2 \times 1 \times 10^{-3})}{2} = 1.125 \, \text{j}
\]

and the power price = \( 5 \, \text{v} \times 1 \, \text{A} = 5 \, \text{w} \), which is the same as for 5 ms with 1,000 \( \mu \text{F} \).

Thus the tradeoffs are clearly defined: increased power cost and stress versus increased size and bulk. Of course, intermediate solutions in which both \( V_{in} \) and \( C \) increase are also possible.

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**Sustaining the output.** In the case of failure of the unregulated dc power to this power supply, the regulated output voltage and load current are maintained as long as the capacitor voltage is greater than the load voltage plus the minimum drop across the regulator. The capacitor discharges at a fixed rate, so the duration of interrupt capability depends linearly on capacitance and excess voltage.

**Calculator notes**

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**Program provides card storage of SR-52 data-memory contents**

by David T. Phillips
Glendan Co., Goleta, Calif.

Data tables and computation results can be stored on the magnetic cards normally used by the Texas Instruments SR-52 calculator for program storage. The stored data has the full 13-place accuracy used internally by the calculator, rather than the 10-place accuracy of the light-emitting-diode display.

The SR-52 card-programable calculator stores its program in the 28 registers R70–R97. Each register holds eight program steps. The contents of registers R70–R83 can be stored or read from side 1 of a program card, and the other 14 registers are read in or out from side 2 of the card.

The SR-52 stores computational data and results in registers R00–R19. There is no direct provision for card storage of the data that is in these 20 data-memory locations.
registers. However, the short program shown below exchanges the contents of registers R00–R19 with the contents of registers R70–R89, thus allowing storage of the data-register contents on a program card and also reloading of the data from the card.

The contents of up to 14 of these registers can be stored on half of the card, and the remaining six data registers share the second half of the card with the SWAP program. The program takes about 15 seconds to exchange the contents of two sets of 20 registers.

Under the program shown in the table, buttons are pushed in the following order: in the calculate mode, \texttt{19 STO 90 GTO 168 LRN}; then in the learn mode, \texttt{LBL A ( RCL 90 STO 98 + 70 STO 99 IND RCL 98 IND EXC 99 IND STO 98 1 +/- SUM 99 SUM 98 RCL 98 IFPOS 184 LBL B RCL 90 HLT LBL C STO 90 HLT)}.

The instruction \texttt{IFPOS 184}, which the coding form shows at program-storage locations 207–210, loops the program back to \texttt{IND R CL 98} at location 184. Program A exchanges the register contents, B shows the highest register exchanged, and C allows the user to alter the number of registers to be exchanged.

To store data, first enter or compute data in registers R00–R19. Next load the \texttt{SWAP} program from side 2 of the card. Then swap the register contents, by pressing key A. Finally, write the new card by pressing \texttt{INV 2nd READ} once for each side of the card. To see the number of the highest register to be swapped, press key B. To change the highest register, enter the number of the new highest register and then press key C.

To recall data, first read in both sides of the \texttt{SWAP} card, by pressing 2nd \texttt{READ}. Then move the data to registers R00–R19 by pressing key A. After that, proceed with computations, loading of program, and the like.

---

SR-52 Coding Form

**Program Title: SWAP**

### LOC CODE KEY COMMENTS

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<th>CODE</th>
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<th>COMMENTS</th>
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<td></td>
<td></td>
<td>Program storage locations</td>
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<td>159</td>
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<td>000–159 are registers</td>
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<td>220</td>
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<td>9</td>
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<tr>
<td>00</td>
<td>0</td>
<td>highest</td>
<td></td>
</tr>
<tr>
<td>81</td>
<td>HLT</td>
<td>in 90</td>
<td></td>
</tr>
</tbody>
</table>

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**LABELS**

- A: SWAP
- B: Last register
- C: Ent last register

**REGISTERS**

- 00 $\leftrightarrow$ 70
- 01 $\leftrightarrow$ 71
- 02 $\leftrightarrow$ 72
- 03
- 18 $\leftrightarrow$ 88
- 19 $\leftrightarrow$ 89
- 90 Highest reg. – normally 19
- 98 Index 00 $\leftrightarrow$ 19
- 99 Index 70 $\leftrightarrow$ 89

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Engineer’s note: a regular feature in Electronics. We invite readers to submit original design shortcuts, calculation aids, measurement and test techniques, and other ideas for saving engineering time or cost. We’ll pay $50 for each item published.
You've noticed the heavy coverage on this page devoted to the memory capability hidden in the SR-52 calculator. We'll cap it with a wrap-up of the situation by Bill Peterman, project engineer at AMF Tuboscope Inc., Houston, Texas. He notes that although memory locations 60 through 69 of the TI device are indeed available for memory usage as stated [Aug. 19, p. 114 and Sept. 15, p. 122], they are not particularly useful for data storage, since they're erased whenever you press the clear key.

On the other hand, says Peterman, they're perfect as pointer registers for the indirect mode, or as storage registers for intermediate calculated values that are repeatedly used in a program but not needed after the program is run. But, he cautions, "You must not let the parenthesis depth in a program overlap the algebraic registers used." That means as you use each memory register, starting at location 69 and working downward, you should reduce by one the algebraic stack depth available for nested parenthesis calculations.

Peterman also points out that it's not necessary to skip program steps 070 through 099 when you're using the program-memory registers as data registers unless the program steps themselves are serving as data registers in the program to be run. For example, if you want to use registers 90 through 97, all program steps from 000 to 160 should be available for program use.

In recent months, a whole rash of sophisticated linear integrated circuits have come on the market for building power supplies and voltage references. With these new devices, you can drastically reduce parts count, cut down design time, and save money to boot.

Large-scale integration in power linears is bringing about complete switching regulators on a chip—devices are available for either single-ended or push-pull operation. Three-terminal IC voltage regulators, which used to be strictly fixed-output devices, are now available in adjustable versions. Moreover, output voltage can be varied over a substantial range—on the order of 35 volts in some—and output current for these regulators has been boosted to several amperes.

Monolithic voltage references are also being improved; some devices are now offering an adjustment range of several percent from nominal. Even discrete-like zeners now available in IC form permit you to achieve temperature coefficients of less than 1 part per million per degree celsius in free-air environment.

It's ironic that engineers grounded in linear-circuit and analog-system design are a vanishing breed—an alarming situation caused by the glamor and high rewards associated with digital-product design. Today's best new engineers want to be digital designers, and for good reason: they get to work on the high-visibility microprocessor, memory, and consumer LSI projects that often develop into major product areas and new million-dollar markets. The situation is already so bad that, complains John R. Walker, senior vice president of operations at Datapoint Corp., San Antonio, Texas, "It's really getting tough finding competent power-supply and analog-circuit designers—men who understand and know how to deal with a superbeta or a gain-bandwidth product."

Laurence Altman
SOC, our new line of open-frame power supplies:

- Standard voltage and package sizes
- 115/208/230 Vac input standard
- Made in U.S.A. with quality components
- No overshoot with turn-on, turn-off or power failure
- Stocked for immediate delivery
- Conservatively designed and rated
- Low heat dissipation, high temperature stability
- One-year warranty, worldwide service organization
- UL recognized

Output Current (Ade) Common Specifications:

<table>
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<th>Model No.</th>
<th>Series</th>
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<tr>
<td>SOC 28-3.1</td>
<td>28V</td>
<td>67</td>
</tr>
</tbody>
</table>

• Free-air rating — no external heatsink. • ±5% adjustable.

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Nitron's new generation of NVMs: First the 64X4 NCM7040, and now the 256X4 NCM7050. Available off-the-shelf. So don't lose any more valuable time. Or data.

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Electronics / October 14, 1976
**New products**

**Multiplexer expands scope uses**

Special triggering, variable threshold control added to one-channel scope yield a four-channel display for troubleshooting logic systems

by John Gosch, Frankfurt bureau manager

**Combinatorial triggering and** a continuously variable threshold voltage are the prime features of a multiplexer designed to expand any general-purpose single-channel oscilloscope into a parallel four-channel display for digital signals.

Developed by the small German firm, Dolch Logic Instruments GmbH (DLI) in Heusenstamm near Frankfurt, the SM40 scope multiplexer will be marketed in the U.S. by Glass Gorham Co. of Skokie, Ill. Deliveries in the U.S. of the $395 unit will start before the end of this year, says Volker Dolch, president of DLI and designer of the instrument.

The new multiplexer has a frequency response from dc to 50 megahertz and allows the synchronous display of the four logic signals in either the alternate or chopped mode. And since several SM40s can be cascaded—by connecting the “sync out” terminal of one unit to the “ext trigger in” terminal of the next—an oscilloscope can be made to show not only four digital signals, but also multiples thereof, such as eight, 12, or even 16 signals.

The prime application of the SM40 is in troubleshooting logic systems, both in the laboratory and in the field, Dolch says. Simple to operate, the cigar-box-size, two-and-a-half pound instrument should “prove a powerful tool in evaluating information blocks and data words such as memory addresses and instruction sets,” he points out. Its multichannel display and analysis capability make the unit particularly useful in troubleshooting microprocessor systems.

In these and similar jobs, the multiplexer’s variable threshold-level control should come in handy. The threshold can be continuously set by a calibrated high-resolution 10-turn potentiometer incorporating a digital dial. Variable from -10 to +10 volts, the threshold has a range large enough to serve MOS, transistor-transistor, emitter-coupled, and other logic types that are operated within a power supply range from -20 to +20 V, assuming a 50% threshold point.

By varying the threshold during the measurement, it is possible to define absolute logic levels as well as evaluate pulse shape and rise time. Since the multiplexer has an input characteristic (1 megohm in parallel with 20 picofarads) equivalent to regular oscilloscopes, any 10:1 slope probe can be used to expand the voltage range by a factor of 10, the company points out.

The multiplexer’s other principal feature—combinatorial triggering—is also helpful, Dolch stresses, as it allows users to evaluate complex sequential digital signals that cannot be adequately analyzed with conventionally triggered scopes. Implementing combinatorial triggering in the SM40 is a trigger-generator consisting essentially of a 5-bit AND gate that, via four toggle switches, ties together the four data channels and an external trigger input. Each switch has two logic positions—1 and 0—and with the four switches a certain bit pattern can be selected ahead of time.

Only when the signals to be displayed correspond with the preselected bit pattern is a trigger pulse fed to the scope. This type of triggering is useful particularly in troubleshooting complex digital systems because only that portion of, say, a 4-bit data word is displayed on the scope that follows the preselected bit combination.

Glass Gorham Co., Skokie, Ill. 60076.

Dolch Logic Instruments GmbH, Heusenstamm, W. Germany
Light-emitting-diode displays are gaining in sophistication these days. And now, from the Optoelectronics division of Hewlett-Packard Components comes a compact four-character alphanumeric unit that contains a significant portion of the display system inside its 12-pin dual in-line package. Because the shift registers and the LED drivers are internal, the associated circuitry needed for the display is considerably simplified. For example, for a typical 32-character system, the parts count is reduced by a factor of 36, says HP.

Each of the four characters of the new HDSP-2000 display is formed by a 5-by-7-dot matrix capable of displaying the full ASCII font, including lower- and upper-case letters, punctuation marks, mathematical and other symbols, as well as numerals. Character height is 0.15 inch.

The serial-in/parallel-out 7-bit shift register associated with each character controls constant-current LED row drivers. Full characters are generated by external column strobing. The constant-current LED drivers, which are typically capable of sinking 13.5 milliamperes per diode, are externally programmable.

Because of the small size and high degree of integral circuitry of the HDSP-2000, HP expects the unit to open up many new markets. Anticipated applications include interactive point-of-sale equipment, portable business terminals, medical instrumentation, word-processing systems, and microprocessor-based instruments or control mechanisms.

The HDSP-2000, which operates from a nominal supply voltage of 5 V, is directly compatible with transistor-transistor logic and readily lends itself to microprocessor control. Maximum current consumption is 15 mA per row. With 15 dots lighted, power consumption is 150 milliwatts per character, increasing to 225 mW when 20 dots are lighted. However, the HDSP-2000 has an intensity-control pin, permitting its brightness to be varied via duty-cycle modulation to reduce power consumption.

The sandwich-like construction of the unit's package is made up of a ceramic substrate, a Kovar spacer, and a glass lid. The glass, which is thermally matched to the Kovar and the ceramic, is a red contrast filter for crisp display appearance. Packages can be stacked end-to-end to form a string of characters, without affecting the 175-mil center-to-center spacing between characters. Operating temperature range for the HDSP-2000 is -20°C to +70°C.

Price is $47 per four-character cluster for orders of 125 clusters. Small quantities are available from stock.

Inquiries Manager, Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, Calif. 94304 [341]

Power relays
sell for $1.99

Priced as low as $1.99 each in quantities of 1,000, a family of power relays, designated the HL-series, is noteworthy for its high reliability and small size. Processes that contribute to the unit's reliability include spot-welding all connections, arc barriers between contacts, and debris wells, along with simultaneous molding and heat-riveting. The single-pole, single-throw version can carry 15 amperes at 125 volts ac; the dpdt unit is rated 10 A at 250 V ac.

Arrow-M Corp., 250 Sheffield St., Mountainside, N.J. 07092 [347]

Ceramic-encased resistors
range down to 0.001 ohm

A commercial series of ceramic-encased wirewound resistors from RCD Corp. ranges from 30 kilohms in...
The keyboard switch with a heart of gold

...keeps your product WORKING year after year... after year. In your keyboard or ours, Cherry key switches just don't fail. The knife-edge contact area is so small (9 millionths of a square inch)... the contact pressure so great (about 5,000 psi)... the gold alloy so pure and film-free... that you are assured of positive contact every time. For 50 million operations and beyond. (Which is probably beyond the life expectancy of your product!)

Cherry "heart of gold" keyboard switches are available individually or with two-shot molded keycaps. Hopefully, you want keycaps. Because, we have keycaps... in more legends, sizes, type faces than you're likely to find anywhere else. Sculptured keycaps? We've got 'em. Gloss or matte finish? We've got both. Colors? Lighted? Specials? Sure! Some "off the shelf"... all at prices that make it obvious why the Cherry way is the economical way to put a heart of gold in any keyboard.

CHERRY
CHERRY ELECTRICAL PRODUCTS CORP., 3608 Sunset Avenue, Waukegan, IL 60085

For free test sample switch and catalog, just TWX 910-235-1572 or PHONE 312-689-7700

Cherry switches now available locally from distributors.
New products

value down to 0.001 ohm. And the PW-series devices, in sizes from 2 to 50 watts, include noninductive designs for high-frequency applications and printed-circuit styles with either radial wire leads or lugs. A high-power mounting plate is available for increased dissipation. Delivery is from stock, and special orders require a lead time of four weeks. Price in 1,000-piece quantities ranges from 7 cents each to 19 cents each.

RCD Corp., 8 Blueberry Lane, Bedford, N.H. 03102. Call Ken Puleo at (603) 669-0054 [345]

Printed-circuit-board relay multiplexes to common line

From three to seven isolated circuits can be connected to a single common line with a series of printed-circuit-board relays that come in nonlatching or magnetic-latching versions with up to four blades. Coils are for direct-current operation only with nominal voltages of 5, 6, 12, and 24 volts dc. Power consumption is 0.5 watt for the nonlatching version and 1.0 w per coil for the latching.

Snap-action switch plugs into pc board

Cherry Electrical has added to its line of snap-action switches an open switch for insertion on printed-circuit boards. A front-mounting peg on the new S38-20H switch is flexible so that the switch is simply plugged in; no extra mounting hardware is needed. The switch is also fairly small: 0.658 inch high by 0.768 in. long by 0.228 in. wide. It extends about 11/2-in. above the pc-board surface and is rated at 2 amperes, 250 volts, ac. List price is $1.05, with a net of 48 cents in 2,000-piece quantities and 36 cents for 10,000 pieces. Prototype samples are available from stock, and production-lead time is 10 to 12 weeks.

Contact rating is up to 1 amperes resistive at 24 v dc or 24 v ac maximum. Size is 0.9 inch by 0.95 in. by 1.032 in. high. Prices start at $4.26, with quantity discounts available. Delivery of production quantities takes four to six weeks.

Printact Relay Division, Executone Inc., 29-10 Thomson Ave., Long Island City, N.Y. 11101. Call R.D. Burn at (212) EX-2-4800 [346]

Four-terminal resistors are current sensors

Four-terminal current-sensing resistors come in resistances from 0.01 to 1.0 ohm with tolerances including 1%, 3%, 5%, and 10%. And the series CS 4LPW units have ratings of 3, 5, 7, 10, and 15 watts. The four-terminal configuration offers a couple of advantages over a two-
For your pulse and digital timing measurements

The instrument shown includes a 7904 mainframe configured for pulse and digital measurements. Our Δ time plug-ins, the 7B80 and 7B85 time bases, give you differential time measurements with sweep rates to 1 ns/div. The 7A19 provides one vertical channel with a rise time of 0.8 ns and the 7A26 provides dual channels with rise times of 1.8 ns.

It’s easy to use. Your pulse train is displayed on a main sweep with two intensified zones that easily identify the time interval of interest.

And our package digitally displays your time interval on the crt in the correct units of measure. A Δ symbol precedes the time interval readout to indicate it is a differential time measurement. To make sure your answer is precise, you expand and adjust each intensified zone on separate delayed sweeps so each is positioned exactly where you want it on the pulse train.

Turn a switch and you are in the delay time mode; this mode lets you make propagation delay measurements using the beginning of the main sweep as a reference.

It gives you confidence in your answer. The Δ time you see displayed digitally on the crt is accurate to within 1%, even when you’re measuring fast pulses.

And there is almost no chance or operator error. You view all three sweeps at the same time; that is, you observe the two intensified zones on the main sweeps while you expand those portions on two separate delayed sweeps.

It’s convenient. Your pulse and digital timing measurements are easy to document photographically. The Δ time is displayed along with the main and delayed sweep speeds and amplifier sensitivities—all in the appropriate units of measure. This gives you the information you need in one complete crt display.

It’s a flexible package. This 7904 configuration gives you Δ time capability—an easy and accurate way to make pulse and digital measurements. Because it’s a plug-in scope, you can include other digital measurement capabilities—logic analysis, and digital and differential amplitude capability, to name a few.

To order the plug-in scope described here, call your local Tektronix Field Office.* For immediate assistance, call your Tektronix Field Engineer or circle 219 on your reader service card and we’ll have him call you. For a copy of our new application note on pulse and digital timing measurements, circle 218 or write us at Tektronix, Inc., P.O. Box 500, Beaverton, OR 97077. In Europe, write Tektronix Limited, P.O. Box 36, St. Peter Port, Guernsey, Channel Islands.

The 7000-Series... more than an oscilloscope

*Ordering information:
7904 Oscilloscope ............... $4300
7A26 Amplifier .................. $1200
Recommended probe: P6063A, 1X, 10X
7A19 Amplifier .................. $ 950
Recommended Probe: P6056, 10X ... $ 85
7B80 Time Base ................. $ 745
7B85 Delaying Time Base ........ $ 895

U. S. Sales Prices FOB Beaverton, Oregon

Pick this plug-in scope
Dialight LEDs
The widest choice for your every application.


Mix 'em or match 'em. LED logic state fault indicators are available in red, yellow and green, in a variety of shapes, some with a built-in integral resistor. Can be driven from DTL and TTL logic. Designed for easy alignment on PC boards so that multiple functions can be displayed.

Dialight, the company with the widest choice in switches, LEDs, indicator lights and readouts, looks for needs... your needs... and then they develop solutions for your every application. No other company offers you one-stop shopping in all these product areas. And no other company has more experience in the visual display field. Dialight helps you do more with these products than any other company in the business, because we are specialists that have done more with them. Talk to the specialists at Dialight first. You won't have to talk to anyone else. Send for your free new copy of Dialight's current catalog.

Terminal design. Voltage developed across the sense leads, for example, is independent of the temperature coefficient of the leads so that the temperature coefficient of the developed sense voltage can be held to less than ±30 ppm. In addition, lead length does not affect sense-voltage reading in a high-impedance circuit. Leads are oxygen-free copper or copperweld, depending on resistance, and they're electro-solder plated and are at least an inch long. Dimensions range from 0.88 in. long by 0.31 in. square for the 3-w size, to 1.88-in. long by 0.50-in. square for the 15-w size. The resistors are also available with standoff feet. Insulation resistance is 1,000 megohms dry; dielectric strength is specified at 500 volts ac.

TRW/IRC Resistors, P.O. Box 393, Boone, N.C. 28607. Call Art Brown at (704) 264-8861 [343]

TOPICS
Components
Dialight, Brooklyn, N.Y., announces that its 249 series of subminiature indicator lamps is now covered by listing with Underwriters’ Laboratories. ... General Electric’s Data Communication Products Dept., Waynesboro, Va., has announced qualification of its 3SBC (150-grid) series relay to MIL-R-39016/36 and 37 with established reliability failure-rate levels L and M. ... Cherry Electrical Products Corp., Waukegan, Ill., is now providing its popular subminiature thumb-wheel switch with 0.5-inch J-shape pin terminations. The new switch is designated the TSOJ-01M. ... Corning Glass Works, Corning, N.Y., has expanded its line of Spinseal axial-lead ceramic capacitors to include ultra-stable (COG) units.
HP's new thermal printer just keeps purring along.

Say goodbye to the clank and rattle of mechanical printers and say hello to quiet, reliable operation. With its thermal printing technique, the new HP 5150A Thermal Printer needs only two moving parts — those that transport the tape.

That's just a beginning. Built-in flexibility and plug-in options mean that, at last, there is a printer that you can custom-tailor to your data printout needs without delay or compromise.

**Alphanumeric printout.** A full 64-character upper case ASCII printing set, with figures and symbols that read and reproduce clearly. Print speed is three lines per second.

**ASCII Interface option.** Interfaces to most ASCII coded sources or HP Interface Bus. Full 64-character, 20-column printout.

**BCD Interface option.** Interfaces with BCD ± 8421 sources. Prints standard 16-character set, but can draw on the full 64-character complement of the 5150A. Ten or 20 columns.

**Scanner option.** The 5150A can function as a system controller for up to 13 instruments on the HP Interface Bus, providing automatic data-acquisition capability.

**Clock option.** With it, an entirely new order of convenience, flexibility and control becomes possible. Simple, front-panel controls let you record the time of day and select the time interval between samples.

**Special BCD printouts.** Special formats or re-interpretation of BCD inputs can be provided at minimum cost.

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*Domestic USA prices only.
Our $9.95 CPU is actually less than half the price of the 8080 or 6800 CPU.

And it's just the beginning of your saving. On-chip RAM, ROM and timers make an even bigger difference. Difference in cost. Difference in reliability and difference in manufacturing time. The following is the whole price/performance story of our F8 system, from minimum configurations to expanded systems.

**Lowest cost configuration**

Our minimum configuration F8 is perfect for controlling home appliances, braking systems, vending machines, ignition systems and other uses with modest memory and I/O requirements.

Two chips do it all -- a $9.95 PSU (Program Storage Unit) and F8 CPU (Central Processing Unit) for $9.95. The CPU is an 8-bit device, with a cycle time of 2 microseconds. It's the heart of all F8 microprocessors. It includes 70 instructions, 64-byte RAM (Random Access Memory), instruction register, accumulator, 16 individually controllable I/O lines, power-on reset, on-chip clock and control lines to other devices.

The PSU features a 1K byte ROM (Read Only Memory), program counter, 16 individually controllable I/O lines, 8-bit data port, stack register, incrementer/adder, and programmable timer and interrupt.

We're the only manufacturer in the world to offer this 2-chip performance. The 8080 requires 7 chips (9 chips with timer) to do the same thing.

**Double the program storage**

If you need more program space, just substitute PSU's. Our new 2K byte PSU offers twice the ROM for only $14.95.

**Built-in interface to external memory**

But suppose you need a couple of RAM's added to your CPU and 2K/PSU. Again, substitute PSU's for one with a built in memory interface. Avoid paying for extra chips. Order our new 2K/PSU-M for $14.95. This is super microprocessor power on 4 chips.

The 8080 takes 10 chips (12 with timer) to do the same thing.

---

**Fairchild's $9.95 F8 Microprocessor**

Half the Cost

Twice the Versatility

Electronics / October 14, 1976
A system that needs no ROM

If you don't want to commit to ROM, consider interfacing RAM and/or PROM (Programmable Read Only Memory) directly into the CPU. This approach is ideal when your production run is under 1,000 units, and for development prior to long production runs.

This F8 configuration uses only four chips: a CPU, a $7.45 SM (Static Memory Interface with interrupt and programmable timer) and two 2K PROMs. Competitors take twice as many chips to do the same thing.

Where more I/O is needed

By adding our $6.45 PIO (Parallel I/O) to this aforementioned configuration, you gain 16 I/O ports, another level of interrupt and a programmable timer. Now you have the perfect 5-chip microprocessor system for electronic scales, paper tape and cassette tape handlers, electronic games, traffic light control, cash register and similar applications.

Competitors take 10 chips to do the same thing.

Memory intensive systems

In memory intensive applications like message concentrators, floppy disc controllers, and store-and-forward message switching you'll enjoy big benefits with the F8's DMI (Dynamic Memory Interface). This $7.45 device fits between your CPU and dynamic (or static) memory WITHOUT A MEMORY REFRESH CHIP. Memory refresh logic is built into the DMI, and operates in sync with the CPU. Your CPU never stops. There's no cycle stealing. No performance degradation. We are the only manufacturer in the world to offer this advantage.

A supercharger for this memory intensive configuration is OMA (Direct Memory Access). This $5.95 option comes in one chip. It creates a direct link between your memory and external data. All functional and internal system timing is built in. The DMA can run at 500K bytes/sec and never slows down the rest of the system.

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1725 Technology Dr. San Jose, CA 95119 (408) 998-0123

*All prices quoted are for 100 to 999 plastic packaged parts, effective Sep. 1, 1976.
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New products

Instruments

True-rms meter sells for $345

4½-digit portable unit measures ac and dc voltage and current plus resistance

Deriving the true root-mean-square measurement of a waveform with bench or portable instruments has been an expensive proposition. But engineers at Data Precision Corp. have changed that. Their $345 model 248, a portable digital multimeter, gives a direct reading of true rms in ac volts with a one-year accuracy at 50 hertz within 0.4% of input +0.2% of full scale.

The unit also provides a true-rms readout of ac current and can measure dc voltage and current, plus resistance, in five ranges each. The 4½-digit meter features a 10-microvolt sensitivity in dc and ac, and a basic accuracy within 0.05% for a year. The battery-operated instrument sells for some $50 more than Data Precision’s average-responding model 245 introduced about four years ago, but it offers a lower scale—100 millivolts full scale for all measurements—in addition to those provided by its predecessor.

Harold Goldberg, Data Precision president, points out that the model 248 uses a light-emitting diode rather than a gas-discharge display like the 245, making for low-voltage operation. He looks for the new instrument to find widespread use where portability is important, and already has an order from the Bell System, whose telephone installers will use it for test purposes.

The rms-to-dc conversion is done principally in a linear bipolar IC developed by Analog Devices Inc. [Electronics, Sept. 16, p. 35]. The model 248 measures dc volts from ±10 microvolts to ±1,000 V and dc current from ±10 nanoamperes to ±2 A. Ac voltages from 10 microvolts to 500 V are measured with true-rms response from 30 hertz to

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Electronics / October 14, 1976
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When connected to an ac line, the battery pack is always recharging, whether or not the instrument is on. A blinking decimal point indicates that five minutes of in-spec operation remain before recharging is necessary. Delivery is from stock.

Data Precision Corp., Audubon Road, Wakefield, Mass. 01880. Phone Robert Scheinfein at (617) 246-1600 [351]

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Direct readings of average power for cw or repetitively pulsed lasers with wavelengths from 200 nanometers to 1,100 nm are provided by the model 460 laser power meter. Extra-cost accessories are available for extending the 460's capability to include peak-power measurements, integrated energy measurements, and pulse-shape determinations. The meter has full-scale ranges, for average-power measurements, from...
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Electronics / October 14, 1976
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Integral Electronics Corp., P.O. Box 286, Commack, N.Y. 11725. Phone Marcy Talbot at (516) 269-9207 [354]

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New products

Seminconductor

Detector chip
"sees" smoke

I/P device works with an GaAs infrared emitter to detect light obscuration

Battery-operated industrial and home smoke detectors usually touch off their alarm in one of three ways: a resistance change triggered by smoke striking a gas sensor, a current change initiated when smoke slows the travel of an alpha particle in an ionization chamber, or optical techniques that detect the obscuration or scattering of light caused by the presence of smoke. Designers at Micro Components Corp. maintain that optical techniques are less expensive than gas sensors and more sensitive to smoke than ionization chambers. So they have come up with a smoke-detecting chip that uses integrated-injection logic with an infrared light-emitting diode emitter.

The device, designated MCC 158, has all of its smoke-detection and trouble-indicating logic implemented in it. The trouble-indicating logic will trigger the smoke detector's horn intermittently when the circuit detects that the IR photodiode or input amplifier isn't working, or when the battery power is getting low.

Underwriters' Laboratories specifies that battery-operated smoke detectors must work for a full year, be able to trigger the horn when there's a 2% obscuration of light by smoke, be able to continuously sound the horn for four hours, and must assure a seven-day warning of battery power dissipation. George Fowler, an MCC design engineer, says the MCC 158 meets all of the UL specifications.

The monolithic circuit, in addition to the smoke and trouble-indicating logic, also includes an on-chip direct-current output driver to trigger the horn, and an on-chip pulsing driver for the LED. That LED plus the silicon photodiode detector will be external devices in the overall smoke-detection circuit.

In operation, the MCC 158 drives the LED in a pulsed mode so that the smoke logic looks for smoke during a 50-microsecond window every 5 seconds. Any signal outside that window will be ignored, minimizing the possibility of false alarms. This is achieved by the synchronous nature of the detection circuitry: if the smoke logic receives pulses from the receiver (photodiode) detector and pulser at the same time, the smoke logic begins a timing cycle. If the cycle is longer than the pulser repetition rate, the MCC 158 will not trigger an alarm unless successive signals from the smoke channel indicate the presence of smoke. Further, Fowler says, the addition of an external capacitor to the smoke logic can set the device to trigger on any desired smoke-detecting pulse, further cutting down the chance of a false alarm.

The MCC 158 is in a 16-pin plastic dual in-line package and is designed for 6-volt operation, using four AA or C-type cells. Its price is $1.58 in quantities of 10,000. Deliveries will begin in December.

Micro Components Corp. 99 Bald Hill Road, Cranston, R.I. 02920. Phone (401) 463-6000 [411]

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  Examples of prices are $11.75 for the 600-v IR5063 and $18 for the 1,000-v IR5066. Both prices are for quantities of 100 to 999.

Sales Manager, Semiconductor Division, International Rectifier, 233 Kansas St., El Segundo, Calif. 90245. Phone (213) 322-3331 [414]

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A monolithic 8-bit digital-to-analog converter that comes with an internal reference has a maximum non-linearity of half a least-significant bit (0.2%). The DAC90 also has a settling time within 0.2% of only 200 nanoseconds. Gain drift is typically ± 50 ppm/°C. The converter, which is offered in both military and applications that demand a variable output or higher current.

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Voltage detectors can be set between 2 and 30 volts

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- Fast 8-bit d-a converter is linear to within 0.2%

A monolithic 8-bit digital-to-analog converter that comes with an internal reference has a maximum non-linearity of half a least-significant bit (0.2%). The DAC90 also has a settling time within 0.2% of only 200 nanoseconds. Gain drift is typically ± 50 ppm/°C. The converter, which is offered in both military and applications that demand a variable output or higher current.

- Both voltage detectors pull a virtually constant current of approximately 20 microamperes in their sensing modes. This is low enough for them to be useful in battery-operated equipment, particularly as low-battery indicators. When packaged in an eight-pin minidip, the devices sell for $1.50 each in hundreds. They are also offered in TO-99 packages.

Intersil Inc., 10900 North Tantau Ave., Cupertino, Calif. 95014. Phone (408) 996-5000 [413]
Why Parylene works where other microelectronic protection fails:

**Controlled conformality**

There's a uniform coating of parylene all the way around the half-mil tip of this phonograph needle. That's true conformality, and only parylene gives it, in precisely controlled thicknesses from .002 to 3 mils, in one step. Unlike spray or dip coatings, parylene won't bridge or puddle, or thin out at sharp edges, creating potential failure points. The parylene coating is completely uniform, no matter how dense or intricate the module. And because it's applied at room temperature, there's no component discomfort.

**Crevice penetration in hybrids**

This beam lead has a 0.3 mil parylene coating all the way to the weld. Parylene penetrates deep within small crevices, maintaining clearance while putting a coherent coating under beam leaded chips and air bridges. No area is left unprotected, preventing shorts and allowing the designer great latitude in component spacing and sizing. And parylene secures loose debris while preventing breakoff of pigtails during shock and vibration loadings.

**Lead Strengthening**

It took up to 75 grams pull to break these 1 mil wires. Bare 1 mil aluminum wires, for instance, exhibit bond strengths of 3-5.5 grams; coated with 1 mil of parylene, pull strength increases by 60-70 grams. So wire and bond are stronger, and sideward shorts and loop collapse during extreme g-loads are prevented. Parylene coatings will penetrate the less than 1 mil clearance between beam lead bonded chips and the substrate, giving such strong coating coverage that the chip cannot be lifted without destroying it.

**Δ200°C thermal shock protection**

This hybrid microelectronics relay has undergone 200 45-minute cycles from -120 to 80°C, simulating earth-orbiting conditions. This X-ray shows all leads remain intact. Parylene protection was at work, on the transformer core and then the whole assembly before packaging (TO-116). There was no appearance of corona up to 5000 Vdc; leakage was reduced from 10µA to <0.01µA at 1000V. RTV encapsulation suffered dimensional mismatch, straining and snapping leads, with 500 V/mil bulk breakdown.

**Broad cost effectiveness**

These are some of the circuit modules now being protected with a conformal coating of parylene. Because nothing else offers parylene's combined protection against thermal cycling, shock, vibration, humidity, solvents, radiation, ionic contamination. Better barrier protection than liquid coatings like silicones, epoxies, and urethanes. On hybrids you can combine parylene with a hermetic seal for optimum environmental protection... and parylene alone will often do the job, and at less cost than hermetic seals. Parylene is compatible with active devices, and meets the tough requirements of MIL-I-46058C. For long term reliability, parylene provides a cost-effective solution.

Union Carbide invented the parylene system. Various patents apply; commercial use of the patented technology is licensed. Write for our 16-page brochure: Union Carbide Corp., 270 Park Avenue, Dept. RFB-65, New York, N.Y. 10017. For instant communication, and information about a trial run at reasonable cost, call Bill Loeb at (212) 551-6071.


In Europe: Mr. Peter Crook, Bakelite Xylonite Limited, Redfern Road, Tysley, Birmingham, England.
In Japan: Mr. N. Fusada, Tomoe Engineering Co. Ltd., Shin Shin Kaibldg., 14-1 Nihonbashl 3-Chome, Chuo-Ku, Tokyo.

Circle 147 on reader service card
VACTEC Photodetectors

The Industry’s Broadest Line Provides More Semiconductor Detectors for More Design Applications

Vactec serves manufacturers of a wide range of modern electronic products. Pictured are a few examples. All these devices are both made and sold by Vactec, including complete lines of LDR’s (photoconductive cells, CdS and CdSe); silicon solar cells, as well as silicon high speed and blue enhanced cells; NPN phototransistors and darlington; opto-couplers (LED/LDR, lamp/LDR and neon/LDR); selenium photovoltaic cells; silicon photodiodes, blue enhanced and PIN; and custom C-MOS and bi-polar IC’s. Write for technical bulletins on the types that suit your requirements. Or send your application, and Vactec will recommend the right cell for the job.

Vactec, Inc.
2423 Northline Industrial Blvd.
Maryland Heights, Mo. 63043
(314) 872-8300

Vactec serves manufacturers of a wide range of modern electronic products. Pictured are a few examples. All these devices are both made and sold by Vactec, including complete lines of LDR’s (photoconductive cells, CdS and CdSe); silicon solar cells, as well as silicon high speed and blue enhanced cells; NPN phototransistors and darlington; opto-couplers (LED/LDR, lamp/LDR and neon/LDR); selenium photovoltaic cells; silicon photodiodes, blue enhanced and PIN; and custom C-MOS and bi-polar IC’s. Write for technical bulletins on the types that suit your requirements. Or send your application, and Vactec will recommend the right cell for the job.

Vactec, Inc.
2423 Northline Industrial Blvd.
Maryland Heights, Mo. 63043
(314) 872-8300
New products

Quad op amp spans 8 MHz, pulls only 35 mW/amplifier

The high-performance HA-4602/4605 quad operational amplifier is a dielectrically isolated device that combines bipolar and complementary-MOS circuitry. Typical specifications include a small-signal bandwidth of 8 megahertz, an input offset voltage of 0.3 millivolt, and a power consumption of 35 milliwatts per amplifier. Other important characteristics are a typical slew rate of 4 volts per microsecond and a typical settling time (within 0.01% of final value) of 4.2 µs. The 4602, which is rated for operation from -55°C to 125°C, sells for $9.90 in hundreds, while the 4605, which operates from 0°C to 75°C, is priced at $4.95 in similar quantities.

Harris Semiconductor, P.O. Box 883, Melbourne, Fla. 32901. Phone Joe Santen at (602) 294-1431 [418]

Plastic-packaged SCRs switch 25 A at up to 800 V

A series of plastic-packaged thyristors with designations from 2N6504 through 2N6509 are SCRs with current ratings of 25 amperes and peak reverse blocking voltages ranging from 50 volts to 800 volts. All the devices have a peak nonrepetitive surge-current rating of 300 A.

Housed in TO-220 packages, the devices all have glass-passivated junctions and a center-gate-fire design for maximum parameter uniformity and stability. Designed for half-wave ac applications, such as motor and heating controls, the SCRs are claimed to sell for as little as two-thirds of the price of metal-cased equivalents. Two representative prices, for quantities of 100 to 999, are $1.43 for the 50-volt 2N6504 and $4.95 for the 800-volt 2N6509.

Technical Information Center, Motorola Semiconductor Products Inc., P.O. Box 20294, Phoenix, Ariz. 85036. Phone Thyristor Marketing at (602) 244-4426 [419]

Fairchild adds circuits to Isoplanar C-MOS family

Fairchild has added two products to its Isoplanar family of C-MOS circuits. The 4528 is a dual retriggerable, resettable one-shot whose propagation delay is independent of the value of the timing capacitor. The 4511 is a BCD-to-seven-segment latch/decoder/driver. Pricing, in hundreds, varies from $1.28 to $2.22 for the 4528, and from $1.64 to $3.12 for the 4511.

Fairchild Camera and Instrument Corp., Digital Products Div., 464 Ellis St., Mountain View, Calif. 94042. Phone Bill Callahan at (415) 962-3816 [420]
A great place to start expanding your company is right here.

New products

Packaging & production

Trimmer saves time and money

Automatic laser system uses 8080 microprocessor with extensive software

High-throughput laser trimming at low cost is the principal advantage of Quantrad Corp.'s model 1080 automatic trim system. Priced at about $80,000, the system is designed around an 8080-based microcomputer and comes complete with a general-purpose Basic compiler permitting extensive computation. Software is provided in duplicate floppy-disk form. Some of the standard programs include a highly flexible data entry allowing joystick or keyboard positioning, editing for user-development of specific applications, display and alarm functions, and accumulation of deviation data for quality-control purposes.

The 1080 design features, in addition to complete program control, the latest technology in trim hardware. The pulsed neodymium yttrium-aluminum garnet laser is employed in what Quantrad calls a synchronous-trimming system: when the beam is accelerating or decelerating, the pulse-rate increases or decreases accordingly to assure a clean, uniform cut. The beam positioner is controlled by a closed-loop galvanometer system that eliminates the hysteresis inherent in most positioner designs, making it possible to approach a location from two different directions and attain a reproducibility of position of 0.0005 inch at slewing speeds up to 50 inches per second.

A TV viewing system that provides full coverage of the 2-by-2-in. trim area is standard equipment with the 1080. The television monitor also serves as the display screen for the output of programs, showing system status, alarm functions, and all statistical data.

Minimal set-up time is afforded by the ease and flexibility of programming. The trimming instructions may be entered via the keyboard in the form of coordinates taken from a drawing, or by what Quantrad refers to as adaptive programming—positioning the non-destructive blinking cursor displayed on the monitor using the joystick control. The probe network, which is capable of measuring more than 32 resistors per circuit with 64 Kelvin probes, may be made to order for large production runs or set up by the user for small runs with a probe-card-making system or a magnetically-indexed system. In performing resistor measurements, probe selection is matrixed and connections are directed by an eight-bit word which contains the probe-number and destination information. Resistor measurement is interfaced to the microcomputer by precise digital-to-analog conversion, and the reference and measured voltages enter a comparator which controls the laser for trimming to tolerances as low as 0.05%. Because many manufacturers...
Reliability test results:

TRW’s X675HV series is designed to meet the requirements of voltage multipliers and high voltage filters in high density, high voltage power supplies, instrumentation, data displays, pulse modulators and copiers.

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The X675HV series can replace traditional dielectrics in many applications with substantial savings in size at comparable lower costs. On quantity orders, modifications can be made to your specifications.

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Electronics / October 14, 1976
YOU WERE HIRED FOR YOUR BRAINS, NOT YOUR BODY.

It’s not as silly as it sounds. Because many bright engineers and technicians are still spending their time soldering, desoldering and resoldering. Instead of designing.

Which is pretty silly considering the waste of talent. Especially when there’s a better alternative.

With CSC Proto-Board® solderless breadboards, assembling a circuit is practically as fast as designing one. No special jumpers or patch cords required—all types of components—from complex microprocessors to resistors, capacitors and LEDs—connect and interconnect as simply as pushing in a lead ... or short lengths of #22-30 solid hookup wire. And circuit changes are done with the same plug-out, plug-in ease. All thanks to rugged, nickel-silver contacts and CSC’s superior use-tested design.

Proto-Board breadboards are available in a variety of sizes, from 630 to 3060 solderless tie-points (six to thirty-two 14-pin DIP capacity), at prices from $15.95* (kit) to $79.95. And if you’d like built-in regulated supplies, they’re available too. in models priced at $75 and $120.

Before you start your next project, put down your soldering iron and call 203-624-3103 (East Coast) or 415-421-8872 (West Coast) for full specifications and ordering information. Once you do, you’ll find yourself soldering less ... and more than likely, earning more.

<table>
<thead>
<tr>
<th>MODEL</th>
<th>NO. OF TIE-POINTS</th>
<th>14-PIN DIP CAPACITY</th>
<th>SUGGEST LIST</th>
<th>OTHER FEATURES</th>
</tr>
</thead>
<tbody>
<tr>
<td>PB-6</td>
<td>630</td>
<td>6</td>
<td>$15.95</td>
<td>Kit — 10 minute assembly</td>
</tr>
<tr>
<td>PB-100</td>
<td>760</td>
<td>10</td>
<td>$19.95</td>
<td>Kit — with larger capacity</td>
</tr>
<tr>
<td>PB-101</td>
<td>940</td>
<td>10</td>
<td>$29.95</td>
<td>8 distribution buses, higher capacity</td>
</tr>
<tr>
<td>PB-102</td>
<td>1240</td>
<td>12</td>
<td>$39.95</td>
<td>Large capacity, moderate price</td>
</tr>
<tr>
<td>PB-103</td>
<td>2250</td>
<td>24</td>
<td>$59.95</td>
<td>Even larger capacity only, 2.74 per tie-point</td>
</tr>
<tr>
<td>PB-104</td>
<td>3060</td>
<td>32</td>
<td>$79.95</td>
<td>Largest capacity, lowest price per tie-point</td>
</tr>
<tr>
<td>PB-203</td>
<td>2250</td>
<td>24</td>
<td>$75.00</td>
<td>Built-in 1% regulated 5V, 1A low-ripple power supply</td>
</tr>
<tr>
<td>PB-203A</td>
<td>2250</td>
<td>24</td>
<td>$120.00</td>
<td>As above plus separate + - 15V and + - 15V internally adjustable regulated outputs</td>
</tr>
</tbody>
</table>

*Manufacturer’s suggested list

Prices and specifications subject to change without notice.

continental specialties corporation
44 kendall street
box 1942, new haven, connecticut 06509
203-624-3103 twx 710-465-1227
west coast office box 7809, san francisco ca
94119 • 415-421-8872 twx 910-372-7992

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circle 152 on reader service card
New products

have developed their own chip-handling equipment, Quantrad offers interfacing systems and hardware for low-cost updating of a company's laser-trimming system to automatic control.

Quantrad Corp., 139 Illinois St., El Segundo, Calif. 90245. Phone Wayne Stevenson at (213) 322-1452 [391]

Ultra-violet lamps speed EPROM erasing

Offering powerful short-wave capability, two new ultra-violet lamps reduce erasing time of erasable programable read-only memories to minutes. The model S-52T erases up to 16 chips in about seven minutes, while the UVS-54T handles up to eight chips in about 14 minutes. Available in 115- and 220-volt versions, both include timer assembly and holding tray and are housed in a rugged case made of tough Cycolac plastic. Both versions are available from stock.

Ultra-Violet Products Inc., 5100 Walnut Grove Ave., San Gabriel, Calif. 91778. Phone Skip Spoden at (213) 285-3123 [393]

40-pin DIP clip costs only $13.75

Selling for $13.75, the PC-40 test clip is the lowest priced 40-pin DIP clip presently available, claims the manufacturer. Compatible with 0.6-inch-center ICs of as many as 40 pins, the clip features noncorrosive nickel-silver contacts for low-resistance connections and a narrow throat suitable for high-density circuit boards. The test terminals have gripping contacts to hold instrument
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changeable tips—pretinned chisel­
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Mini-lab temperature-tests
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Offering price savings of 30% over
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temperature control range of
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Plastics' use in electrical/electronic applications could easily quadruple by the end of the century — reaching 6.8 billion lbs. Reason? A rapidly growing technology is leaning away from metal to plastics and plastic-enclosed devices. Stay on top of the dramatically advancing world of plastics at NPE/76, December 6-10, McCormick Place, Chicago.

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- New processes
- New applications

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Electronics / October 14, 1976
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Yes, indeed, for over two years thousands of Analogic's AN2538's have been proving themselves as the premier "single-chip", bipolar, 3½ digit DPI. Imagine an MTBF of 60,000 hours. That's something you can really put on the line! And the AN2538s are made by Analogic, the World's Largest Manufacturer of DPIs.

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For complete data on the AN2538 and short-form catalog showing our complete line of 16 types of DPIs, contact Bob Shipione at (617) 246-0300 or for your local Analogic sales office or stocking distributor, write today: Analogic Corp., Audubon Road, Wakefield, Mass. 01880.
All aerosols are not alike.

The constant progression of sophistication in electronics has demanded a parallel progression in standards of purity. Industrial cleaning is one very vital link in maintaining component and system purity and reliability.

Let's look at eight important criteria and compare Miller-Stephenson products to the general aerosol industrial cleaner industry.

SOLVENTS:
Miller-Stephenson — Most of our aerosols contain 80% Active Ingredient, 20% Propellant.
Other Aerosol Cleaners — Active Ingredient averages 70-75%.
Miller-Stephenson — Uses only Certified Virgin Solvent.
Other Aerosol Cleaners — Some utilize reclaimed solvents. Though lower in cost, reclaimed solvents usually contain foreign substances.

PROPELLANTS:
Miller-Stephenson — Uses only the highest purity, safest propellants. They are nonflammable - TWA 1000 ppm.
Other Aerosol Cleaners — Many use cheap, sometimes flammable, sometimes higher order of toxicity propellants.

FILTERING:
Miller-Stephenson — We double filter “Freon” solvent and propellant — first with a 0.5 micron filter, then with a Millipore 0.2 absolute filter.
Other Aerosol Cleaners — Some use no filters; others only a 0.5 micron filter.

LOADING LINES:
Miller-Stephenson — All loading lines are dedicated to the individual ingredients used.
Other Aerosol Cleaners — Loading lines are often used for multiple products and if not thoroughly flushed, contamination will occur.

LOADING ENVIRONMENT:
Miller-Stephenson — Class 100 Clean Room conditions.
Other Aerosol Cleaners — Normally uncontrolled — environmental contamination can occur.

VOLUME PRODUCTION:
Miller-Stephenson — Our principal raw materials come direct from Du Pont tankers into our 5500 gallon storage tanks through a closed system direct to container.
Other Aerosol Cleaners — Low volume suppliers often load from open 55-gallon drums thereby introducing possibility of contamination.

CONTAINER:
Miller-Stephenson — Our new seamless cans further reduce the possibility of contamination.
Other Aerosol Cleaners — Cans with soldered seams may introduce residual contaminants.

SAFETY IN SHIPPING:
Miller-Stephenson — Most of our “Freon” aerosol solvents are non-regulated items. exempt from all Federal Regulations “Restricted Articles”. May be Shipped Air Transport.
Other Aerosol Cleaners — Do not meet Air Transport Regulations.

MS aerosol solvents have the lowest residual contamination in the industry — some approaching 5-7 ppm. The general range for the industry is 50-130 ppm.

“Freon” is Du Pont’s registered trademark for its fluorocarbon compounds.

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Enclosed is $2.00, please send my “Trial Unit” of MS-180.
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These TRW/Cinch Edge Connectors were created to beat rising material costs while maintaining product performance. Designed with 20 micro-inches of gold—at the contact areas only, over 30 microinches of nickel underplate—with high capability, glass-filled polyester insulators, they cost about 20% less than our standard commercial units, 50% less than our military types. The use of nickel, an effective barrier to base metal migration, rather than copper underplate, provides a longer wearing, more stable gold layer.

As with other members of TRW/Cinch Edge Connector family, you have a choice of seven sizes from 6 to 25 positions, in both dip solder and solder tab terminations—and delivery is when you expect it, from TRW/Cinch Connectors or its distributors.

For more information call your nearest sales office or distributor (listed in EEM)—or contact TRW/Cinch Connectors, An Electronic Components Division of TRW, Inc., 1501 Morse Avenue, Elk Grove Village, Illinois 60007, (312) 439-8800.

Circle 158 on reader service card
New products

Subassemblies

Hybrid op amps minimize drift

Units for inverter uses have feed-forward amplifier for fast slew and low drift

Operational amplifiers in module form for inverting applications are bulky and expensive. Currently available space-saving hybrid counterparts use a field-effect-transistor input stage, which makes them subject to drift. Engineers at Datel Systems Inc. have overcome the drift problem in their AM-500 series of 14-pin DIP-housed hybrids for fast-settling inverting usage in high-frequency drivers, fast integrators, or the output stages of fast digital-to-analog converters and sample-and-hold circuits.

Datel has incorporated a feed-forward amplifier design that provides a settling time of 200 nanoseconds to an accuracy of 0.01%, a slew rate of 1,000 volts per microsecond and a drift of just 1 microvolt/°C. Those specifications are accomplished in combination with a minimum gain bandwidth of 100 megahertz.

Eugene Zuch, product marketing manager, says that Datel uses an AM-500 in its own recently introduced SHM-5 ultra-fast sample-and-hold module, which has a signal-acquisition time of 200 ns and is accurate to within 0.1% (Electronics, Sept. 2, p. 198). Its usage in that module has given Zuch confidence that the AM-500 specifications are accurate because “hundreds” of the circuits have been successfully produced, he says.

The specifications also include these dc characteristics: open-loop gain of 10^6, 30-megohm input impedance, and a 1-nanoampere bias current. Input offset voltage is ±0.05 millivolt, and input-voltage drift is 1 µV/°C. Three versions are being offered: the model AM-500GC, which covers the tempera-
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New products/materials

ture range from 0°C to 70°C, with a price of $85 in quantities of one to nine and a hermetically sealed glass case; AM-500MR for -25 to +85°C, which sells for $95, and the AM-500MM for the -55 to +125°C range, which carries a $135 price tag. The latter two units are in metal cases, and delivery time is six weeks; the AM-500 GC is available from stock.

data-acquisition system handles low and high levels

Designed to handle full-scale inputs as low as ±10 millivolts and as high as ±10 volts, the model SDM853 data-acquisition system is a 12-bit unit with 8 differential or 16 single-ended input channels. The system is extremely flexible because its input analog multiplexer, its gain-setting instrumentation amplifier, its sample-and-hold amplifier, and its analog-to-digital converter are not internally connected. The C-MOS analog multiplexer eliminates the need for stocking two comparable units for single-ended and differential inputs. The instrumentation amplifier can be programmed to have gains from 1 to 1,000, with an offset voltage drift of 2 microvolts/°C at a gain of 1,000. Minimum throughput sampling rates as high as 30 kilohertz are possible with full 12-bit performance; if 8 bits are adequate, throughputs up to 43 kHz can be achieved.

In quantities of 100, the SDM853 sells for $170 each. Delivery is from stock to two weeks.

Power op amp delivers more than 1 ampere

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produce output currents in excess of 1 ampere. With an adequate heat sink, the units can dissipate 22 watts at 25°C. The units have a full-power bandwidth of 15 kilohertz (40 kHz, typical), a quiescent power requirement of only 200 milliwatts, and an input offset voltage of 1 millivolt. Typical applications of the amplifiers include cathode-ray-tube deflection, servo driving, audio amplification, and use in automatic test equipment.

The model 833-21 operates from -55°C to 125°C and sells for $26.15 each in hundreds. It is a pin-for-pin replacement for National's LH0021, and its specifications are similar. The model 833-21C covers -25°C to 85°C, and the device carries a 100-piece price tag of $13. It competes with National's LH0021C.

Beckman Instruments Inc., Technical Information Section, Helipot Division, 2500 Harbor Blvd., Fullerton, Calif. 92634

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New products

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North Atlantic Industries Inc., Terminal Dr., Plainview, N.Y. 11803. Phone Ken Salz at (516) 681-8600 [387]

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ILC Data Device Corp., Airport International Plaza, Bohemia, Long Island, N.Y. 11716. Phone (516) 567-5600 [386]

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Teledyne Philbrick, Allied Drive at Route 128, Dedham, Mass. 02026. Phone (617) 329-1600 [385]
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Multicore Solders, Westbury, N.Y. 11590

[476]

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New products/materials

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Atomerger Chemetals Corp., 100 Fairchild Ave., Plainview, N.Y. 11803 [478]

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Electronics/October 14, 1976
New literature

Selecting a 4-k RAM. A report entitled "The 4-k RAM dilemma: 16-, 18- or 22-pin?" tries to help designers select the best devices for their particular applications. It describes the five basic types of dynamic 4,096-bit random-access memory now offered, and compares them on the basis of addressing, multiplexing, clocking, input/output, price, board area, availability, and reliability.

Copyes are offered by Texas Instruments Inc., Inquiry Answering Service, P.O. Box 5012, M/S 308 (Att: "4-k RAM dilemma" Literature), Dallas, Texas. Circle reader service number 421.

Designing microprocessor-based systems. A six-page application note (AN 167-13) discusses the role of logic-state analyzers in the design of microprocessor-based systems. The note includes a few case histories of problems that were solved easily with these analyzers but would have been difficult with traditional methods. A brief bibliography is included. The report can be obtained from the Inquiries Manager, Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, Calif. 94304 [422].

Terminal blocks. Catalog 201 describes the newly expanded line of barrier terminal blocks for printed-circuit boards and panel mounting offered by RDI/Reed Devices Inc., 21W183 Hill Ave., Glen Ellyn, Ill. 60137. The 16-page catalog gives prices and lists more than 110 distributors. [423].

Digital instruments. A broad range of portable, bench-type, and systems-oriented digital multimeters as well as a 100-megahertz counter/timer are covered in a 12-page catalog put out by Data Precision Corp., Audubon Rd., Wakefield, Mass. 01880 [424].

Safe cables. A line of communications and control cables that is UL-listed, CSA-certified, and OSHA-acceptable is presented in a 28-page catalog released by Alpha Wire Corp., 711 Lidgerwood Ave., Elizabeth, N.J. 07207 [425].
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