THE "INTERMEDIATE" SYNTHESIZER

160 MHz for $5900

Buying a frequency synthesizer has been something like buying a car. There's a confusion of models, options, and price ranges. Except—there has never been a so-called "intermediate"-model synthesizer. That's because price and performance ranges of synthesizers have tended to cluster just at both ends of the spectrum. The choice was between lower-cost, limited-frequency-range models and those with everything, including a sky-high price tag. So, the buying decision was one based on either trade-off or over-capability.

This is not true any longer! GR has filled the price-capability gap with the new 1165 Frequency Synthesizer. Frequency range is wide, 0.01 to 160 MHz in 100-Hz steps. The price is only $5900, less than half the price it used to cost to get 160 MHz. If you can furnish your own frequency reference signal (5 or 10 MHz), you can get a model for only $5300. In the $5900 model, frequency accuracy is maintained either by an internal precision 10-MHz oscillator (1 x 10^-9 per day) or by an external drive or lock source. Output is 0.1 to 1 V into 50 ohms. Both frequency and level can be externally programmed; the 1165 is ideal for applications requiring remotely-programmed local oscillators. Harmonics are typically down 30 dB (at maximum output into 50-Ω load); spurious, discrete non-harmonic signals are typically down 60 dB.

For complete information, write General Radio, West Concord, Mass. 01781; telephone (617) 369-4400. In Europe: Postfach 124, CH 8034, Zurich, Switzerland.

GENERAL RADIO
A wave analyzer with a 10,000-second sweep time? Why?

...because, in low-frequency spectrum analysis work, you need to use a narrow-bandwidth window. The narrower the window you use, the slower you must sweep it across the frequency range to be analyzed. And the slower you sweep, the smaller a frequency range you can cover in any given time. Thus, until now, your choice has been either accuracy or range but not both.

The new HP 3590A/3595A system solves that dilemma. The HP 3595A plug-in is a sweeping local oscillator with 10,000 seconds of sweep time available. By using it with the HP 3590A Wave Analyzer mainframe, you can scan the entire three-decade audio frequency range at 2 Hz per second, in one sweep. And, by adding an HP X-Y recorder, you can see the results on a single 11 x 17-inch graph.

In addition to extended sweep time, the 3590A/3595A combination also gives you a choice of five sweep rates (from 1 Hz to 1,000 Hz per second) and four filter bandwidths (from 10 Hz to 3,100 Hz), an 85 dB dynamic range over either of two frequency ranges (20 Hz to 62 kHz and 200 Hz to 620 kHz), 3 µV to 30 V sensitivity, and built-in autoranging for ease of operation.

The result is a systems-analysis tool ideally suited for work in the lower frequency ranges, with the capability to work in higher frequency ranges as well!

The 3590A Wave Analyzer mainframe is $3200; the new 3595A plug-in with the 10,000-second sweep time is $1250. Other plug-ins available for the 3590A are: the 3592A slave and program unit, for use with a second mainframe, $80; the 3593A with 3-digit mechanical display and 620-second maximum sweep time, $1100; and the 3594A with 5-digit electronic counter frequency display and 620-second maximum sweep time, $1600.

To get complete information on the HP 3590A and the various plug-ins, contact your local HP field engineer. Or, write to Hewlett-Packard, Palo Alto, California 94304. In Europe: 1217 Meyrin-Geneva, Switzerland.
For $25 what can you expect from a solid-state display?

10 years of brilliant service.

For $25 each in 1 K lots, HP sells you a hermetically sealed, IC-compatible display with on-board decoder-driver. You just plug the module in; it accepts direct 4-line BCD input. You also get a 5 x 7 dot matrix character that prevents ambiguous readings and a parallax-free design in a package that’s one inch high and practically flat. It’s the brightest display idea since the tube.

Hewlett-Packard

Our reliability study shows that the 5082-7000 numeric display glows on for 24 hours a day, 7 days a week for more than 10 years. Ask your HP field engineer for a copy of the study plus volume price quotes on the biggest all-around bargain you can buy in solid-state displays. Or write Hewlett-Packard, Palo Alto, California 94304; Europe 1217 Meyrin-Geneva, Switzerland.
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- **New Literature**
The Russians were at the Paris components show again this year, trying to drum up Western sales for their wares. Visitors descended upon the Soviet exhibit in large numbers, observing closely the integrated circuit displays, and comparing them with what they saw last year, when the Russians revealed their ICs for the first time. Several things had happened in the interim.

A handful of new devices were added to the IC's already introduced. Agenl s had been named to handle sales and market development in some of the Western countries—France and West Germany, for example.

The Russians had provided potential Western customers with sample ICs and are now awaiting responses.

The circuits exhibited in Paris suggest a broad technological capability, ranging from military-grade thin-film hybrids through thick-film hybrids in IBM-type packages. Also shown were monolithic IC's in both flatpacks and dual-inline plastic packages. Logic schemes comprised DCTL, RTL, DTL, TTL, ECL, and MOS. Included among the linear circuits were operational amplifiers and specialized consumer electronic items. The thin-film hybrids are made using thermocompression-bonded gold lead wires rather than the more advanced approaches, such as beam leads. Russian materials specialists disclaim the use of any unusual thin-film materials or out-of-the-ordinary ceramic substrates. Critics who examined the Russian IC's said the workmanship was somewhat below par.

Nonetheless, the Soviets speak with pride of their accomplishments, calling attention to a new monolithic MOS memory matrix with 1,360 MOS transistors on a single chip; ECL with 4-nanosecond delay and 80-milliwatt dissipation; TTL with 60-nsec delay and 3-mw dissipation; and DTL with 90-nsec delay and 23 mw dissipation.

While these specifications clearly indicate that the Russians have no technological advantages over their Western competitors, it is equally clear that they are taking the long-term view. Last year, according to their own estimates, they exported $150,000 worth of IC's to Eastern Bloc countries. In the future they envision export sales of several million IC's annually—the exact quantity and timing depending on present evaluations by potential customers. But this evaluation phase may stretch out further than the Russians expect. For example, a solid prospect a year ago for Russian-made MOS devices was Italy's Ing. C. Olivetti, for its desk calculators. But at the time of the Paris show, Olivetti still had not decided to buy.

In the meantime, the Russians are not pinning their hopes on IC's alone. During the recent component shortages in West Germany, the Russians enjoyed an upsurge in diode sales and could have sold a lot of transistors, too, except for discrepancies in packages. In still another area, both Kodak and IBM are showing interest in Russian light-emitting diodes, and both companies may test sample lots. The Russians also are hoping to sell power transistors, field effect transistors (both n and p channel), and varicaps. The latter have tuning ratios of up to 1.8 and Q's from 20 to 300.

Not all of the approximately 150 new devices the Russians exhibited in Paris were solid state. Shown for the first time outside the Soviet Union were radar tubes
Readers Comment

To the Editor:

The remarks attributed to Richard H. Stern, chief of the new patent unit in the Justice Department’s antitrust division [Feb. 16, p. 14], raise some questions about the nature of patents. How can a patent, as stated in the U.S. Constitution, “secure for limited time to authors and inventors the exclusive right to their respective writings and discoveries” without hampering free enterprise?

Each of the patents that I hold grants to me as well as my successors, heirs or assigns a time limited right to exclude others from making, using or selling the said invention. These rights represent a payment to me in return for my willingness to make my invention known at once.

It would be most interesting to learn from Mr. Stern what kind of limitation of the inventor’s rights he has in mind.

Jorgen P. Vinding
Monte Serano, Calif.

- Mr. Richard W. McLaren, assistant Attorney General in the antitrust division of the Justice Department, in an address, April 9, to the antitrust section of the American Bar Association said that the patent-antitrust cases filed by the department have been, by and large, challenges to licensing arrangements and practices that law-review articles have questioned and knowledgeable patent-antitrust lawyers have counseled their clients against for a good quarter of a century. The objective of the patent-antitrust program is, according to McLaren, “to honor the patent grant, but to prevent its expansion beyond its intended scope—to protect competition against unreasonable restraints.”

Place in the sun

To the Editor:

After reading the article on building low-threshold MOS, [April 13, p. 118], I was somewhat surprised to find that, on page 122, National Semiconductor was left out of the list of companies building low-threshold MOS. You must have a short memory. In your March 16 issue [p. 33] National was recognized as the largest MOS supplier. National announced its first low-threshold, 1-0-0 process in July 1967.

In addition, I must disagree with the “industry spokesman” who says that low-threshold devices can’t drive TTL without a sense amplifier. That is completely untrue. Originally, an external resistor was required to handle the TTL sink current in our 1967 product, but recent products interface directly with bipolar at the input or output.

Gene Carter
National Semiconductor Corp.
Santa Clara, Calif.

Patent rights

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Gene Carter
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Santa Clara, Calif.

McGraw-Hill News Service

Director: Arthur L. Moore; Atlanta: Fran Ridgway; Chicago: Robert E. Lee; Cleveland: Arthur Zimmerman; Dallas: Marvin Reid; Detroit: James Wargo; Houston: Barbara LeRoux; Los Angeles: Michael Murphy; San Francisco: Margaret Drossel, Tyler Marshall; Seattle: Ray Bloomburg; Washington: Charles Gardner, James Canan, Herbert W. Cheshire, Seth Payne, Warren Burkett, William D. Hickman; Bonn: Robert F. Ingersoll; Brussels: James Smith; Hong Kong: Kate Mattock; London: John Shinn; Mexico City: Gerald Parkinson; Milan: Jack Star; Moscow: Jack Winkler; Paris: Robert E. Farrell, Stewart Toy; Tokyo: Marvin Petal
**Problem:** You want a zero offset source follower, operating from DC to 100 MHz.

**Solution:** One Siliconix 2N5912 and two matched resistors as shown.

Half the device acts as a current generator for the source follower. Since the FETs are matched to less than 15 mV, $V_{GR} = I_o R_1 = I_o R_2 = V_{GR}$ and near zero offset is achieved.

We have more applications information on this and other FETs. Just write or call!

---

**Who's Who in this issue**

**Goldmark**

"Well known" becomes a supreme understatement when applied to Peter Goldmark, who wrote the article that starts on page 94. President and research director of CBS Laboratories, Goldmark, a native of Hungary, holds a Ph.D. in physics from the University of Vienna. Among his many achievements are field sequential color television and the long-playing record. He also is responsible for the high-resolution read-out and ground recording system used in the lunar orbiter.

**Odone**

Like Hannibal before him, Giovanni Odone, author of the article that starts on page 102, crossed the Alps. But in Odone's case the trip was from the University of Rome to the research department of Switzerland's Paillard S.A. Odone holds a doctorate in physics.

**Carter**

An engineering-marketing team proved to be the right combination for the article that begins on page 107. Author Dale Mrazek is a digital applications engineer at National Semiconductor, while co-author Gene Carter is National's product marketing manager. Mrazek is a BSEE graduate of the University of Denver, and Carter is an alumnus of the Milwaukee School of Engineering.
Now...new, lower capacitance values!

Type 192P PACER®
FILMITE® 'E'
CAPACITORS

- Low-cost film capacitors with one-third the size of conventional tubulars
- Eight new low-capacitance values (100 pF thru 390 pF @ 200V) added to broad range of standard ratings
- Special construction—extended foil sections terminated in metal end caps, assuring positive contact with every turn of the electrodes
- End caps are also effective moisture barriers
- Entire assembly protected by special sleeve of high dielectric strength
- Request Engineering Bulletin 2066B

Newly broadened low-cost line!

Type 196D Dipped
Solid-Electrolyte
Tantalex® Capacitors

- Here's a capacitor design that admirably fills the need for low-cost yet dependable solid tantalum capacitors suitable for printed wiring boards
- Straight leads as well as crimped leads are readily available to meet your manufacturing needs
- Covering a broad range of capacitance values from .1 µF to 330 µF, with voltage ratings from 4 to 50 VDC, Type 196D Capacitors are protected by a tough insulating coating which is highly resistant to moisture and mechanical damage
- Request Engineering Bulletin 3545A.

For Engineering Bulletins as noted above, write to:

THE BROAD-LINE PRODUCER OF ELECTRONIC PARTS
Why the bankers gave us the money:

At a time when credit couldn’t get any tighter without twanging, when the semiconductor industry needed another bunch of hotshots like you need a power failure, a new company got the Bank of America, Schröder Rockefeller, The Capital Group, Inc. and Donaldson, Lufkin & Jenrette to give it enough cash, enough credit, enough commitment to make the new company a serious marketing factor before its first anniversary.

This is what we told them:

1. **We are hotshots.**

If you have to call us names, that’s as good as any other.

As individuals and as a growing team, the members of this company invented circuits, processes and markets. Each has had a serious technical or marketing position with a major semiconductor firm. Each has his own commitment to excellence.

Let’s face it: That’s why we got together.

2. **We know what we’re doing.**

We’re in the large chip MSI and LIC business. Period. No jelly beans. No 10,000-gate freaks. Only the tough-to-make, easy-to-utilize mainstream circuits.

We selected the best people in the business to build (to our specifications) a processing facility that was optimized for the precise, complimentary process control requirements of complex, high performance digital and linear integrated circuits.

We decided to make only one quality of circuit: mil spec reliability or better. By this concentration of technical resources, we’re able to get yields that let us sell circuits which meet the most stringent military reliability requirements and the equally stringent pricing requirements of the commercial market.

And it feels so good, we’re going to keep it up.

Oh, yes. Out of our checkered pasts we remembered that there was a kind of annoying difference between employees and owners. So we fixed it. Every employee here is an owner. (As a matter of fact, every owner is an employee except for the bankers.)

3. **We know who you are.**

You’re in the fastest-growing part of the market; probably the computer and peripheral equipment business.

You’ve been had by experts, so you’re ready to listen when we say: We’ll never announce a product that isn’t in high volume production, in-house qualified through documented electrical and mechanical life testing, 100% stress tested to MIL STD 883 and in inventory.

The other reason we got the money is that we told the bankers we’d introduce complete product lines—digital and linear—for sale in volume before we are a year old.

And we will.

Advanced Micro Devices Inc.

901 Thompson Place, Sunnyvale, California 94086
Established May 1, 1969

Advanced Micro Devices has perfected the production technology of complex, mainstream digital and linear monolithic circuits.
If you want 52,593* MSI and Linear circuits, we can ship today.

If you need another 125,566* - you’ll have to wait a couple weeks.

<table>
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<th>Order Number</th>
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Compensated Operational Amplifiers

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Dual Operational Amplifiers

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High Speed Operational Amplifiers

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Video/Sense Amplifiers

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Nine MSI
(More to come.)

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<td>8285A</td>
<td>Dip</td>
<td>-55°C to +125°C</td>
<td>11.70</td>
</tr>
<tr>
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<td>Dip</td>
<td>0°C to +75°C</td>
<td>11.75</td>
</tr>
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<td>9310 U6B931051X</td>
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<td>-55°C to +125°C</td>
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<tr>
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<td>Dip</td>
<td>0°C to +75°C</td>
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<td>Dip</td>
<td>-55°C to +125°C</td>
<td>16.00</td>
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Demultiplexers

<table>
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<th>Price 100pc Mix</th>
</tr>
</thead>
<tbody>
<tr>
<td>9301 U6B930159X</td>
<td>Dip</td>
<td>0°C to +75°C</td>
<td>6.25</td>
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<tr>
<td>9301 U6B930151X</td>
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Multiplexers

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<tr>
<td>9309 U6B930959X</td>
<td>Dip</td>
<td>0°C to +75°C</td>
<td>5.30</td>
</tr>
<tr>
<td>9309 U6B930951X</td>
<td>Dip</td>
<td>-55°C to +125°C</td>
<td>7.95</td>
</tr>
<tr>
<td>9312 U6B931259X</td>
<td>Dip</td>
<td>0°C to +75°C</td>
<td>5.30</td>
</tr>
<tr>
<td>9312 U6B931251X</td>
<td>Dip</td>
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Registers

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<td>Dip</td>
<td>0°C to +75°C</td>
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</tr>
<tr>
<td>9300 U6B930051X</td>
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<td>-55°C to +125°C</td>
<td>9.75</td>
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Adders

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<td>Dip</td>
<td>0°C to +75°C</td>
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</tr>
<tr>
<td>9304 U6B930451X</td>
<td>Dip</td>
<td>-55°C to +125°C</td>
<td>10.25</td>
</tr>
</tbody>
</table>

1. All units listed here are functionally, electrically and pin-for-pin replacement for the original manufacturers' devices.
2. All units, hermetic DIP and TO-5, are subjected to 100% stress test as outlined in MIL STD 883 Test Method T5004.

MSI & Linear Mixing Discounts!

Only Advanced Micro Devices offers quantity discounts on combined orders for complex digital and linear circuits. A minimum of ten pieces of a single device type can be combined for 100 or 1000 piece mixed quantity pricing. Here's an example:

<table>
<thead>
<tr>
<th>Qty.</th>
<th>Device</th>
<th>10-99</th>
<th>100pc Mix price</th>
<th>Savings/Device</th>
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<tr>
<td>10</td>
<td>101A</td>
<td>$36.00</td>
<td>$30.00</td>
<td>$6.00</td>
</tr>
<tr>
<td>15</td>
<td>715C</td>
<td>9.55</td>
<td>7.95</td>
<td>1.60</td>
</tr>
<tr>
<td>25</td>
<td>723C</td>
<td>3.90</td>
<td>3.25</td>
<td>0.65</td>
</tr>
<tr>
<td>25</td>
<td>9310</td>
<td>14.00</td>
<td>11.75</td>
<td>2.25</td>
</tr>
<tr>
<td>25</td>
<td>9300</td>
<td>7.80</td>
<td>6.50</td>
<td>1.30</td>
</tr>
</tbody>
</table>

*Last time we checked (at 5:00PM, April 11, 1970) we had about a third of our product in box stock inventory and the rest in finished dice, two to four weeks away from box stock.

To order, call us at 800-538-7904 toll free or, in California, call 408-732-2400.

For product and quantity pricing information, circle the bingo number.

Advanced Micro Devices has perfected the production technology of complex, mainstream digital and linear monolithic circuits.
"No one made a small, quiet, medium-speed chain printer for $9500. So Mohawk did."

George C. Hohl, OEM Marketing Director, discusses a new product.
"We saw a gap in the printer field. Either you paid a lot of money to get a lot of speed and sophistication, or you could pay a little and get very little in return. We decided to aim our printer somewhere in between.

"Chain printers are mechanically simpler, easier to maintain, less expensive. Their flat face characters give good print characteristics, too.

"Our design requirements were rough. We wanted 300 lines-per-minute with such niceties as easily changeable fonts, and yet we wanted to sell it for less than $10K. It had to be small, and yet we couldn’t lose accessibility. The design engineers grumbled, but they made it.

"The changeable font cartridge is great—an operator can quickly switch the font chain—and we’re offering fonts from 16 to 128 characters.

"We designed a disposable ribbon cartridge to make ribbon changes quick and clean. Paper handling is enclosed to stay clean, too. And everything that could be modularized, was modularized.

"We considered noise reduction vital—anyone who has worked in a printer room knows why. Well, compared to other printers, you’d hardly know this one was working.

"We’re selling the printer for $9500 in OEM quantities, and some variations cost even less. So you get a lot of performance in a very little printer—for very little money."

Mohawk Data Sciences Corp.
Herkimer, New York

OEM MARKETING CENTERS: CORPORATE (315) 867-6475; NORTHEAST (617) 891-5870; SOUTHEAST (404) 631-3443; CENTRAL (312) 298-4141; WEST COAST (213) 685-5165.
For a year now, Gerard L. Seelig has been president of the Lockheed Electronics Co. As such, his main mission has been to increase the firm's nonmilitary and aerospace business. In other words, he's turning Lockheed Electronics into a commercial electronics company, and judging by the numbers, he's doing a good job. As of this year, nearly 40% of the firm's business is commercial, and in the next few years, he predicts, that figure should rise to at least 50%.

Thus, it's understandable that Gerry Seelig bristles at headlines like the one that appeared earlier this month in the business section of a leading daily newspaper: "Lockheed's Illness Is Contagious." The reference, of course, is to the financially distressed parent company, the Lockheed Aircraft Corp. in Burbank, Calif., and its trouble-plagued C-5 military aircraft program. But publicity like that isn't needed by Lockheed Electronics, a $100 million company in its own right. "You might almost say that one of our biggest problems right now is getting out from under the wing of the C-5," Seelig complains.

Getting wet. Ten years ago, 90% of Lockheed Electronics' business was military and most of that was performed for the parent organization. And as many other military- and space-oriented companies have found out, "You can't just wave a magic wand and come up commercial," Seelig explains. "There are one of two ways you can do it, but only one works. You can jump right in—always a risky way of doing things—or you can do it slowly, one toe at a time. Roughly five years ago, we started to make major diversifications into the commercial-industrial sector."

As it is, Lockheed Electronics is now split into three major groups: Military Systems, Technical Services and Support, and Commercial Electronics, which includes both the Industrial Technology and the Data Products divisions.

Not that Lockheed Electronics is in any way getting out of the military business. In fact, the Military Systems division recently won a $7 million Navy contract for the Mark 86 gunfire control system. And the division is also working on, among other things, follow-on contracts for checkout and test equipment for the Polaris and Poseidon missile programs.

But still, it's his company's growth in the commercial sector that Seelig would rather expound upon, and he points particularly to the Data Products division in Los Angeles—which shouldn't be surprising. Before taking over the
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Electronics | April 27, 1970
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Keyboard layout is simple and easy to follow. Sharp's decimal point pre-set system, rounding-off/down selector, and constant switch cut down on calculation time. All keys have double-set protection built in.

This is the Compet to ask if you want the best of both calculation worlds—printed proof at electronic speed.

The stylish, convenient Compet CS-661.

Who's Who in electronics

reins at Lockheed Electronics' Plainfield, N.J., headquarters, Seelig was general manager of Data Products. He says: "When I joined that division in 1963, we did less than $6 million worth of business, 90% of which was military and 90% of which was intercompany. Now, DPD is doing $50 million, more than eight-fold growth, and 90% of it is commercial business."

Core of the matter. Last year, according to Seelig's estimates, Data Products sold 1.4 billion bits of ferrite-core memories, making it one of the country's largest independent suppliers of core memory. Some of it was sold as just plain cores, some in stacks, but more were sold as memory systems, he explains. The division is also producing plated-wire memories, but that's still a pilot operation—"About two to three million bits per week—just 10% of what we do in cores."

The division, according to Seelig, also ranks as one of the leading independent manufacturers of interconnections for computers—in other words, p-c board.

And, finally, there's the MAC (for multiapplication computer) 16 minicomputer, a 1-microsecond, 16-bit, 4,000-word machine that has been on the market for two years and sells for $11,500. So far, the venture into computers has been a success. "We've had several million dollars worth of sign-ups for the machine in the first year, and we expect to do three or four times that this year," he says.

MAC son. So successful, in fact, has MAC 16 been, that Lockheed Electronics is now set to announce its second entry in the minicomputer field, the MAC 16J (for junior, as it's jokingly referred to around the plant). This too is a 1-µsec machine for under $8,000.

Seelig's faith in computers and computer systems at Lockheed Electronics has a solid basis. Besides being the man who built up the Data Products division, Jerry Seelig feels that his promotion to president of the company was a not-so-subtle way for the parent organization, Lockheed Aircraft, to assure the continued expansion of its Plainfield subsidiary into commercial EDP.
If price and performance are important—here's a 7 MHz value

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**Value** — $5 mV to 20 V per division deflection factor. Here is sufficient capability to pick up most electronic or electro-mechanical system outputs without distortion or need for additional amplifiers.

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**Value** — Easy to use. Logical arrangements of controls, beam finder, auto-triggering make operation easy. Interlocking controls on sweep time and magnifier prevent readout errors.

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**Value** — Available in single channel cabinet or rack versions (1215A or 1215B), or in dual-channel cabinet or rack versions (1217A or 1217B). Electrical characteristics are identical. Rack version is only 5¼" high. Panel on the cabinet version is about the size of this page.

**Value** — Price, 1215A/B, $950; 1217A/B, $1175. Add up the features, then divide by price and you'll find this is the greatest performance/dollar value ever offered.

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For prices and delivery or additional data on Delco's new DTS 802 and 804 contact us or your nearest Delco Radio distributor.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>DTS-802</th>
<th>DTS-804</th>
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<tbody>
<tr>
<td>Collector to emitter voltage</td>
<td>1200V max.</td>
<td>1400V max.</td>
</tr>
<tr>
<td>Collector to emitter voltage</td>
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<tr>
<td>Sustaining voltage</td>
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<td>Emitter to base voltage</td>
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<td>Collector current (Ic)</td>
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<tr>
<td>hFE, Ic = 3.5A, VCE = 5V</td>
<td>2.2 min.</td>
<td>2.2 min.</td>
</tr>
<tr>
<td>*P.E.T.; Ic = 7A, VCE = 200V, tp = 300 µsec, duty cycle &lt;4%</td>
<td>420 mJ min.</td>
<td>420 mJ min.</td>
</tr>
</tbody>
</table>

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Meetings

Cherry blossom time in Dayton

Echoing the nation’s concern about the fact that man is rapidly polluting the planet on which he lives, the National Aerospace Electronics Conference is seeking this year to emphasize the “use of aerospace electronics to better our environment, to help solve the problems of the planet on which we live.” Good idea, perhaps, but the technical program falls short of this mark. With but a few exceptions, the program deals with what is generally expected at technical conferences—solutions to special engineering problems. And most of them, far from influencing the environment, have yet to operate effectively in the laboratory.

This year’s Naecon, the 22nd annual event, takes place from May 18-20 at its usual locale—Dayton, Ohio. The 13 technical sessions and a technical exhibit will be housed at the Sheraton-Dayton Hotel.

In general, the session most nearly coming to grips with the environment is one on general aviation, an unlikely discussion topic in the shadow of the Air Force’s giant Dayton-area development and procurement facilities. Wright-Patterson Air Force Base, for example, manages more than half of the Air Force’s $7 billion budget for research, development, test, and evaluation.

Helping the nation’s numerous small aircraft navigate more effectively is the theme of the papers in the general aviation session. Michael Brandawie of the Federal Aviation Administration will go into the new area-navigation regulations, and George Cherry of MIT’s Instrumentation Laboratory will describe how the FAA has integrated into navigation equipment the microwave scanning-beam instrument-landing system the agency has been testing.

Time-frequency concepts for air traffic control, of the kind used in the Air Transport Association’s collision-avoidance system and regarded by many as the potential savior of the air traffic system in the 1980’s, will also have its own session. Robert Buck will present the FAA’s viewpoints on the subject, and William Weiss of Automationics will report on the FAA-sponsored analysis of a national airspace system using the time-frequency concepts. Jack Craigie of TRW will discuss a navigation satellite system, and William Sen of the Air Force’s Electronic Systems division will describe his organization’s attempts to combine communications and navigation using time-frequency techniques.

Computers. Two computer sessions will deal with parallel processors and software, with Leon Wald of Honeywell talking about an associative memory using large-scale integration, and speakers from the Systems Development Corp., Automationics and Logicon examining special aerospace-oriented languages. Other sessions will be devoted to microelectronics, with Robert Werner of the Air Force Avionics Laboratory describing high-speed—up to 100 megahertz—metal oxide semiconductor circuitry, and Gordon Babanus, also of the Avionics Laboratory, digging into the impact of silicon-nitride technology on the memory field. The rest of the technical areas include: bio-cybernetics, airborne reconnaissance and secondary power systems. A classified session will discuss the future of aerospace communications.

For further information, contact Joseph P. Matt, 4130 Linden Ave., Room 750, Dayton, Ohio 45432.

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Hybrid microcircuits will dominate this year’s Electronic Components Conference to be held May 13-15 at the Statler-Hilton Hotel in Washington. Highlights will be papers on materials selection, interconnection design, and fabrication techniques for hybrids, indicating
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Meetings

(Continued from p. 22)

a significant change in emphasis from discrete components to "integrated" units. Sponsors are the Electronic Industries Association and the IEEE.

Two sessions will be devoted to materials, with papers on materials for hybrid resistors and capacitors. For example, A. Feuersanger and M.S. Wasserman of General Telephone and Electronics Laboratories, with I. Pratt of the U.S. Army Electronics Command, will report on the preparation and properties of thin-film capacitors with high capacitance per unit area—an important characteristic in crowded microcircuits. Capacities of 10 microfarads per square inch have been achieved. Metal nitrides as resistor materials will come under the scrutiny of C.Y.D. Huang of TRW. His focus will be on resistive glazes composed of titanium nitride and titanium. And Akio Sato, Y. Oda, and Y. Hishinuma of Nippon Electric, will report on a tantalum nitride thin-film resistor with a low-temperature coefficient of resistance.

Hybrid design is becoming more analytical, judging by the contribution of Jack G. Hewitt of the University of Denver and T.E. Christen of Hewlett-Packard. They'll discuss electrical conduction in cermets.

In the thick. Thick-film hybrids will be treated in three sessions. Typical entries: "The Effect of Geometry on the Characteristics of Thick-Film Resistors" by Dietrich E. Riemer of EMR Telemetry; "The Influence of Geometry and Conductive Terminations of Thick-Film Resistors" by Sidney J. Stein and J.B. Garvin, Electro-Science Labs; and "The Economics of Thick-Film Hybrid Microcircuit Production" by Donald Hamer of State of the Art Inc.

Face-down bonding and beam-lead bonding of monolithic IC's are increasingly important techniques in hybrid circuit fabrication. This is reflected in a paper from Alexander Coucoulas of Western Electric on beam-lead applications of
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Meetings

(Continued from p. 24)

compliant bonding, and in one by L.K. Keys, F. Francis, A. Russo, and S. Herring of Magnavox on photoetching and screen printing of conductor patterns for face-down bonded devices. R.J. Clark and J.W. Lunden of General Electric will speak on the use of related technique—semiconductor on thermoplastic on dielectric (STD)—in hybrid circuits.

The session on networks and filters focuses on hybrids, too. Topics include inductorless filters (G.S. Moschytz of Bell Labs), a gyrator suitable for monolithic bipolar construction (Edward L. Renschler and E.H. Allen of Motorola), and principles and design of gyrator filters (J.J. Golembeski and T.N. Rao of Bell Labs).

Although hybrid circuit components are in the spotlight, conventional discrete components aren’t neglected. J. Burnham and R.S. Buritz of Hughes, for example, will discuss failure mechanisms in high-voltage mica paper capacitors, and Sam Y.M. Feng will speak on design analysis of toroidal inductors.


Calendar


Industrial and Commercial Power Systems and Electric Space Heating & Air Conditioning Joint Technical Conference, IEEE; San Francisco, May 4-7

Safety in Research and Development, National Safety Council and the American Society of Safety Engineers; Cambridge, Mass., May 4-5.


(Continued on p. 28)
Is the 901 counter-timer just too good to be true?

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Last year, when we introduced the Model 901, we were years ahead of the industry. Here was the first state-of-the-art universal counter-timer that could count directly to 200 MHz without a plug-in, and—with a plug-in—could go right on counting into the gigahertz range. What's more we offered it at the unheard of low price of only $2475—about $250 to $1000 below the nearest competition!

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CEI DIVISION

Meetings

(Continued from p. 26)


Midwest Symposium on Circuit Theory, IEEE and the University of Minnesota; University of Minnesota, Minneapolis, May 7-8.


International Microwave Symposium, IEEE; Newport Inn, Newport Beach, Calif., May 11-14.

Short courses

Update-Medical Electrical Safety, IEEE Group on Engineering in Medicine and Biology; Marriott Motor Hotel, Philadelphia, May 15; $35 fee.

Systems Evaluation and Measurement Techniques, University of California at Los Angeles, Boelter Hall, May 18-22; $310 fee.

Workshop in Heat Transfer Computer Programs, University of California at Los Angeles, Bel Air Sands Hotel, May 18-29; $420 fee.

Aerospace Vehicle Systems Engineering, University of California at Los Angeles, Boelter Hall, May 18-29; $420 fee.

Call for papers


Photo-Optical Instrumentation for the 70's, Society of Photo-Optical Instrumentation Engineers; Convention Center, Anaheim, Calif., Sept. 14-17. July 3 is deadline for submission of abstracts to Technical Program Committee, SPIE Symposium, P.O. Box 288, Redondo Beach, Calif. 90277.

Digital Computers in Process Control, Louisiana State University, Baton Rouge, Feb. 17-19, 1971. July 12 is deadline for submission of abstracts to Dr. Cecil L. Smith, Chemical Engineering Department, Louisiana State University, Baton Rouge, La. 70803.
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For more information, call your MICRO SWITCH Branch Office or Distributor (in the Yellow Pages under "Switches, Electric"). He'll show you how little it takes to participate in our savings plan.
Plastic package tests proposed by Air Force

The Rome Air Development Center has proposed tests for plastic-packaged microcircuits. They appear to be the first step toward qualifying plastics for military applications other than the highest reliability uses. The tests, which draw upon methods set forth in Mil-Std-883, don't mean the military is endorsing plastics. As Joseph Brauer, chief of the solid state applications section in RADC's reliability branch, puts it, "The tests mean we've proposed that the services lift the statutory ban on plastics and use those that pass these tests. We haven't qualified anything yet; we've proposed a way for the vendors to qualify."

RADC officials point out, however, that even devices that pass the proposed tests will not be accepted for Class A applications as spelled out in the recently distributed specification, Mil-M-38510. Those are applications in which reliability is "absolutely imperative," RADC officials note.

The proposal includes Group B and C tests and adds 100% screening for devices that might be used in Class B and C applications. Group A tests per Mil-Std-883 will remain the same for plastic or hermetic circuits. Group B spells out tests that must be applied to samples from each lot, including salt-atmosphere tests that aren't applied to hermetically sealed devices in Group B. Also added are biased moisture life tests at 85°C and 85% relative humidity with minimum power bias for 1,000 hours. Then there's another set of tests—Group C—detailing periodic sampling of circuits being considered for Class B and C applications.

Raytheon to build mm-wave system for Air Force

The Raytheon Co. has landed a $1,743,619 Air Force contract to develop what may become the military's first general-duty millimeter-wave tactical communications system. Designated the AN/GRC-173, the system will operate between 33 and 40 gigahertz as a line-of-sight digital data transmission system.

While much of the system's capacity will be occupied by data transmission, phase-shift-keyed modulation television, voice, and facsimile also will be part of the mix. Though the exact operating bandwidth is not known, it is said to be the equivalent of 6,000 high-grade voice circuits (3 to 4 kilohertz). A 250-megabit-per-second data rate is a contract requirement, but Raytheon will try to achieve 500 megabits.

Range is to be about six miles, with a bit-error rate of 1 in 10⁶. Output is slated at 100 milliwatts.

Viatron OCR features new font

Viatron's just-introduced optical character reader attacks the almost standard optical-character recognition font, OCR-A. Viatron's gear uses its own Viafont. At present, virtually all OCR's can print and use OCR-A and, to a lesser degree, other fonts. Viatron believes that Viafont applications can surpass those of established fonts and convince users that capital investment in OCR-A readers can be written off.

The company plans to accent its reader's low price in its marketing attack. A tape reader will sell for $2,400 while a card reader will cost $4,800, and an automatically fed document reader will cost $7,200—each price is claimed to be less than a tenth that of competing devices. Viatron also plans an aggressive licensing program that would allow OCR makers and users to adapt their equipment on a royalty basis to read Viafont.
Finally, Viatron claims it can pack six lines of 80 characters each onto a Hollerith-size card, which, if keypunched, is limited to only 80 character positions.

But Viatron's strongest weapon is the potentially vast increase in OCR users. With its prices competing, in some cases, with $500,000 price tags, Viatron figures other OCR makers will almost be forced to adapt to Viafont.

Low cost is achieved through an easy-to-read font and simple electronics—only three MOS/LSI chips. Viafont is read ignoring all but vertical strokes. These are detected by a silicon-diode detector array fed by a fiber-optic bundle split nine ways to divide letters into nine horizontally scanned stripes. Using the length and position of the vertical lines of each letter, and their order of appearance, the OCR's and System 21 console translate printed word to data in ASCII code at about 80 characters per second.

Tektronix, continuing its advance into the peripheral-equipment market, will introduce a real-time computer display at the Spring Joint Computer Conference to be held in Atlantic City May 5-7. The oscilloscope giant also is expected to unveil a hard-copy printer.

Dubbed the T4005, the display will use an 11-inch diagonal cathode-ray tube similar to the one Tektronix puts in its T4002 terminal. The T4005 will be able to overlay plots and magnify them up to 32 times.

The first of 64 semiconductor memory systems for Illiac 4 has been accepted by Burroughs from Fairchild. The University of Illinois' supercomputer, originally designed to execute up to a billion instructions per second, has been running into its share of troubles—like skyrocketing costs and the need to eliminate the medium-scale integrated circuits originally planned in favor of standard IC's [Electronics, April 14, 1969, p. 47]. As a result, in an attempt to recoup some of the lost space and speed, contractor Burroughs and the university decided to use semiconductor memories in place of the magnetic thin films initially called for.

The LSI memory system, which Fairchild calls a processor element memory (PEM), consists of four memory boards and a control board and has a total capacity of 131,072 bits. Illiac 4 will have a total memory capacity of 8,388,608 bits.

According to Fairchild, the memory is equal in cost to high-speed core systems—less than 10 cents per bit—and is three times faster—200 nanoseconds for a full operating cycle. The basic memory unit is a 256-bit bipolar chip called the 4100. Off-the-shelf delivery for the 4100 is expected this summer; the company is taking orders for the PEM for delivery in the third or fourth quarter of 1970.

Texas Instruments is having its ups and downs. Growth of its MOS activity has led the company to upgrade its MOS facility in Houston to a full-fledged department. But at the same time, TI has had production woes in Dallas, where diode-transistor-logic production has been cut back to four days a week.

TI will not comment officially about the cutback or talk about the problems that caused it. All that the company will say is that "less than 6% of the total Dallas-based personnel is on a four-day week."
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Path open to all-solid tv

Fairchild makes monolithic arrays of photodiodes or phototransistors on chip with scan circuitry

All-solid-state tv cameras—relying on flat arrays of light-sensing semiconductors to image a scene—have received much praise and high billing as the wave of the future. But the wave has been a long time building: no one yet has a solid state light-sensing array that is practical, and which doesn’t create more problems than it solves.

However, this may not be the case much longer, for Fairchild Camera & Instrument’s R&D division in Palo Alto, Calif., has developed techniques for fabricating monolithic arrays of photodiodes and photo transistors which fit, together with their scanning and accessing electronics, on a single chip of silicon.

Shorter tail. This ability to integrate light-sensing and scanning functions solves a major problem faced by solid state arrays: the signal and scanning wires connecting the light sensors to external circuits have been extremely bulky, resembling an impractically large tail wagging a very small dog.

Fairchild’s prize so far is a linear array of 48 junction photodiodes, each 5 mils apart, on a 60-by-30-mil chip of silicon, says the array’s designer, Gene P. Weckler. And the chip also contains a 48-bit dynamic shift register, built with silicon gate technology, and field effect transistors that act as accessing switches at each diode. As a pulse ripples through the shift register, the output from each bit turns on the FET’s in sequence, allowing the signal on diodes to be read out. The photodiodes operate in a charge-storage mode which sums the light falling on the junction over a period of time and enhances the junction’s sensitivity.

The result is an array that needs only six leads, regardless of the number of light-sensing elements, says Weckler. Five leads are on the input: two two-phase clock signals, and one each for the control pulse that starts the shift register, the pulse that signals the restart of the diode readout cycle, and a common power supply lead. And at the output, there’s a single video lead onto which the analog signals from the individual diodes are automatically multiplexed.

Better. The array is designed to operate at scanning frequencies between 10 kilohertz and 1 megahertz, says Weckler, and the array “has exceeded this operation at both ends of the range.” Weckler hopes to get 1-mil spacing between the diodes, and to build complete arrays with 100 to 200 detectors on a single chip. Plans are also under way to market the arrays outside Fairchild through the company’s Microaive and Optoelectronics division in Mountain View, Calif.

Waiting for the solid state integrated arrays with watering mouths has been another Fairchild division, Space and Defense Systems, in Syosset, N.Y. This division, which does much of Fairchild’s film and television camera work, has been developing the camera system concepts for capitalizing on the availability of such arrays, says Thomas J. Palamenghi, director of the advanced reconnaissance systems group. Such a camera would be small and rugged and operate at low semiconductor voltages. And the sensor array wouldn’t, of course, need the high-vacuum envelope or high-voltage electron scanning beam required by conventional tv tubes. Westinghouse is also developing such silicon solid state sensor arrays. However, it’s been concentrating more on developing dense arrays of photodetectors, rather than on combining the arrays with scanning electronics.

Right now, Palamenghi is decid-
ing which type of array—using photodiodes or phototransistors—to develop into a camera system. Transistors would give the camera sensitivity, he points out, but the diodes could be made to have more uniform response. However, it's possible to overcome the usual nonuniformities among the elements in a solid state array by storing a correction factor for each element in a small semiconductor memory. These factors are then applied as the photodetector signals are read out.

Making it. Linear photodetector arrays are quite suitable for reconnaissance applications, Palamenghi points out. They're also easier to fabricate than two-dimensional arrays. For example, a square array of 100 by 100 elements—which Fairchild has built—requires 10,000 perfect elements. This is much more difficult to accomplish than making the linear array's 100 to 200 elements. Longer arrays—up to 10,000 elements—could be made by putting perfect linear segments end to end, says the Fairchild group director.

Fairchild could have a flight version of a solid state camera ready in about a year and a half, says John Hunt, manager of the electro-optical components and equipment section in Syosset. "There's no reason now, except for straight engineering, why we can't make a variety of cameras," he says. The first one might be a panoramic type which uses a mirror to sweep the scene across the array.

Another important camera application would be in an earth resources satellite in which the linear array is swept over the earth scene by the motion of the satellite. Such a solid state camera would be excellent for taking multispectral pictures of the earth, Hunt continues, resulting in near-perfect registration of the different spectral images in a composite photograph. Such multispectral imaging is being done in the first earth resources satellites (ERTS-A and B) with three separate return-beam vidicon cameras [Electronics, May 12, 1969, p. 98].

Knowing less. Perfect registration could be a problem here because it depends on knowing precisely the position of the scanning electron beam in each of the tubes. With the solid state arrays, however, registration depends only on knowing the position of the individual detector elements. And this is known with extreme precision because of the photolithographic techniques used in the arrays' manufacture. Total number of lines per picture of a silicon array could exceed the nominal 4,500 achievable in present high-performance vacuum tv tubes, according to Hunt.

Hunt also sees the solid state camera being used in covert applications, in which a scene is illuminated by an invisible infrared source. Silicon has much greater sensitivity in the near infrared than conventional vidicons which operate largely in the visible light region.

Space electronics

Off the ground

Now that NASA has awarded its $50-million-plus contract for Application Technology Satellites F and G to General Electric, the 500-pound satellites can be moved out of the embryonic stage in which they floated during the two-year competitive design study. ATS-F is now scheduled to be fired into synchronous orbit, 22,300 miles up, by a Titan 3C in March 1973; ATS-G is to follow by 1975.

Both craft will have a whopping big antenna, three-axis stabilization, and a pointing accuracy of 0.1°. ATS-F will carry an extensive payload of communications and other experiments.

Some dish. ATS-F's most prominent feature will be a 30-foot parabolic dish, the largest antenna ever deployed in space, made by the Lockheed Missiles and Space Center. This antenna will be used despite GE's commitment, during the early phases of the design competition, to a petaline dish made by Goodyear. An internal NASA Goddard report praises Lockheed's design while criticizing the designs of Goodyear and Convair. (Losing bidder, Fairchild Hiller, was teamed with Convair before Lockheed came along.) The report notes that Convair's expandable-truss antenna, a complex structure of hinged equilateral triangles, can't be analyzed to determine the distorting effects of solar heating.

Such distortion can severely degrade antenna gain at X band, the frequency of one of ATS-F's communication experiments, by warping the reflector surface more than the specified limit of 0.05 inch from...
Dishing it out. ATS-F and G will feature 30-foot deployable reflector, largest ever in space. Three-axis stabilization, with beam-pointing accuracy of 0.1°, will depend on east-west solar sensors and a Polaris sensor. Telemetry and command links will be at 136.23/137.11 MHz and 154.2 MHz, respectively. The commandable gravity-gradient experiment, dropped from ATS-F, may reappear on the G version in 1975.

As the world watched

Apollo 13 Commander James A. Lovell probably summed it up best when, while struggling to bring his crippled spacecraft safely back to earth, he radioed mission control at Houston: "I'm afraid this is going to be the last moon mission for a long time."

The evaluations of just what happened on the aborted Apollo 13 flight are still going on. These, plus any rebuilding of all-but-completed spacecraft that result, could very well postpone the launch of Apollo 14 which is scheduled for next fall.

Almost immediately after the explosion late on April 13 of an oxygen tank in the Apollo 13 service module—which, it later turned out, had blown a 13-foot-long panel from the module and appeared to damage the flaring bell of the propulsion unit—electric power from two of the three hydrogen-oxygen fuel cells dropped to zero, and the power from the third dropped alarmingly. With electrical and oxygen supplies failing fast in the command module, the astronauts began shutting down systems in the command module and moving into the lunar module. And at mission control, the decision was made: terminate the mission and bring the Apollo spacecraft back to earth.

From several options, mission controllers decided to keep the Apollo on its course to the moon; then, as it swung around the moon, they could power it out of a lunar orbit and set in on a course back to earth. All depended on the electrical batteries and life-support systems in the lunar module. It had quickly become the astronauts' lifeboat while the command module had changed into a cold, dark inhospitable shell. Everything hinged on successful operation of the lunar module's guidance and navigation, and communications systems.

From the start, electrical power and oxygen supplies were low, and had to be carefully husbanded. All unnecessary electronics was shut off. The S-band communications system was operated at reduced power and even the AC Electronics inertial navigation system was eventually replaced by the lower power TRW-Hamilton Standard backup strapdown guidance system for the last two course corrections by the lunar module en-
gnine. Most of the time, the inertial systems were operated under reduced-power, standby conditions; they were on completely only when midcourse corrections had to be made.

There were five burns during the return flight. The first, occurring soon after it was decided to bring the ship back, returned Apollo 13 to a free-return trajectory to bring the ship back to earth. As the craft came around the moon, there was a second burn, one which simply added velocity and shortened the return by 10 hours. Then followed two more corrections—the trans-earth return trajectory under normal conditions allows for three—and then the final correction on the morning of splashdown, just before the command and service modules separated.

The guidance systems operated near perfectly, although the astronauts could not take star sightings with the alignment optical telescope in the lunar module. This could have been because the spacecraft's attitude prevented the astronauts from recognizing any familiar constellations, or because gaseous debris speeding along with the craft reflected sunlight that showed up as stars, confusing any attempt at taking sightings. Any updating that was done depended on lining up the telescope on the dark-light line along the circumferences of the earth and sun.

Also, early on splashdown day, the lunar module power began charging the recovery batteries in the command module. These batteries normally supply power during the re-entry phase, after the service module has been jettisoned. The inertial guidance system in the command module was also turned on and realigned with the system in the lunar module. Teams at AC Electronics and at the Massachusetts Institute of Technology had earlier, in fact, simulated what would happen in the spacecraft. They first cooled Apollo-type inertial measurement units down and then turned them on and they were certain the precision components in the platforms would operate satisfactorily. And they did.

Cabin temperature in the command module also began going up as the electronics was turned on. Interestingly, the temperature is usually maintained by heat generated in the electronics systems.

Manufacturing

Bang-up test

Nondestructive testing of aircraft sandwich structures is, at best, an art; no one technique is suitable for all honeycomb materials. Holography as well as static deflection of the test specimen, vibration, r-f heating, magnetic techniques, and thermal, vacuum, and pressure loads are used to detect flaws in the bonding of the honeycomb structure to the aircraft or spacecraft skin.

Scientists at the Hughes Research Laboratories in Malibu, Calif., believe they have a holographic technique that is unique. Compared with commercially used holographic honeycomb testing, the Hughes method is faster and, because it employs a ruby laser, doesn't require the massive stable table usually needed to hold the helium-neon lasers rigid.

Ready to go. George Smith, a vice president and director of the research labs, believes the Hughes method is ready to graduate from lab status; it could be used to check bonding integrity of the main support structures of the communications satellite built by the firm's Space Systems division with honeycomb material.

Donald Close, a member of the technical staff at the labs, says the technique combines the use of a single-mode giant pulse (10 joules of available light) ruby laser to make the holograms, "impulse loading" of the test specimen to stress it during the hologram exposures, and a series of five to seven holograms all made in 1 millisecond. What's impulse loading? It's the way Hughes researchers describe their method to excite or stress the honeycomb panel—they hit it with a hammer. The hammer, though, while imparting a mechanical rap to start the specimen ringing, also incorporates a Microswitch to trigger the system timing.

About a millisecond after the hammer tap, the laser fires to make a conventional two-beam hologram of the test specimen mounted a short distance from it. Each exposure requires about 30 nanoseconds. The second exposure is made 50 microseconds after the first, and so on until all the holograms have been made.

Loops. The spots on the test panel skin under which there is a poor bond—or where there's no bond of the honeycomb structure indicate unbonded areas appear as knots in a piece of wood in this combination of several holograms of the excited panel.
Why National Semiconductor buys Teradyne J259's by the dozen

National Semiconductor can trace its considerable success as an IC manufacturer to many factors. One of the most important is the productivity of its testing facility, built around a lineup of 12 Teradyne J259 computer-operated test systems. "The Teradyne systems," according to Jeff Kalb, National's TTL product manager, "give us the economy of testing that is so important to profitable high-volume production."

National, along with most other major IC producers, has found that the J259 boosts productivity in many ways. No other test system, for example, gives its user as much multiplexing freedom as does the J259, which lets National leverage its investment by making each J259 support several test stations doing several different jobs.

Reliability is another all-important key to productivity. National experiences minimal downtime with its J259's. This is as it should be; we design and build our equipment to work shift after shift, year after year, in industrial use. Teradyne systems are right at home on production lines like National's, where the workload is heavy and continuous. And operation never has to be interrupted for calibration; the J259 has no calibration adjustments.

The J259's great versatility is also put to good use at National. The same systems that test wafers and packages also generate the distribution and end-of-life data that engineers need to control production processes and ensure high device reliability. Production, engineering, QC, and final test—all share simultaneously in the benefits from National's J259's.

A computer-operated system is only as good as its software, which in the case of the J259 is the best there is. National's J259's are orchestrated by Teradyne-supplied master operating programs for datalogging, classification, and evaluation. As Teradyne updates and improves its software, National is kept fully informed.

National's array of J259's handle the testing of its digital IC's smoothly and economically. For its linear-IC testing, National has turned to Teradyne's J263 computer-operated linear-IC test system.

Teradyne's J259 makes sense to National Semiconductor. If you're in the business of testing circuits—integrated or otherwise—it makes sense to find out more about the J259. Just use reader service card or write to Teradyne, 183 Essex St., Boston, Mass.

Teradyne makes sense.
to the skin—appear in the multiple-exposure hologram as fringe patterns that resemble knots in a piece of lumber. Actually, says Close, the holograms can be considered a velocity map of the test panel because no-bond areas resonate faster than good-bond areas.

Hughes takes multiple holographic exposures on one plate because not all the fringe patterns show up on a single hologram, and not all the bad-bond areas would be detected. But by making several holograms with the panel in different states of excitation, combining them on one plate, and then playing them back, all the bonding flaws are revealed.

Alex Jacobson, head of the labs’ Unconventional Imaging section, says commercial holographic non-destructive testing using helium-neon lasers require the test specimen to be kept extremely still while holograms are made. Because these are low-power lasers, the exposure takes several seconds vs. Hughes’ 1 m sec to make several exposures. And with the commercial techniques, two holograms are normally made—one with the specimen unstressed and one with some kind of stress that doesn’t cause the specimen to move, such as thermal or magnetic stress. But several two-hologram series may be required.

Tap-tap. Jacobson looks for the Hughes technique to replace ultrasonic and “coin-tapping” methods.

The Hughes way is subject to one limitation researchers hope to change. Jacobson says a minimum of five minutes is required to develop the silver-halide holographic plate. A new photopolymer developed at the labs could provide a film that can be instantly developed, but it hasn’t been packaged for commercial use yet.

Oceanography
Keeping afloat

Most engineers working on federal programs these days know of the paucity of new business. But just how tight it’s really getting was graphically demonstrated at an industry briefing in mid-April at which the Coast Guard was to spell out its needs for the National Data Buoy development project: nearly 700 industry representatives turned out.

And this for a program that got its first real money in January—a modest $6.5 million for fiscal 1970. For a project with an annual budget that wouldn’t even buy one F-111 aircraft, the questioning and interest was intense from attendees who represented not only smaller companies but many of the aerospace giants. The battle for business was brought home to one Coast Guard officer in the buoy program who noted his project office recently received an amazing 24 proposals for one $70,000 study contract.

Cash flow. While the program doesn’t look as though it will fulfill the optimistic forecast made in late 1967 that a 10-year effort would run an estimated $500 million to $1 billion, the Coast Guard now plans on program costs through fiscal 1977 of $137 million. Prototype hardware and modifications of support equipment would account for about $67 million. Of course, all this depends on Congress’ continuing to appropriate the funds for the buoys.

Money has been, and will continue to be, the pacing factor. One Coast Guard officer admits to “fooling around” for the last two years because “we didn’t have the money.” Even now, with the formal eight-year, long-range-development plan and concept formulation completed, the Coast Guard could accelerate the program if it gets the cash.

The Coast Guard got in the act early in 1968 after the Interagency Committee on Oceanography and others concluded there was an ordinarily large number of independent efforts under way all aimed at the same thing—developing unmanned buoys to collect marine environmental data. So the Coast Guard took on the role of lead agency, its first project management job. And it’s proceeding carefully and methodically, to say the least, because, as one Coast Guard officer puts it, “If we don’t do a good job on this one, we’ll never get another chance.” The service is looking past the buoys.

Afloat in ‘80’s. Under current plans, the development project will continue through the 1970’s, with deployment of the operational system slated to begin no earlier than 1980. The work breaks down into three parts: advanced development through 1974, engineering development through 1979, and operational system procurement and operation beginning in 1979.

The advanced development phase includes the design, development, and evaluation of a so-called low-capability buoy which will make only a limited amount of meteorological measurements and which might be designed to be air dropped. The design of these buoys is to be completed in fiscal 1971, and procurement and fabrication of about 10 moored and 20 drifting prototypes will begin in fiscal 1972. In 1974, the design of a prototype high-capability buoy will begin. And about 35 prototypes will be built with deployment of this pilot buoy network scheduled for
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Sea monster
The Coast Guard, as part of its National Data Buoy development project, started operating on February 1 the first environmental reporting buoy off the coast of Norfolk, Va. For this experiment, it borrowed a 100-ton, 40-foot discus buoy developed and built for the Office of Naval Research by General Dynamics' Convair division.

The "monster buoy" was refurbished as an automatic weather station and is presently sending back data every six hours on air and water temperature, barometric pressure, wind velocity, solar radiation, rainfall, dew point, surface current velocity, and average wave height. The Coast Guard, however, is having its troubles with sensor failures in such areas as the dew point unit and the underwater instruments.

The data link from the buoy is via high frequency to the Coast Guard's Miami radio. Presently controlling the automatic station is a Navy computer at La Jolla, Calif., but the Coast Guard plans shortly to install a small general-purpose computer to control the buoy at Miami.

the summer of 1977. The Coast Guard, for planning purposes, is estimating an operational system of 150 buoys.

Industry participation to date has been minimal. Sperry Systems' Management division is the systems support contractor and Texas Instruments is presently surveying oceanographic and meteorological sensors. These contracts carry hardware exclusion clauses, which means that the companies can't bid on hardware until two years after contract work is completed, or July 1972.

Start. But the first big job is coming this summer. In July or August, the Coast Guard expects to issue requests for proposals for two prime contracts on a 10-buoy system, says Cdr. John E. Wesler, deputy manager for the buoy project. One is for a system integrator responsible for the buoy mooring and telemetry, and the other is for a prime contractor to manage the sensors. These buoys will be deployed in the northern part of the Gulf of Mexico in the summer of 1971 as a testbed to evaluate existing technology and provide experience in deploying and maintaining a network of automatic data buoys. The buoys will be outfitted with environmental sensors specified to correct the most common causes of operational failure.

Sensor reliability, particularly in underwater oceanographic instruments, has long plagued the Navy. "We know the weakest part of our system is sensors," says Wesler. So the Coast Guard in this initial network wants to get the best possible buoy and mooring to eliminate the variables in evaluating sensors. One reason oceanographic sensors have been so deficient, Wesler says, is there has not been enough demand for such instruments. There are only two small companies really in this business, he noted, and an order for 10 sensors at a time is big business. "We hope by putting money into sensors that we can make drastic improvements in the technology."

Random failures in sensors will be a problem in meeting the system objective of once-a-year servicing. The Coast Guard figures that a mean-time-between-failure of more than 15 years will be needed to meet high-capability buoy performance goals. Just how tough a job this will be can be judged from the mtbf of existing sensors, which is typically on the order of a few months, the Coast Guard estimates. What this means is that the emphasis will not be on the development of new sensing techniques or advancing state-of-the-art accuracies, but developments of sensor reliability and calibration stability, according to Wesler.

Computer. One, possibly two, general-purpose digital computers will be needed on the big buoy, and the Coast Guard is counting on large scale integrated circuits to improve reliability and reduce computer size and power demand. It is thinking about a memory capacity of at least 8,192 words of 13 bits each and an add time of 32,000 times a second. State-of-the-art digital magnetic tape records can meet the requirement of accepting 140,000 bits of data for each synoptic measurement period and total storage of 700 million bits, but more than one tape transport may be needed for each buoy. Again, existing hardware can meet data handling requirements, but reliability is the problem, Wesler says.

The external data link, which will transmit environmental and buoy status data to shore will also need receiving equipment to handle command instructions from shore bases. The two modes of communications that will be used are high-frequency ionospheric propagation, and line-of-sight via satellite relay at vhf or uhf.

All satellite. The data buoy system will eventually have to use satellite relay exclusively because of only 90% reliability of over-the-horizon b-f transmission. This one factor alone consumes half of the 20% data loss allowed in the system performance goal, placing perhaps an impossible reliability burden on the system hardware. The goal for the system is to get at least 80% of the environmental data to the user during at least half of the observation periods.

The Coast Guard hopes to be able to tie into an Environmental Science Services Administration satellite in about 1973. Another satellite that could be used in the aeronautical services satellite in the planning stage for an early 1970's launch.

Antennas
Dishing it out
There's a way to make a higher resolution radar out of a system with only a moderate sized aperture, and scientists at the Air Force Cambridge Research Laboratories are working to improve it.

According to Allen C. Schell, of the center's microwave physics laboratory, a computer-based technique being developed there can often yield a 50% improvement in angular target resolution, and sometimes even up to 100%, a definite plus in situations like those around
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crowded airports where today's broad-beamed FAA radars lack the target discrimination many feel is necessary. Schell also feels the technique could be adapted to radar systems forced to use small diameter antennas like those in aircraft—in effect, simulating a larger dish than would physically fit the plane's nose.

Spots sun. So far, though work on the signal-processing technique has been going for about 18 months, demonstrations have been limited to radiometric work. Using the laboratory's 28-foot antenna in Waltham, Mass., Schell has scanned the surface of the sun at 8-millimeter wavelengths and processed the resulting data in the labs' computer complex.

To see how much improvement in resolution the technique added, Schell arranged for comparison a simultaneous scan by the 120-foot haystack antenna of Lincoln Laboratory. Using this as a standard, Schell was able to arrive at the 50% to 100% improvement figure.

But scanning the sun is a passive application, and the data processing so far isn't done in the real time radar would need—an x-y plot of the sun takes about 45 minutes to compute, according to Schell. Also, the results aren't totally dependable yet; the scheme may be resolving things that aren't really there, he says.

With this in mind Schell is now debugging the program, and in a couple of months he feels that he'll have a much better gauge of the scheme's true performance.

Two parts. The software involves two major phases: first, the spectral components of the source (like the sun) or target (in radar applications) must be restored. The antenna, because its pattern isn't a single perfect beam but rather is divided into lobes and nulls, always gives an incomplete picture of what it views; so by melding the incoming data together with the antenna pattern it's possible to flesh out the picture. The second step is to retrieve and sharpen detail through iterative solution of a complex integral equation.

The accuracy of the technique is only as good as the accuracy of the antenna pattern; it's here, Schell feels, that some of the suspected errors in the enhancement process may lie. "This technique is only as good as the accuracy of the antenna pattern measurement," he says, "and at Prospect Hill, where the labs' antenna is located, the antenna pattern was measured with the dish pointed at the horizon, while most of our data was taken nearer the sun's zenith. Consequently, gravity probably deformed the dish subtly and so changed the pattern."

But for small dishes whose patterns are easy to gauge, or for larger existing dishes whose characteristics can be measured very accurately—like antennas at major air traffic control centers—the technique could be made to work well.

Heads up. After sharpening up the computer program, Schell hopes to be able to encourage development of a relatively small analog computer module—"one small enough to fit a plane"—which could raise the performance of radar systems with low angular resolution to levels associated with larger dishes.

Government

Penny wise . . .

Without fanfare, and almost without informing the men involved, the Air Force is about to dismantle the Office of Aerospace Research (OAR). Many of its high ranking officials already may quit in disgust.

OAR oversees the activities of the 1,200 personnel at the Air Force Cambridge Research Laboratory, and Hanscom Field, Mass.; the Aerospace Research Laboratory at Wright Patterson AFB, Ohio, with an estimated 300 to 400; the Office of Scientific Research in Washington with about 150 men; and the office of research analysis in New Mexico with its 60-man staff.

The Air Force appears to be disbanding the headquarters organization of OAR, and directing its laboratories to report to the Air Force Systems Command's director of
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Communications

Turning MCI on

The communications industry and its customers are watching closely as Microwave Communications Inc. moves closer to its September 11 start on the first special service common carrier linking Chicago and St. Louis. MCI president John D. Goeken has signed up the Raytheon Co. for the $3.5 million turnkey installation that will provide the equivalent of 1,800 channels of 4 kilohertz each with 1,600 channel ends. Customers being lined up for the initial installation, says MCI, range from the Continental Can Co. and the Boise Cascade Corp., with Continental Can ready to take the equivalent of 80 voice channels, down through Famous Barr, a time-sharing-service company that will require "only a couple of low-speed data channels."

Between those extremes are organizations like Holiday Inns, MCI says, which will link its Memphis, Tenn., headquarters via the Bell System with MCI's St. Louis terminal on the Laclede Gas Co. building. It then will interconnect with the Chicago terminal being readied at the new John Hancock Tower with its antenna 1,450 feet above street level.

Who does what. Raytheon, says MCI, has subcontracted much of the system, with Canada's Northern Electric providing multiplex equipment; the Rohn Manufacturing Co. is engineering the 12 towers in the loop, while Prodelin is supplying the antennas. Raytheon itself is providing the 20 KTR-3 transmitters, including the two needed for each terminus.

In evaluating suppliers, MCI's Goeken says that ITT's hardware provided the lowest error rates but the fact that it was produced by the company's West German operation raised a question about ability to meet delivery schedules. Raytheon came in next and got the job, while Lenkurt Electric ranked third and Collins Radio last. However, all of the four FCC-approved systems ranked close, and the parent firm, Microwave Communications of America, notes that "we don't..."
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U.S. Reports

expect to lock in with one manufacturer” as it moves to build the more than 12 regional systems planned.

For example, ITT equipment is specified in the filing of Microwave Communications of America’s affiliate, Interdata Communications, for a New York-Washington route. Lenkurt gear is specified in two new FCC filings by MCI Mid-Atlantic Communications, running from Washington to Atlanta, and MCI Kentucky Central, linking Atlanta, Chattanooga, and Nashville with major Kentucky cities with an eastern terminus at Cincinnati and a western hookup at St. Louis. Raytheon systems are also specified for the newly filed 64-site system of MCI Texas-Pacific, tying Dallas to Los Angeles at a cost of more than $8 million. Raytheon is also named in filings of MCI Michigan and MCI New England.

For the record

Pocket model. Japan’s Canon Inc. and Texas Instruments have built a calculator that fits into a slim case not much larger than a paperback book. Called Pocketronic, the mini-calculator adds, subtracts, multiplies, and divides: it can also perform repeated multiplications and divisions with a common factor, and its results appear as hard copy printed on a strip of paper. Canon and TI designed the machine around three MOS circuits that contain almost all the machine’s circuitry. Not included in the three chips are the system clock, the paper advance controls, the printing mechanism, and the 10-key keyboard. The printing mechanism is based on the thermal printer TI introduced last year [Electronics, May 12, 1969, p. 178], and the paper tape, made by the 3M Corp., comes in a cassette. Marketing of the calculator will start in Japan next fall and in the U.S. early in 1971. Price: under $400.

Satellite spooers. Sen. Warren G. Magnuson (D., Wash.) is introducing legislation to set up a World Environmental Institute to foster cooperation among nations on both
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U.S. Reports

sides of the Iron Curtain in fighting pollution. Announcing his plans at the International Geoscience Electronics Symposium in Washington earlier this month, Magnuson touched off speculation that the Earth Resources Technology Satellite program might be used in such an international effort. In addition to imaging the earth through remote sensors, the ERTS satellites will be able to interrogate ground-based data buoys and measuring stations. These stations could, for example, be placed to detect pollutants flowing into rivers and streams from industrial plants anywhere in the world. NASA has been trying to acquaint foreign governments with this nation's earth resources survey program working with individual governments, such as Brazil and Mexico, and through the United Nations Outer Space Committee.

Payoff. An in-house R&D program by the Hoffman Electronics Corp. to develop a high-accuracy, solid state, coherent-pulse doppler radar altimeter has paid off with a $760,000 initial contract award from Lockheed California for development and production of prototype quantities. The instrument is to be used in the Navy's S-3A antisubmarine aircraft. Although the initial contract is small, company officials see several million dollars in potential business from the altimeter, and predict that it will develop into very significant business over the next four or five years.

A watched clock. The Bulova Watch Co. has developed and introduced its version of the quartz crystal wristwatch, and although it's the third to do so, behind Seiko and Longines [Electronics, Jan. 19, p. 66 and Sept. 29, 1969, p. 10E], the company claims to be the first of the three to retail them in the U.S. (the first one was sold in New York City earlier this month). The watch is not, however, aimed at a mass market. In the first two men's styles it sells for $975 and $1,325. And for the girls, there's a special diamond brooch model for only $50,000.
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It’s a complete read-write memory system on a chip: 256-bit storage, read-write control logic, sense amplifier, and chip-select logic... with speed and total installed cost comparable to a core memory. And it needs no peripheral electronics.

Each bit is a bistable flip-flop that permits nondestructive readout. But pattern is 256 1-bit words—expandable to 4096 bits by stacking. Access time? Typically 1 μs.

CIRCLE 221 ON READER SERVICE CARD

TRIPLE-66 BIT...another MOS shift register from Philco-Ford.

This newest Philco® dynamic shift register is designed to keep your package count down. By using a common power supply, common clock, and common ground, we also minimize the number of wired connections.

This “3-in-1” shift register stores 64 bits of information plus 2 bits of parity on each channel. Shift frequency ranges from 10KHz to 1MHz. Input logic levels are —3 volts for a "0"; —9 volts for a "1." Output capability of each register is 2 ma load current at —3 volts.

Try the pL5R198C for applications like temporary storage, digital rate conversion, precision delays, signal processing correlation.

CIRCLE 222 ON READER SERVICE CARD
We make an impressive array of silicon photodetectors.

With field-proven performance that's based on 15 years' experience in silicon technology. It all adds up to a broad capability in custom photodiodes, monolithic linear arrays and mosaics—n on p (including guard ring) and p on n structures.

Sophisticated application? We make things like star tracking detectors and sun sensors, high-speed laser detectors and photomixers, flash and explosion detectors, optical fuzes, missile guidance devices, and nuclear radiation monitors.

Want facts and figures? Philco® visible and near-infrared photo devices have specs like these: spectral range of 0.3 to 1.1 microns; responsivities to 0.60 amp/watt @ 0.9 micron; leakage currents as low as 10^-18 amp; detectivities > 10^12 cm-Hz^1/2/watt; linear arrays of over 150 elements, each as small as 0.001" square; and array cross talk < 0.01%.

Interested in two-color detection? We combine silicon with InSb to achieve visible and far-infrared detection.

New, high-bit capacity static MOS ROMs:

You can get Philco® 2048-bit static ROMs custom-made for your application in just 6 weeks. For microprogramming, look-up tables, code conversion, control logic...you name it.

Select the bit organization that fits your needs: 8-bit, 4-bit, 2-bit, or 1-bit words. Need a large memory? Just stack these ROMs for any bit capacity, in multiples of 2048.

Because the pMS2048C is a static device, output data remains valid as long as an address is present...and the output is compatible with both bipolar and MOS circuits.

Some Philco static ROMs are available off the shelf. The pMS2240C, for example, is a 2240-bit MOS device configured as a character generator. It's preprogrammed and, when addressed by the standard ASCII code, generates 64 alphanumeric display symbols...to create all the characters on a conventional teletypewriter. Access time for first-row bits is 1 µs, 0.7 µs for successive bits.

Broadband mixer-detectors...with new mod modules.

Our P5100 Series mixer-detector assemblies are state-of-the-art in the true sense. They're designed with highly efficient Schottky-barrier or backward diode chips in integrated circuit modules for broadband performance from uhf to Ku band.

How? The hermetically sealed module eliminates the package parasitics which limit the broadband performance of conventional semiconductor diodes.

You get performance characteristics like these: 7 db noise figure in mixer applications; 12 db noise figure in Doppler mixers; -55 dbm sensitivity without a requirement for external bias in detector applications.
Does your tester test comparators and op amps?

Does your tester test with single card programming?

Is your tester simple to operate and fast?

If not, you should be using our Model 1420 tester. This stout-hearted tester takes ICs through fourteen rigorous tests* as fast as blink, blink, blink. No knobs to fiddle, no dials to twiddle. In fact, it’s so easy to operate even your secretary can use it.

And it’s the most complete testing system available, with a digital readout meter to indicate to what degree each IC passes or fails. Plus, all kinds of options like classification, data logging and an accuracy rating from 1% to 5%, your choice.

That’s why every major op amp manufacturer owns our 1420 or the 1410.

Check out our low priced line by writing for specs and addendum. Better yet, call marketing, Signetics Measurement Data Division, (415) 961-9384. And see the line for yourself.

Yes, we make house calls.

*The fearsome fourteen... 1) power consumption overrange (greater than 200%), 2) power consumption (less than 200%), 3) offset voltage (source resistance zero ohms), 4) offset voltage (source resistance programmed), 5) + supply sensitivity, 6) - supply sensitivity, 7) common mode rejection, 8) bias current, 9) offset current, 10) gain (programmed full load), 11) gain (programmed light load), 12) noise, oscillation, 13) + slew rate, 14) - slew rate.
Time interval measurements like you've never seen before.

Plug the HP 5379A Time Interval Unit into the HP 5360A Computing Counter and things happen faster than they could ever happen before.

This unique combination resolves time intervals to 100 picoseconds. With absolute accuracy of 1 nanosecond. And you can measure zero, positive and negative intervals, which makes it ideal for checking coincidence.

By adding the HP 5375A Keyboard, you can enter programs to produce instantaneous answers about phenomena that you could never previously measure with a counter. Things like peak-to-peak jitter; rms jitter; phase measurements; duty cycle; and radar ranging in feet, inches, meters or any other units you want.

Thanks to the 5379A, dozens of jobs can be handled easily and accurately. These include calibration of radar, lasers, and laboratory instruments; testing semiconductors and computers; cable fault location; delay line adjustment; ballistic and nuclear measurements.

It does all this while saving you the cost of a computer, because the computer's built in. And you're not buying a counter that only measures time interval. You're getting the most advanced frequency measuring system available today, ideal for measuring pulses, pulse trains or any time-based events.

The cost is $750 for the plug-in and $6500 for the mainframe. Your local HP field engineer will be glad to arrange a demonstration. Give him a call. Or write for complete information to Hewlett-Packard, Palo Alto, California 94304; Europe: 1217 Meyrin-Geneva, Switzerland.

HEWLETT PACKARD
ELECTRONIC COUNTERS
When Sperry Rand's PACT (Progress in Advanced Circuit Technology) program addressed itself to radar altimeters, the objective was a receiver module on a single substrate. The objective has been achieved.

Sperry has moved out of the lab and is ready to move into the airplane with a receiver for the 4300 MHz band. It incorporates microstrip technology into a module that includes an oscillator, a switch, a mixer and a filter. The complete receiver function is packaged on a single 3" x 3" x .055" substrate.

The PACT technical development provides substantial improvement in both performance and reliability. The oscillator provides an extremely clean, low-noise input to the mixer. The oscillator-mixer combination has a double sideband noise figure of only 4 db, referenced to a 1.5 db preamplifier. Temperature range is −55 to +100°C.

Reliability is greatly improved, of course, by the module's integrated, all-solid-state construction. Sperry's design replaces the customary triode oscillator and its associated reliability problems. Many other discrete components and connectors required by conventional designs have been eliminated. The new module is designed to operate under the shock, vibration and temperature conditions of the airborne environment.

The altimeter receiver again demonstrates Sperry leadership in microwave integrated modules. We would like to apply our capability to your system challenge. To help start our conversation, write or call Sperry Microwave Electronics Division, Sperry Rand Corporation, P. O. Box 4648, Clearwater, Florida 33518. (Phone 813-855-4471).
German government urged to counter Japanese invasion

Japanese competition is beginning to worry the West German electronics industry. In a strong statement, the country's Central Association for the Electrotechnical Industry urges the Bonn government to take measures giving German firms a better competitive edge against the Japanese challenge at home and on world markets.

Among the measures proposed are easing of credit policies for private firms investing abroad, government underwriting of long-term investments, and adapting of national financing and sales conditions to international practices. The association also wants the government to direct, or to participate in, foreign-market research and to disseminate its findings to the industry, as the Japanese government is said to be doing.

Voices of concern are also heard from company officials themselves. They point out that in 1968, the latest year for which official figures are available, imports from Japan were nearly twice as high as West German deliveries there—$50 million vs. $26 million. More than 60% of Japanese goods imported were for consumer electronic products, and the rest were for desk calculators, measuring equipment, controls and small tools.

This development is seen as a result of Japanese attempts to become less dependent on U.S. markets alone and to build, as one official puts it, "a strong second leg in Western Europe."

Automatic testing spurs dvm battle

Look for intensifying competition in England between sellers of programable digital voltmeters. At the Instruments, Electronics and Automation Exhibition in London next month, two established instrument companies will announce their entry into the field, and at least two of the existing half-dozen sellers will show additions to their lines.

Most important new entrant is Racal Instruments Ltd., leader in the counter/timer field, with a range of seven new fully programable dual-ramp integrating instruments offering accuracies from 0.1% to 0.01% and priced from $850 to $1,200. The other new competitor is G. and E. Bradley Ltd., calibration instrument specialists, with a single partially programable dvm priced at $820. In addition, Solartron, which currently has the biggest slice of the market, and the EMI subsidiary SE Laboratories Ltd. will introduce new high-precision fully programable units.

The basic reason for the push is the trend toward instruments that can be incorporated in automatic checking systems. Plus that, the market for nonprogramable instruments, which can't be automatically controlled, is considered static at best. Because almost all autocheck systems include a dvm, this is where competition will be most intense.

France seeks to sell computers to Cuba

Fidel Castro may plan his sugar production on French computers. France's state-backed computer maker, Compagnie Internationale pour l'Informatique, confirmed that it is negotiating the sale of "one or two" Iris 50 machines, equivalent to the IBM 360-50, to a Cuban planning agency. Press reports that Castro might take five or more machines are untrue, says CII export sales manager Henri Peyroles. He adds, "friendly foreign powers"—meaning the U.S.—were consulted and don't object.

So far, CII has won six export orders for Iris 50, backbone of France's Plan Calcul. Rumania has ordered five machines and the sixth is going to CMM, a private service bureau in Milan.

Electronics | April 27, 1970
Head-up display aimed at Concorde uses paired CRT's

Elliott Flight Automation Ltd. hopes to initiate head-up instrument displays in civil aircraft. The company has developed a projector unit about the size of a portable typewriter, containing two 1.7-inch cathode-ray tubes, that attaches to the cockpit roof above the pilot's head. It projects a binocular image onto an integral retractable reflector normally about 7 inches in front of the pilot's eyes. Elliott's main sales prospects will be airlines which will operate the Concorde. Because the Concorde has to be landed in an extreme nose-up attitude, a head-up display would ease the pilot's job.

Military monocular head-up displays, which put the image some distance from the pilot are well developed, but not suitable for civil aircraft because of their bulk. If they are scaled down, their image is too small to see. So far, attempts to provide a large image for the civil pilot have centered around reflectors in contact with the pilot's face, an uncomfortable setup. Elliott's system, in which the two side-by-side images projected by the CRT's appear as one, is claimed to solve the size problem and also permit the pilot to move his head from side to side without losing the image. Elliott will start flight trials in a Comet jet transport later this year.

Solar cells to power in-the-air blimp...

A giant solar-powered blimp to serve as a "space platform" for research and telecommunications is under study by France's space agency. The upper surface of the 900-foot dirigible would be covered from stern to stern with sheets of thin-film cadmium-telluride solar cells, developed under a government contract by RTC la Radiotechnique-Compelec, French subsidiary of Philips' Gloeilampenfabrieken [Electronics, Nov. 10, 1969, p. 238]. Standard silicon cells would be too heavy, and RTC engineers figure they will even have to slice in half the 20-micron-thick molybdenum film base on which they deposit CdTe.

Stationed 70,000 feet up, the dirigible would photograph the universe from beyond the earth's atmospheric turbulence, study weather and, possibly, control air traffic and transmit telecommunications. It could be either remotely controlled from the ground or manned by a crew of five to 10 men.

French scientists caution that the idea is only in the brainstorming stage. The space agency is now weighing whether to do a full-scale cost and feasibility study. If they decided to build the blimp, the French would likely seek foreign partners to share costs.

... and recharge down-to-earth radios and meters

Philips, meanwhile, is designing a transistor radio for the consumer market using CdTe solar cells to keep its battery charged. Technical details are secret, but insiders say the Dutch firm has definite plans to market the radio next year.

Three French electronics firms—Schneider Radio-Television, Compagnie des Compteurs, and CRC—are reportedly designing CdTe-powered voltmeters and other measuring instruments. Ambient room light would be enough to keep instrument batteries charged, avoiding the minute changes in a battery's power curve that can falsify precise measurements.

RTC, the world's only CdTe solar-cell producer, is offering cells for 18 cents a square centimeter and predicts greater production will push the price down to 5 cents within a year.
Standard items form troubleshooter for electronic systems on British jets

Airplane builders install electronic systems from many different suppliers. The individual differences in each system force the airline to carry a correspondingly wide—and expensive—range of specialized test gear for troubleshooting.

One way out is to buy specially built comprehensive-test systems such as Hawker Siddeley’s Trace, which means test equipment for rapid automatic checkout and evaluation. Both Boeing and Pan Am have bought Trace equipment, as have other airlines. However, because Trace is specially built, it is expensive, restricted to its predetermined function, and, in its most comprehensive form, needs a computer to control it.

Now British Overseas Airways Corp. and Marconi Instruments Ltd. have gotten together and devised a comprehensive troubleshooter based on the Autotest programable production-checkout gear marketed by MI for the past two years. For a basic cost in the region of $120,000, BOAC has a machine that will pinpoint faults in over 130 circuit boards used in the non-radio-frequency electronics systems in its new Boeing 747 jumbo jets.

That covers more than 80% of the boards in the central air-data computer, the autothrottle computer, the yaw-damper computer, the inertial-navigation system, the passenger environmental-control system, and many minor systems.

BOAC says the system shows a big cost saving over any alternative way of doing the job. What’s more, the airline has been able to specify exactly the generating and measuring instruments incorporated. And they all have a switch-over to manual control, so the instruments can be used for more than just their programed function, which increase utilization.

Basically, the system BOAC is buying is a set of standard, programable generating and measuring instruments which carry out an automatic sequence of measurements on components under central control from a punched tape. The results are printed out in sequence as an absolute value, plus an indication of whether the value is within acceptable limits, too high or too low.

In the aircraft tests set-up, the instruments include digital voltmeter, phasemeter, counter/timer for pulse measurement, and ohms/a-c converter for resistance measurement, plus wave and signal generators. The instruments come from a variety of sources, according to BOAC’s requirement. They are contained in two 6-foot and two 3-foot 19-inch racks, together with 400-hertz a-c power supplies, d-c power supplies, switching logic, reed-relay switching units, interfaces, and the printer.

On tape. The controller is the standard unit, costing about $14,500, used in production-checkout test applications. The program instructions in standard eight-hole format, are keypunched for each test. The tape code identifies the instruments required, the settings required, the top and bottom limits acceptable, and timing instructions, such as the necessary delays while the instruments settle. The final instruction in a sequence is “digitize”; when the tape is running through the controller, this command starts the test which has just been spelled out. The bottom half of the control unit contains solid state buffer memory, which stores the serial instructions from the tape and holds them until the test is complete.

In BOAC’s application, the equip...
ment will diagnose faults in circuit boards which have already been removed from their black-boxes and labelled "suspect." The board plugs into a slot in a jig which fits on top of the two smaller racks. To keep the jig to a manageable size, the 130 boards are grouped into 11 families. Between jig body and interface block there is a 470-way connector. For any test, all the connections are made, but only some are active. Similarly, a particular board will utilize only some of the paths between this big connector and the slot in the jig. All the signal inputs to the board and outputs to the measuring instruments pass through these connectors. Because the system is modular, it has considerable potential for expansion. And although it has not yet been delivered, the airline is already working on ways to include more tests.

Japan

Bumpy road to power

A flip-chip transistor with 3/4-watt dissipation is claimed by its developer, Sanken Electric Co., to have the highest power rating of such devices now on the market. The power transistor is one of a line of four flip-chip transistors developed for consumer applications, especially for the hybrid thick-film IC's increasingly being used. The transistors have been designed so they can be sold at prices compatible with this type of application yet have excellent characteristics in audio and other applications.

Sanken's power transistor has the high maximum collector-current rating of 0.5 ampere and good current-amplification-factor linearity. The other three transistors are small signal transistors; two types with power dissipations of 250 and 100 milliwatts, one npn type with a rating of 200 mw.

The specified 3/4-watt rating applies when four solder bumps on the chip are bonded to an alumina substrate measuring 35 by 25 by 0.8 millimeters. The ratings are not the maximum possible for the transistors, though. A coating of heat-conductive material, such as thermal-conductive epoxy, can be applied to increase heat transfer from the back of the chip to the substrate. This trick can raise dissipation of the transistor to about 3 watts.

Strata. The transistors are protected from the environment by two passivation layers. The first layer is thermal silicon dioxide, measuring about 0.7 microns thick, grown during the diffusion processes. The second layer is silicon dioxide, about 2 microns thick, sputtered onto the transistors after metalization. The sputtered silicon dioxide is physically different from the thermal material and does an excellent job of protecting the transistors, according to Sanken engineers.

Solder bumps are fabricated on aluminum metalization in holes etched in the sputtered silicon dioxide. The aluminum is prepared for the solder by deposited coatings of chromium and copper, and a plated coating of copper. Sanken says that a proprietary process enables it to make all four bumps the same height for trouble-free bonding. Transistors can be easily bonded to the silver palladium wiring that's widely used in thick-film hybrid circuits.

When ordinary transistors are used in hybrid IC's, it is necessary to perform at least four bonding operations per transistor—at least two wires with two ends each. Replacing the precision wire-bonding operations with the one-shot solder-bonding operation both decreases the need for labor and increases the reliability of the finished circuits, Sanken men say.

60 Mhz by cable

To prepare Japan for an era of more talk, pictures and data, the telephone company there has just begun field testing a 60-megahertz coaxial cable transmission system. The C-60M system, developed by the Electrical Communications Laboratory of the Nippon Telegraph and Telephone Public Corp., can transmit 12 super-master groups of 900 voice channels each, or a total of 10,800 voice channels.

Coaxial cables are now made with up to 12 coaxial pairs and production of cables with 18 coaxial pairs is contemplated in the near future. Two coaxial pairs are needed for one system, because a separate coaxial pair is needed for each direction. Thus, a single 12-pair cable can accommodate six coaxial systems, or 64,800 voice channels.

Color, too. One color television channel can be substituted for two super-master groups, enabling a total of up to six television channels to be transmitted. Filters designed for telephone use are retained for this application, and the same pilot frequencies are used.
What's more, television transmission capability can be added to the system merely by adding television converters at terminal stations; the intermediate repeaters are completely unchanged.

Nippon Telephone calls the development the first coaxial transmission system in the world that can be used jointly for voice channels and multiple commercial television channels. For joint use of the cables, non-linear distortion in the repeaters had to be kept to a very low level, because the line spectrum from television can cause serious noise in telephone circuits, and phase distortion must be held to a low level to prevent degradation of the tv signal.

**Video.** Used for visual phone service the coaxial system will probably be able to accommodate up to 30 or 36 systems. Further evaluation of the frequency response needed for acceptable quality received pictures is necessary before choosing final parameters.

In the future, pcm will probably become the main method of transmitting visual telephone signals, but the new coaxial system can be in service long before the higher-order pcm systems required for this service are developed, and thus will be used initially.

It is the start of visual telephone service—which will probably begin during the next few years—that makes the new wide-band coaxial system necessary. The present 12-Mhz coaxial system with 2,700 voice channels and the present 4-MHz coaxial system with 960 voice channels—together with microwave systems including those with 2,700 voice channels, 1,800 voice channels, and 960 voice channels—would probably be sufficient for increases in voice communications for the next seven or eight years.

The large capacity of the new coaxial systems will also provide increased capacity for increasing demands for transmission of data and facsimile.

The new system uses the same coaxial pairs used for the 12-Mhz transmission systems. But attenuation at the higher frequencies is greater, forcing a reduction in repeater spacing to about 1 mile from the 2.8 miles used for the 12-Mhz system.

Design of repeaters with good performance and high reliability is the most important factor in the success of this system. Most of the key components in the repeaters had to be designed specially because they didn't exist when the project was started. Transistors used must have extremely low distortion. Then, a very large amount of feedback is required to bring the distortion of the amplifiers down to acceptable limits. This meant developing low-distortion transistors that have extremely high cut-off frequencies. Low excess phase and refined feedback techniques enabled use of the very large amount of feedback needed to keep amplifier distortion within acceptable limits.

**Gain.** Very precise automatic gain control in the repeaters maintains signal level along the transmission line at a constant value. To prevent increase in distortion the transistor bias is kept constant; instead age is accomplished by a thermistor in the feedback circuit of each repeater. The thermistor corrects for the change in coaxial cable loss over a 27°C operating range, entered on 15°C.

This correction is not perfect, though. To correct for remaining error in gain the 61,16-Mhz pilot frequency—the highest of four pilot frequencies—is rectified at attended repeater stations, which are spaced 62 miles apart. The rectified signal is amplified and fed to series-connected heaters attached to the thermistors in all repeaters between two attended stations. The age circuit used can adjust the gain by ±1 decibel.

In another departure from previous practice, a step-recovery diode is used for generation of the carrier frequencies used in the system. A stable-frequency source generates at 2.2 Mhz, and the pulse wave of the signal distorted by the step-recovery diode gives harmonics from which higher frequency carriers are obtained. In previous systems, a coil with a saturating core was used for generating harmonics, but it does not work satisfactorily at the high frequencies used in this system.

**Optical couples**

Two negative-resistance light-emitting diodes that can act as both emitters and sensors have been developed by researchers at Japan's Oki Electric Industry Co. Both of the diodes are produced.
Self service. Europe's largest push-button telephone exchange, offering a variety of special services and using many semiconductor circuits, is now in service in Vienna. Made by Standard Telefon und Telegraphen AG, the exchange serves the 1,200 extensions of the city's Creditanstalt-Bankverein AG. However, the exchange is capable of handling 10,000 extensions.

Among the special services built into the exchange is call interruption by some high-level extensions; those using the lines are warned of the cutting-in by an audible signal. Some extensions are fitted with automatic tracing of malicious calls both internally and from outside.

in single epitaxial growth. One uses n-type gallium arsenide as the starting material, with pnp layers grown by doping with silicon, which can act as either donor or acceptor in gallium arsenide. This diode emits in the infrared region at 9,300 to 9,500 angstroms. The other diode is similar, but is based on the three-element compound of gallium aluminum arsenide. It emits visible light in the red to green range.

Both diodes exhibit s-type negative resistance. At very low currents, the terminal voltage increases with current, reaches a peak, and then goes through a negative resistance region where voltage falls with increasing current. The diodes also emit light approximately proportional to the current. But if the diodes are externally illuminated, the voltage peak becomes depressed. This change in voltage characteristics can be used to give switching action, which takes place in tens of nanoseconds.

Oki engineers say that a variety of functions can be performed by the diodes, including use in indicators and displays, photoswitching circuits, photocoupling circuits, light pulse-generating circuits, and optical logic, delay and memory circuits, as well as optical amplifier and pulse-regeneration circuits.

Sources at the Sharp Corp., which earlier announced similar infrared and visible-light-emitting diodes with negative resistance [Electronics, Aug. 4, 1969, p. 229], say that its earliest diodes also showed negative response to incident light. However, they intentionally eliminated that sensitivity in later development models to avoid possible patent problems and, at least for now, any design headaches caused by self-oscillation or other light-induced instabilities.

West Germany

Joint computation

When Siemens AG and AEG-Telefunken pooled resources in turbine and generator engineering about a year ago, there was little doubt that West Germany's two top-ranking electric/electronics producers would some day try the same approach in specific areas of electronics as well.

Last week, both companies let it be known that exploratory talks aimed at some sort of collaboration in data processing have entered a phase where a get-together is seriously contemplated. Discussions are now centering on setting up a jointly-owned subsidiary for computer development, production, and marketing.

Limited. The intended cooperation in computers won't be across-the-board. With its present 4004 models, Siemens has too strong a position in the commercial equipment field to willingly share it. And in the process-control arena both companies compete too fiercely, ruling out a joint venture in this field. Besides, both have know-how exchange agreements with U.S. computer makers—Siemens with RCA, and AEG-Telefunken with General Electric—which may block any cooperative deals involving computers now in production.

The joint venture, instead, is aimed at a large system—the machine costing $3 million and up, roughly comparable to Remington-Rand's Univac 1108, Control Data's CDC 6600 or the upper end of IBM's Series 360. The Siemens/AEG deal probably will be finalized sometime next year.

Some obvious reasons led to the move. Parallel development is costly. Both firms want to make more efficient use of available computer specialists and of financial resources. Equally important, the firms stress, is that a common initiative will more quickly close the gap that still exists in large-systems development. This, then, would put the two computer makers in a better stance to meet competition from abroad.

Good politics. But aside from all that, insiders say, politics played perhaps the biggest role in getting the two to conduct talks. For years, Bonn government officials have been prodding the computer industry to work together and avoid costly duplication of effort. And some ministries involved are said to have hinged continued financial support to just such joint deals. Although its computer promotion isn't quite like the Plan Calcul mounted by the French Government several years ago, Bonn has recently increased sharply its allocations to the industry. Financial help coming from the Ministry for Science and Education alone this year amounts to roughly $20 million, which is 90% more than in 1969. There's also more money coming from the Economics Ministry. It's the likelihood of substantially bigger financial help in the future that's bringing the two companies together.
One more time:

Fairchild built a reputation on new products, new technology and new applications. Last month we ran the editorial below. It announced another new step for Fairchild. A commitment to do business wherever there's business. First source. Or second. It was a major policy decision for us. So major, in fact, we're repeating the editorial this month.

EDITORIAL

If We Can't Sell You Ours, We'll Sell You Theirs.

For a long time, Fairchild built only linears designed by Fairchild engineers. We didn't think anything else was worth the effort. People said we had an NIH (Not Invented Here) complex. And, they were right.

However, it's been brought forcefully to our attention that a couple other guys in this business know what they're doing. The competition is coming out with some pretty worthwhile linears. Our customers have noticed too, because they're talking to other manufacturers about linears we don't make. They're even talking to sole sourcers!

To keep things even, we've decided to give our wandering customers something they're going to need if they start dealing with a sole source linear maker: A second source. Us. (Just in case the original supplier's factory blows up or they lose the formula or whatever it is that happens when you can't get delivery.)

Starting now, Fairchild is introducing a new line of linears. We call them IT circuits (Invented There). The first two are available today: The LM101 and the MC1495. Soon we'll add the LM101A, MC1496 and the SN7524. Of course, we've given them Fairchild part numbers. Here's a conversion chart:

|μA795| Analog Multiplier| MC1495|
|μA796| Modulator| MC1496|
|μA748| Operational Amplifier| LM101|
|μA777| Operational Amplifier| LM101A|
|μA761| Sense Amplifier| SN7524|

There will be other additions to the IT line soon. So be sure you contact your local Fairchild Sales Engineer before you drop a design for lack of a reliable alternate source. Just give him the part number you want and ask him to check the IT line. Farewell NIH.
**Application Digest**

If you'd like any of the following application literature just write: Fairchild Linear Applications, Box 880A, Mountain View, Calif. 94040. Ask for it by publication number.

**Publication Number** | **Title**
--- | ---
138 | µA725 Instrumentation Applications
134 | The Frequency Division Multiplex Channel Amp with the µA748
131 | An Arithmetic Analog Computer using µA735 Logarithmic Amplifier
129 | Low-Pass Active Filter for Electronic Imaging using the µA715
125 | Applications of the µA749 Dual Operational Amplifier
141 | µA742 (TRIGAC) AC Power Control Handbook
164 | Applications of the µA722 10-Bit Current Source
136 | Low-Drift, Low-Noise Operational Amp for Low Level Signal Processing — µA725
133 | More Voltage Regulator Applications using the µA723
128 | A High Speed Sample and Hold using the µA715
126 | The µA749 Dual Operational Amplifier
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127 | A Trapezoidal Deflection Circuit for use with the 3250 Numeric Character Generator using the µA715
111 | A High Speed, Zero Input Current Chopper Amp — µA715
119 | A High Speed Differential Preamp for Thin Film Memories — µA751
124 | The µA746E Color TV Chroma Demodulator IC
183 | A Low-Noise Preamplifier — µA741
175 | The µA739 = A Low-Noise Dual Operational Amplifier
122 | A Monolithic Radiation-Resistant Operational Amp — µA744
171 | Applications of the µA739 and µA749 Dual Preamplifier Integrated Circuits in Home Entertainment Equipment

**Fairchild Cuts Prices of Ten Popular Linear**

Say goodbye to modules. New prices on Fairchild’s most popular Linear ICs now make modules expensive as well as bulky.

**The Price Story:**

<table>
<thead>
<tr>
<th>DEVICE</th>
<th>ORDERING CODE</th>
<th>TEMPERATURE RANGE</th>
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<th>NEW PRICE*</th>
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<td>0°C to +70°C</td>
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**Win $100**

We hope you're getting your entries ready for the contest we announced last month. Just in case you missed the announcement, here's what you have to do to enter:

1. Get all the facts on a Fairchild Linear IC.
2. Design the world's greatest application for it.
3. Send to: Fairchild Linear Contest, P. O. Box 880A, Mountain View, California 94040.

All entries will be judged by the editors of EEE Magazine. Every month, they will select the most imaginative application and give us the designer's name. We'll publish the winning design and give the winner $100 upon publication. Ready. Set. Design!
Introducing the World's First Monolithic J-FET Input Op Amp

Punch-through op amps are obsolete.
Fairchild's new µA740 now offers 150pA (max.) current into either input. While some manufacturers are talking about super beta or punch-through transistors with current gains of 1000, Fairchild technology now makes possible J-FET devices with equivalent betas of over 15,000. And, they're completely compatible with standard monolithic processing.

The µA740 is a simple two-stage design similar to the µA741, but employs J-FET input transistors to obtain extremely low input currents.

µA740 Electrical Performance
Input Current .................. 150pA max.
(both inputs)
Unity Gain Slew Rate .......... 6V/µs
Input Resistance ............... 10¹³ Ohms
Voltage Gain ................... 120dB
Input Offset Current .......... 30pA

The new linear has all the convenience of the µA741: internal frequency compensation for unity gain, input over-voltage protection to either supply, output short circuit protection to ground or either supply, and the absence of "latch-up."

Balanced offset null is easily obtained with a 10KΩ potentiometer and does not affect other parameters.

Other µA740 features include a wide common mode range of ± 12 volts, high differential voltage range of ± 30 volts, and wide operating supply range of ± 5V to ± 22V.

The µA740 is directly interchangeable with the µA741, µA748 or µA709.

The new Fairchild device provides circuit designers with superior performance in such applications as active filters, voltage followers, integrators, summing amplifiers, sample and holds, transducer amplifiers and other general-purpose feedback applications.

The µA740 is now available in TO-99 packages (both military and industrial temperature ranges) from any Fairchild Distributor.

Reader Service Number 320

µA757 Ideal Choice for an AGC-Able AM/IF Amplifier

Fairchild's µA757 can be used very effectively as a high gain, wide AGC range IF amplifier. In this application, the input signal from the generator is matched to the input of the microcircuit with transformer T₁. The output of the 1st section is taken from Pin 12 across a tank circuit which acts as a load impedance. The signal is coupled through a capacitor to the input of the 2nd section, Pin 10. The output of the 2nd section is taken in a push-pull manner with transformer T₃. The secondary of T₃ drives the diode detector from which audio is recovered. Q₁ acts as an AGC signal amplifier to provide gain for the AGC signal from the diode detector.

Voltage gain of the circuit from the input of T₁ to the input of T₃ is typically 80 dB, while the AGC range is typically 70 dB. Input signal handling capability of the microcircuit is typically 300 mV rms at the input terminals of the microcircuit at full AGC. Stable gain is obtained over a wide temperature range, regardless of AGC setting.

Reader Service Number 321
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Despite top level Defense Department support, the Army is running into difficulty with its effort to save Project Mallard, the controversial plan for a standard multinational tactical-communications system. Although the House Appropriations Committee recommended cancellation of the joint effort with Canada, Britain, and Australia last year [Electronics, Dec. 22, 1969, p. 146], the Pentagon has moved to save Mallard and the $14 million it wants for fiscal 1971 by recommending to Congress that Mallard be made solely a U.S. tri-service effort and earmarking another $1.6 million in Air Force money for support.

However, some members of Congress are questioning just how genuine the Air Force and Navy support is for a tri-service program initiated by the Army and of limited use to the other two services. So now the Pentagon is thinking about a second option to save the program: make Mallard a North Atlantic Treaty Organization system, where the system will be managed and procured by NATO. The disturbing aspect of this kind of a deal to some U.S. companies is that it would likely increase the amount of hardware bought in other countries.

Though the White House use of an executive order, rather than legislation, has proved successful in creation of a larger and more powerful Office of Telecommunications Policy out of the Office of Telecommunications Management [Electronics, Feb. 16, p. 83], it has another problem. That is getting the new National Electromagnetic Compatibility Analysis Facility (Necaf) established in the Department of Commerce.

Necaf is essential to the operation of the new office, but is encountering problems on Capitol Hill where rumblings are that both the Defense Department and NASA are concerned that the facility seeks too much authority. Necaf proponents argue that a strong body is needed to prevent spectrum pollution through development of Government-wide standards for all communications equipment and systems. And, though Congress can’t stop establishment of the Office of Telecommunications Policy, it can cripple Necaf by restricting its funding.

There’s little hope for a decision this year by the International Civil Aviation Organization on a transoceanic aeronautical services satellite, even though a recent meeting of the ICAO’s Astra panel leans toward an L-band system [Electronics, March 16, p. 61]. ICAO’s Air Navigation Commission, meeting in Montreal late last month, approved six recommendations made by the Astra panel which is defining the system. But the actions only covered more studies.

The next Astra meeting is not expected until November at the earliest, and the U.S. delegation, which has always favored a vhf system, is working on a new position paper answering the Astra report.

An official of the Air Transport Association says the airlines would pay their share for a vhf-only satellite—vhf can provide airlines with earlier access to a satellite. But ATA recently said it would be willing to support a hybrid satellite—four vhf channels, as before, and the addition of an L-band channel. This craft would relay data and voice communications from airliners in addition to providing ranging data.

Comsat, which would put up the Aerosat satellite, is now finishing...
work on a proposal for such a hybrid satellite that would be leased to the airlines (through ATA's Arinc) and financed by NASA, FAA, and the airlines. The ATA says it has held high-level discussions with both NASA and FAA on funding the satellite, but it isn't clear at this point where either agency would find the money to support such a plan.

The Air Force thought at least one portion of the C-5A transport—avionics—was safe from criticism during the months of clashes between Congress and the military over the problems of the giant Lockheed aircraft. But now some House members are concerned over Air Force admissions at recent Congressional hearings that the avionics are not performing as promised and may even require redesign.

The avionics problems could kick off a new round in the controversy, even though the cost of the avionics has not risen as sharply as other C-5A outlays. Cost of avionics per aircraft is now about $2 million, up from the original estimate of $1.7 million.

One avionics problem is in the plane's multimode radar developed by United Aircraft's Norden division. The radar was not working in the terrain-following mode; side lobe signal returns from objects directly below the aircraft were limiting its forward-looking function. Lockheed maintains "substantial improvements" have been made in the last 60 days. Air Force sources conceded that some improvements had been made, but stated that the radar is still not acceptable.

The Bureau of Public Roads will proceed in about a month with an award for Washington, D.C., demonstration of its Electronic Route Guidance System (ERGS), although budget cutters have sharply scaled down the field tests. Initial plans had called for equipping 100 intersections in the Capital area with electronic gear to direct several hundred specially equipped vehicles [Electronics, Aug. 18, 1969, p. 138].

Now, say Department of Transportation sources, the program has been cut by more than half. Though DOT is still juggling funds for its industry spending—and thus won't discuss numbers—agency officials acknowledge they have received favorable final proposals from General Motors, Philco-Ford, and Kollsman Instrument.

Despite the enthusiastic response from local law enforcement officials to the Justice Department's System for Electronic Analysis and Retrieval of Criminal Histories (Search), there's a feeling the real-time system is going to run into problems. Designed to tie state and local law enforcement agencies into a central index file in Michigan, Search will be demonstrated for the first time in July and August (See p. 115).

One basic problem, says a Sperry Rand Corp. official, is that there is no unique characteristic, such as a fingerprint, programed into an offender's Search record. What's needed, he says, is an advanced, accurate fingerprint reader [Electronics; March 30, p. 52], but adds that "the technology is not yet here." R&D funds for fingerprint scanning and matching techniques should be included under the project, he feels.

While Search officials do not feel the lack of a unique characteristic will cripple the system, they do plan to run controlled experiments on just how many "misses" are caused by the lack of offenders' fingerprints during this summer's tests.
Nobody has asked for this one yet, but if they do we're ready.

We've just given birth to a new operation that's ready for anything. It's called Sylvania Circuit Modules and it can handle anything in hybrid microelectronics, simple and complex circuit boards, and complete circuit assemblies.

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3. Tape Reader or Punch
   DM8550N Quad Latch
   DM8533N Four-bit Binary Counter
   DM8842N BCD to Decimal Decoder

4. Input Interface
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5. CPU
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   DM8551N Bus-OR'd Quad D
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   DM8580N Eight-bit Parallel-In
   DM8580N Parallel-Out Shift Register

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It's MSI

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DM8200N Four-bit Comparator
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ELECTRONIC COUNTERS

There's a new reason why this continues to be the world's most popular counter line.
R-f power transistors abroad: Reliability is the key factor page 80

Though most European and Japanese r-f power transistor manufacturers lag behind U.S. firms in the watt-megahertz derby, an *Electronics* roundup finds that the overseas companies are making considerable progress in achieving low collector voltages and high reliability levels. The main technological emphasis at the manufacturing facilities abroad is on fulfilling customer demands, largely for f-m and a-m communications applications.

In EVR, the CBS approach to video playback, two guns in an electron-beam recorder scan a special silver-halide film, putting monochrome images on one half and coded color data on the other. From this master, copies are made. These are loaded into cartridges, which fit into a playback unit connected to a tv set's antenna terminals. The quality of the regenerated signal is as good as the best broadcast picture, and since an EVR cartridge can hold 100 billion dots of varying shades of gray, it has potential in data-storage work.

Camera's i-r eye focuses on new vistas for ranging page 102

A new type of automatic rangefinder focuses a movie camera by shooting pulses of infrared light at an object and controlling a detector's position with reflected energy through a pair of photoresistors and a small motor. Though the first application for the new device is in photography, it could find important uses in a wide variety of portable distance-measuring equipment and other applications.

A better design for character generators page 107

Character generation in remote computer systems using crt displays can be a costly design problem. One increasingly popular solution to this problem is to include in the circuitry MOS read-only memories and shift registers that produce and refresh alphanumeric characters on the face of the tube. One design approach is simple and inexpensive, and yields drift-free characters.

Coming

A new technology holds out promise of junctionless MOS

A new, simple MOS device handles data by moving charges, coupled to spatially separated depletion regions, across its surface. The technology can be used in variable delay lines, imaging, and displays, and may not require p-n junctions in many applications.
Watt-megahertz ratings run second to high reliability in foreign r-f power transistors

In a roundup of the technology abroad, Leon M. Magill of Electronics' staff finds emphasis geared to customer needs

Does an r-f power transistor gap exist between Europe, Japan, and the U.S.? The answer is yes—and no; it depends on who you talk to and what aspect of that technology you're talking about. There's no overall agreement on where the gap is and who's ahead.

In Japan, for instance, Nippon Electric engineers feel that their r-f transistor technology lags the U.S. by at least a year or two. On the other hand, engineers at Tokyo Shibaura Electric Co. (Toshiba), Mitsubishi Electric, and Fujitsu claim to be a year ahead of their U.S. competitors. (They do note that although they feel they've reached the U.S. in reliability and low-collector-voltage devices, they lag in producing high-power, high-frequency transistors.)

The situation is no different in Europe. The consensus among the large West German semiconductor manufacturers—AEG Telefunken, Valvo Gmbh, and Siemens AG—is that although Germany is not lagging behind the U.S. in basic technology, German firms still have ground to make up in developing specialized circuits for power transistors.

In England, the answers are equally cryptic. Texas Instruments Ltd., for example, claims it leads U.S. manufacturers in interdigitated-device technology, while Mullard Ltd. puts its technology on a par with that of U.S. firms. However, both infer that their emphasis is on reliability rather than high power.

France's Sescosem, the least optimistic of the overseas companies, indicates that its technology lags that of the U.S. by two to three years, while in the Netherlands Philips Gloeilampenfabrieken's Elcoma division estimates its technology is equal to that of U.S. makers.

No matter how the overseas companies rate themselves against U.S. r-f power transistor technology, they feel the U.S. companies mainly are pushing for higher power at higher frequencies. This assessment is correct—to a certain extent. American manufacturers are producing r-f power transistors that operate at 3 gigahertz and put out 2.5 watts, for a watt-megahertz figure of 7,500 [Electronics, Nov. 10, 1969, p. 33]. In fact, some American firms are building devices with outputs of 100 watts at 150 Mhz (15,000 watt-Mhz), and some off-the-shelf items produce 20 watts at 1 Ghz and 10 watts at 2 Ghz. Nearly all of these high power r-f transistors operate at collector voltages of 28 volts or higher, and are housed in stripline packages. But despite their emphasis on higher power at higher frequencies, most U.S. transistor manufacturers also are concerned with reliability, bandwidth capability and collector efficiency.

Reliability, both for military and commercial applications, is a prime concern of U.S. manufacturers. They are well aware of the many user applications that place stringent requirements on r-f power devices, and realize that ruggedness is a must. Some devices on the market

Rugged. Philips' interdigitated r-f power transistor, in a stripline-with-stud package, was developed for single-sideband operation.
are rated for all conditions of load impedance—from an open to a short circuit, and for all phases. Heat sinking and package configurations have received considerable attention to insure reliable operation under even the most stringent environmental conditions. And the U.S. firms seem to be doing something about broadening bandwidth capabilities, whereas, the offshore companies at present are confining their work to the narrowband applications of their customers. Most of the U.S. manufacturers provide input impedance and $Q$ data on their devices so the user can estimate their broadband capabilities; most offshore manufacturers cannot or will not provide such information.

**The Japanese companies** lead the Europeans in the watt-Mhz derby. Although they still lag behind the U.S. they feel that their transistors perform much better at lower collector voltages. Nippon Electric offers a line of r-f power transistors that produce from 35 watts at 50 Mhz at a collector voltage of 13.5 volts, to 3 watts at 2 Ghz at 18 volts. Toshiba offers a 175-Mhz device that puts out 60 watts at 24 volts; Mitsubishi Electric produces a 5 watt-transistor at 1 Ghz; and Fujitsu has a 2-Ghz unit with a 1-watt output.

But the European firms are not too far behind. Philips, for example, has a 50-watt transistor that operates at 175 Mhz. And in England, Texas Instruments Ltd. boasts a 25 watt, 900 Mhz unit, while Mullard’s 470-Mhz transistor delivers 7 watts at a collector voltage of 12 volts. In Germany, Telefunken, and Valvo are marketing lower-frequency devices. Most of them have a 50-watt transistor that operates at 175 Mhz and a 7-watt device at 470 Mhz.

In France, Sescosem at present is marketing only one r-f power transistor—a copy of the 85 watts peak-to-peak 30-Mhz RCA TA-2758. It was originally developed to give its parent company, Thomson-CSF, a second source. However, Sescosem also is producing other power transistors at 200 and 400 Mhz, but these are for internal consumption only.

**The future** should see a move toward higher power at higher frequencies all around the world. But such a trend will have to take many factors into consideration, including broadband capability, efficiency, power handling capability, and packaging.

In Japan, Toshiaki Irie, manager of the microwave transistor section of Nippon Electric, says the input parasitic impedance will have the largest effect on power output, bandwidth, and power gain of the r-f transistor. Thus he feels a technological thrust to reduce the common-lead impedance of power transistors is perhaps the most important factor. Efficiency, Irie adds, can be improved by better heat removal from the collector-base junction. This could be effected by moving the collector junction closer to the emitter surface than to the rear.
surface of the chip, as Bell Labs does in its flip-chip Impatt diode.

The Japanese predictions seem conservative compared to those emanating from the U.S. Toshiba, perhaps the most optimistic manufacturer, forsees by 1971 a power transistor that puts out 10 watts at 3 Ghz—a watt-Mhz figure of 30,000—which is only 40% of its predicted theoretical limit. And Fujitsu Ltd. predicts 80 watts at 1 Ghz. Mitsubishi and Nippon Electric, however, are less optimistic over their 1971 product line. They see only about 20 watts at 1 Ghz—a figure TRW’s Semiconductor division achieved several years ago in its commercial devices.

In Germany, the prognostications are even less optimistic. Valvo engineers see only 6 watts at 1 Ghz for their 1971 line, but do envision higher frequency units, such as 2.5 watts at 2.3 Ghz and 2 watts at 2.7 Ghz, becoming available sometime in the future. Engineers at Telefunken, where broader bandwidths are deemed more important than the quest for higher watt-Mhz figures, claim that 10-watt, 1-Ghz and 5-watt, 2-Ghz power transistors should be ready by 1971. But these devices will be used primarily for in-house microwave communication systems requirements. Siemens sees a 3-watt device operating at 2 Ghz and a 0.4-watt transistor at 5 Ghz by 1971. Hans Hargasser, section head of Siemens’ discrete components division, feels that the state of the art in one-micron structures and related masking techniques should produce a 1-watt, 5-Ghz device by 1971.

In France, engineers at Sescosem feel that most theoretical limits on r-f transistor performance are determined by economics rather than technology. In power output, for example, they feel the limit beyond which it becomes cheaper to use two transistors is not far beyond that of RCA's TA2758—an 85-watt peak-to-peak rating of the 30-Mhz power transistor. "We could go up to 500 watts peak-to-peak on a single-sideband transistor," claims Hervé Deroy, manager of the discrete components division, "but it would be much more expensive than using several smaller ones." Deroy, who sees the upper-frequency limit for silicon power transistors at about 10 Ghz, feels that gallium arsenide devices could be pushed up to 20 Ghz. And according to Deroy the present watt-Mhz ratings of 15,000 should double by the end of 1971.

Of theoretical limits, K.W. Hinkel, head of the department of development and fabrication of transmitter semiconductors at Philips says, "The state-of-the-art is so far away from these limits that it's not worthwhile considering." Hinkel, who considers ruggedness one of the major considerations for r-f power devices, claims that "If you drop the ruggedness requirement, a power increase of roughly two times can be expected from existing devices."

There's some disagreement over what factors determine theoretical limits. Some British r-f power transistor manufacturers, Texas Instruments Ltd., for instance, feel that the maximum power and frequency are limited by the device's input resistance. They see 50 watts at 1 Ghz and 20 watts at 2 Ghz as the maximum that should be available, although they don't say when. Engineers at TI think about 20 watts per chip at 2 Ghz is the upper limit. Mullard engineers claim the theoretical limitations of r-f transistors are based on chip size, voltage breakdown limits, and input/output matching. Using these criteria they say 4 Ghz will be the upper frequency limit; they also see a maximum of 500 watts at 2 Mhz, about 200 watts at 30 Mhz, and 25 watts at 3 Ghz. As for actual predictions, Mullard engineers target the following limits for 1971: 120 watts at 200 Mhz; 75 watts at 500 Mhz; and 12 watts at 2 Ghz.

As in the U.S., the overseas transistor manufacturers
Foreign figures. R-f power transistors available from Europe and Japan show collector voltages ranging from 12 to 28 volts; output power ranges from 1 to 80 watts.

tailor their high-power r-f devices for specific applications. The overwhelming majority in Japan are used in f-m communications, such as mobile radio, CATV, and fixed repeaters for point-to-point communications. In Europe, however, the bulk of the applications are for both f-m transmitters and a-m transmitters, mainly for aircraft communications systems.

Mullard has developed a line of r-f power transistors specifically designed to withstand the higher voltage swings required in a-m. Much of Mullard’s output is geared for export—about 30% for f-m transmitters and an equally large amount for light aircraft a-m transmitters. These require transistors that operate at 12 volts.

All the overseas manufacturers agree on the single importance of providing for safe operation under severe mismatches. Philips’ Hinkel thinks that if power and frequency are the only considerations, you’re out of the market. And Mullard’s chief of transistor activities, John Culling, observes, “Just getting gain is not the real answer—the difficulty is developing a device that will withstand all likely mismatch conditions at its rated power.” And “secondary breakdown characteristics are also of great concern,” says Deroy.

Mitsubishi engineers feel that secondary breakdown characteristics of their devices have been greatly improved, so much so that devices don’t burn out, even under open or short-circuited loads. Dieter Gerstner, manager of r-f transistor research and development at Telefunken, indicates that overload capacity is important to all users. Good linearity characteristics are significant in a-m operation, while for f-m, efficiency is one of the major considerations.

Sescosem’s Deroy feels that a major problem plaguing his users aside from secondary breakdown is maintaining linearity in single-sideband operation. Finding a solution that solves both these problems is almost impossible, he adds, “since relieving one automatically worsens the other.”

Although overseas r-f power transistor manufacturers agree that most of their devices are used for narrow band applications at present, they are concerned about bandwidth capability. The amount of broadbanding in r-f power transistors seems to be tied directly to the ability of matching the transistor to the circuit and compensating for its package parasitic inductance. But according to spokesmen at Fujitsu, Telefunken, and TI, the latter is
Power amplifier. Fujitsu’s 2-Ghz amplifier uses a coaxial transistor (FT-1911) to develop an output of 1 watt and a power gain of 5 db.

the major problem in broadbanding.

"We plan to introduce a multilayer lead technique to minimize the parasitic-inductance problem," says Fujitsu’s Kaichiro Katori, chief of transistor engineering. Multilayer metalization, which is similar to that used to provide crossovers in integrated circuits, reduces the base-to-emitter inductance. According to Katori, "it would then be possible to have emitter metalization that completely surrounds the base and still bring out the base lead through an opening etched through the layers of emitter metalization and insulation. And the capacitance introduced by this technique will be small compared to the intrinsic base-to-emitter capacitance of the transistor." Although multilayer metalized transistors are not yet part of the Fujitsu line they will be offered shortly, Katori indicates. This technique will be used for future high-power transistors—20 watts or more—under 1 Ghz. The technique needs improvement for use at 2 Ghz and higher because of added stray capacitance.

Package parasitics can be used to advantage in some broadbanding applications. Inductance and capacitance of the package can compensate each other through an arrangement that produces a matched-transmission line, says Telefunken’s Gerstner. Mullard engineers share this view. They are aiming at passive matching circuits right on the chip substrate. "The future lies with modules containing transistors with integrated matching networks," asserts Culling. And Mullard is working toward power transistors with matching networks right on the chip that will operate over octave bandwidths with 50-ohm input and output impedances. The multifunction module—a power transistor with other active devices and matching networks—should outsell its discrete competition by 1973, forecasts Derek Crook, power-device marketing manager at TI. And Crook may be right.

On the other side of the Atlantic, however, RCA now has single-stage hybrid-amplifier modules with 20-watt output flat to 1 db in the 225-to-400 Mhz range. Using its transistor chips and batch-fabricated hybrid lumped-constant elements, RCA has had completed amplifiers operating at frequencies from 0.2 to 2 Ghz [Electronics, April 14, 1969, p. 100].

But what about octave bandwidth capability in power transistors operating at 3 Ghz? Here, opinions differ considerably. Nippon Electric’s Irie, for instance, is convinced that such a device won’t be commercially available because designers in Japan really don’t seem to need it; instead, he says Nippon will devote its efforts to devices at 4 Ghz and 6 Ghz—the frequencies occupied by microwave telephone repeaters in Japan. To this end Nippon Electric already has developed a 4-Ghz transistor and is testing it at 6 Ghz. This device produces 100 milliwatts in class A, and has a gain of about 4 db and a bandwidth of 1 Ghz in the common-emitter configuration. Although data on input Q is not available, Nippon Electric engineers claim that in the common-base configuration the gain is slightly higher but the bandwidth is reduced. Although Yasuo Fujii, chief of applications engineering, of Mitsubishi does anticipate an octave-bandwidth transistor at 3 Ghz, he says it won’t appear much before 1974. However, Fujitsu’s Katori predicts such a device by mid-1970.

In Europe, TI’s Crook feels the firm could build such a device right now if someone would foot the development cost. But Mullard’s Culling, who isn’t quite that optimistic, says “a 3-Ghz octave-bandwidth device probably will be available by 1973, although not from us.” And Sesosem’s Deroy puts it this way: “European companies are two to three years behind the U.S., just as their customers are. We’ll wait for the Americans to do it and then follow.”

The consensus of opinion in both Europe and Japan
<table>
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<tr>
<th>Transistor type</th>
<th>Mfr.</th>
<th>Freq. (Mhz)</th>
<th>P. (watts)</th>
<th>P.G. (db)</th>
<th>Efficiency (%)</th>
<th>Package Style</th>
<th>Vcc (volts)</th>
<th>Input Q</th>
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<tr>
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<td>Sescosem</td>
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Siemens is not selling on the open market as of this date.

SWS — Stripline with stud
GE — Grounded emitter
NS — Not specified on data sheet
LIS — Low inductance stripline
PEP — Peak envelope power

NEC — Nippon Electric Corp.
TFK — AEG — Telefunken

Electronics | April 27, 1970
Circle 85 on reader service card
is that emitter stabilization resistors are essential to the design of rugged, high-power r-f transistors to minimize burnout due to high vswr’s. However, exactly where and how they will be incorporated into the structure still isn’t certain. Gerstner of Telefunken, who claims he first proposed emitter stabilization several years ago and has several patents pending, says it would be advantageous to have a resistor at each emitter site. But this arrangement requires a large resistor area on the chip which also produces relatively high capacitances, so it’s not a practical solution for transistors operating at uhf and microwave frequencies. Gerstner proposes a compromise between the number of emitter sites per cell and the number of cells each having one resistor.

Gerstner’s views, however, are not shared by TI’s Crook, who advocates putting an emitter stabilization resistor at each emitter site. An extended linear interdigitated array is used as the transistor structure to provide maximum emitter periphery relative to area. This would better distribute the emitter current density over the maximum edge area, minimizing the possibility of local hot spots, hence increasing stability. **The resistor** is fabricated by leaving a pad of oxide across the strip at the spine joint so that the metalization over the emitter is broken at that point. The emitter site thus has its own resistor beneath this discontinuity. Crook indicates that this arrangement will enable 25-watt devices to survive all phases of load mismatch at the rated power.

Valvo’s attitude is more flexible. The company claims that it uses whatever approach is suitable as long as the transistor can withstand a load vswr of 50. Valvo uses an emitter grid structure and stabilizing resistors to prevent hot spots that can cause failures during severe mismatches and secondary breakdown. Using one resistor per emitter cell instead of at each site is the rule at Mullard. This resistor is fabricated via thin-film technology.

Philips emphasizes adequate material specification in addition to emitter stabilizing resistors. It selects the proper thickness and restivity of the epitaxial layer, as well as using short emitter fingers, to provide equal current distribution in the chip. And the emitter stabilizing resistors are manufactured by either diffusion or evaporation, whichever is more suited to the particular device.

Emitter-stabilization resistors are employed by most Japanese r-f power transistor makers. These vary from evaporated aluminum layers to silicon bulk resistance, depending on the power and frequency of the device.

Nippon Electric, Fujitsu, and Mitsubishi all agree that emitter-stabilizing resistors are necessary—especially when the device operates in the gigahertz region. Toshiba, however, characterizes them as performance-degrading devices even though they improve secondary breakdown characteristics; Toshiba’s solution is to lower the input circuit Q with a lossy input choke.

Fujitsu uses resistors at each emitter site. This tends to equalize the current flow to each emitter and thus prevent constriction, as does the multilayer configuration. And the multilayer configuration produces a much more uniform current flow for interdigitated transistors than does the conventional configuration.

In one transistor Nippon Electric uses a separate resistor at each of 400 emitter sites. However, in many of its transistors the resistors are included at each emitter cell.

**With stabilizing resistors** in series with the emitters, gain decreases at low power levels. And because of a more equal division of current, it’s assured that each individual emitter is working at close to 100% of capacity. There is, however, an optimum value of emitter resistor—a value that’s too high will reduce output power and one that’s too low is useless.

*Matched-transmission line.* Intended for use with stripline circuits, Telefunken’s device removes heat by using a 1-mm beryllium-oxide layer between the emitter on one side and the base and collector on the other. With this package, parasitic inductances are only one-third of those encountered in a TO-60 package.

**HEAT FLOW**
The packaging problems faced by the foreign r-f power transistor manufacturers are no different from those of U.S. firms. Likewise, the approaches taken abroad to increase both operating frequency and output power while maintaining high reliability are quite similar to U.S. methods. Where power dissipation and circuit compatibility are considerations most manufacturers opt for the stripline configuration; for gigahertz frequency operation, coaxial packages get the nod.

Nippon Electric’s Irie feels that the stripline pack is the style of the future and is best for circuit compatibility. And although engineers at Toshiba, Fujitsu, and Mitsubishi agree, they differ as to the methods of obtaining the best package. Toshiba’s senior specialist, Juzo Yoshida, indicates that the stripline package will have to be modified to reduce ground inductance to obtain higher powers at higher frequencies. However, he declines to reveal the company’s method. At Mitsubishi, the basic aim is to reduce chip size and widen the strips to reduce inductance, claims Fujii. “In the future,” he asserts, “we will reduce chip size by two-thirds and bond the wide strips directly onto the aluminum pads of the transistor chip to reduce the base and emitter inductances to about 0.6 and 0.3 nanohenrys, respectively.” Present values are about 1 nh for both.

Stripline is the number one package choice of Telefunken engineers. “Since this technique is most often used in r-f circuit design the package should also be compatible with the technique,” says Telefunken’s Gerstner.

Valvo engineers, like most, agree that stripline is the choice for frequencies below 1 Ghz. But for higher frequencies their approach will be either coaxial or some form of hybrid circuitry. Valvo researchers are investigating the hybrid approach—microstrip and lumped circuits—and most likely will settle on a thin-film hybrid circuit which offers low parasitic inductance making a broadband device possible. Price is also a factor. “Devices shouldn’t cost more than $100 each,” adds Frithjof Lampe, who heads Valvo’s r-f power transistor production. And Valvo’s circuit choice will depend on this factor too.

“The best package style is the coplanar stripline compatible pack,” says Mullard’s Culling. He indicates that there are definite advantages in hermetically sealed air-space stripline packs, however, they are too costly right now. Culling says that the ultimate package for power dissipation will contain its own heat sink. This is academic, however—in practice the sink must either be a stud or a flange. Although flange packs theoretically offer better heat removal than stud types they are difficult to machine sufficiently flat. The stud package, however, does offer good results when dissipating up to about 100 watts. Culling points out that user convenience is most important, and unless new package styles can be second-sourced, users won’t buy them.

“The low-inductance stripline with isolated emitter is, in our opinion, the most suitable package up to 1 Ghz. For higher frequencies, the coaxial package will win,” says Philips’ Hinkel. And Fujitsu’s Katori agrees: “The coaxial package is best at frequencies above 2 Ghz because of lower package parasitic capacitance and superior input-to-output isolation.”

Sescosem engineers favor the stripline package over the coaxial unit on virtually every count. “Connector self-inductance characteristics can be just as good with stripline and power dissipation performance is better,” says Deroy.

In general, two methods are most often mentioned by overseas manufacturers for efficiently dissipating r-f transistor heat. These are the stripline package with a metal stud that screws into a heat sink, and the use of a beryllium-oxide layer that serves as both an electrical

Mismatch data. Dissipation of a power
transistor varies with output mismatch
phase. Here, a Mullard BLY-53A was
measured at 470 Mhz with 2 watts of
drive power and the load vswr held at 50.
Foreign registration—better, but...

As in the U.S., registration of r-f power transistors in Europe and Japan leaves much to be desired—especially for specifying the r-f parameters. But though most foreign manufacturers and their customers are not completely satisfied with their registration systems, they prefer it to the U.S. version [Electronics, May 26, 1969, p. 88].

Under the U.S. EIA registration, a transistor receives a 2N code number followed by a set of numbers that doesn't specify material, frequency range, application, or other useful information. The 2N tells you it's a transistor (IN for diodes) but nothing else.

In Europe, Pro Electron, a Brussels-based agency, assigns designations to new discrete semiconductor devices. The Pro Electron code is based on three letters and two numbers. The first letter identifies the semiconductor material used (A for germanium, B for silicon, C for materials with an energy band gap of 1.3 or more electron-volts such as gallium arsenide, and D stands for materials with a band gap of less than 0.6 such as indium antimonide). The letters A through D indicate one or more junctions; R stands for devices with no junctions and made of materials such as are used in Hall generators and photoconductive cells. The second letter represents the application. For example, L is a high-power r-f device; F means r-f low power; D means low-frequency power; C is a low-frequency preamplifier; P is an optoelectronic device; A is a diode; B is a varicap, U is a power switching transistor, S is a switching transistor, X is a multiplier diode, Y is a rectifying diode, and Z is a zener diode. Devices are characterized as either L or F by their thermal resistance expressed in degrees Centigrade per watt. Anything less than 15°C/watt is considered high power, anything above is low power.

The third letter—if there is one—indicates industrial device and is a Y. If there is no third letter, the device is for consumer or entertainment use.

The digits that follow the letters for industrial units indicate how many devices of that particular type have been registered. The digits start at 10 and go up to 99. When 99 is reached—i.e., after 89 devices—the last letter changes from a Y to an X and the numbering begins anew, working back toward A. There is no Z. For consumer devices the numbers that follow the 2 letters start with 100, allowing registration of 899 similar devices.

The designation BLY 80, for example, means the device uses silicon (B); is for high r-f power use, (L); and is used in industrial applications, (Y); the 80 means that it's the 71st device of its type to be registered with Pro Electron.

Telefunken's Dieter Gerstner says that "good as it is, Pro Electron by no means describes the device completely. Data sheets are necessary to completely describe the device." Valvo considers it superior to the U.S. EIA registration system.

Japan uses a registration code developed by the Electronic Industries Association of Japan; it's similar to the EIA system. And according to most manufacturers and users, it's also inadequate. Nippon Electronic's Toshiaki Irie, microwave transistor manager, indicates the EIAJ system is inadequate because it does not completely describe the device's r-f parameters. Mitsubishi's Yasuo Fujii, chief of applications engineering says, "Circuit designers want to have secondary breakdown characteristics included in the EIAJ registration."

Great expectations. Mitsubishi's r-f power transistor output predictions for 1970 and 1971 are referenced against the company's estimated theoretical limits and were made using a power gain of 3 db as absolute minimum.
insulator and heat conductor. But other approaches aren’t ignored. Nippon Electric, for example, uses double heat sinking—on both the emitter and collector sides of the transistor.

Most foreign manufacturers agree with their U.S. counterparts that two or more transistors will provide higher output powers more reliably than a large unit. However, there are differing opinions on optimal maximum power output of a single large transistor. About half the manufacturers cite 50 watts, while the rest see 100 watts as the limit. And in addition, some makers specify a frequency limit. Siemens, for example, thinks that at 30 Mhz approximately 100 watts is the limit for a single transistor. Mullard, however, indicates that above 400 Mhz, the limit should be about 10 watts, a figure set by usable power gain, collector-voltage, and cost considerations.

Opinion on plastic-encapsulated transistors divides foreign manufacturers into two camps—those that say their customers want them and those who say that plastic transistor applications are marginal and unsuited for extreme environmental conditions. Everyone agrees that low cost is the main advantage of the plastic package. However, there’s a tradeoff—a poor hermetic seal. And if a silicone encapsulation is added to provide a better hermetic seal it increases the feedback capacity and degrades performance at high frequencies.

Although Nippon Electric does not plan to manufacture plastic transistors at frequencies above 2 Ghz, Irie claims that silicone-molded transistors with platinum-gold metallization have higher reliabilities than can-case transistors with aluminum metallization. “Our customers want plastic transistors because the price is low,” says Irie. He claims humidity is no problem; in fact, he says the units can meet very severe specifications. However, he thinks that plastic transistors are not suited for space applications—in such a vacuum environment volatile components can evaporate and be deposited elsewhere. “This doesn’t harm the transistors,” adds Irie, “but it could degrade the performance of associated equipment.”

“Our plastic devices have met full Mil Specs,” says TI’s Crook, He adds that the devices that were certified by the Royal Aircraft Establishment for use in broadband airborne military transmitters were developmental devices, not production transistors. They were developed by TI under government contract. They operate from 225 to 400 Mhz, and produce 15 watts at 100% amplitude modulation and from 15 to 20 watts in the f-m mode.

In France, the military is accepting Sescosem’s SFT-339A plastic-encapsulated power transistor despite possible moisture-leakage problems. The reason, according to Deroy, plastic is the only package available for high power at high frequencies. “They may well reject plastic when other packages are available,” adds Deroy. However, in Germany, the military, with its stringent requirements, will not accept plastic transistors even though Valvo engineers consider them acceptable for military use.

Engineers at Mullard are convinced that it’s the radial lead layout, made possible at low cost with plastic, that makes this type of encapsulation so popular. According to Mullard’s Culling, radial leads are superior to other configurations at frequencies above 175 Mhz because “unwanted inductances go up with frequency, making the shortest leads possible, both inside and outside the package, a necessity.”

But low cost isn’t everything. Fujitsu’s Katori says that low cost cannot compensate for the poor reliability of plastic transistors. Consequently, Fujitsu’s customers—manufacturers of high reliability communication equipment—don’t want them.

The overwhelming choice of material among all the r-f power transistor manufacturers is silicon—simply because silicon technology is better known and understood even though other materials offer some advantages. Germanium, for example, offers higher frequency performance than Si but runs into problems at about 85° C. Gallium arsenide, which also should provide higher-frequency performance, presently is not economically practical.

In addition, GaAs poses several technical problems. Difficulty in controlling the impurity profile, in the insulating masking process, and low thermal conductivity make it unsuitable for use as an r-f power transistor material at present. However, some foreign transistor manufacturers aren’t deterred. Nippon Electric, for example, is doing theoretical and experimental work in GaAs field effect and bipolar transistors. It’s also investigating Schottky-gate transistors for high-speed switching. Engineers at Toshiba also are investigating GaAs r-f power FET’s and bipolar transistors; they feel that a 10-watt, 2-Ghz FET is not too far off.

Valvo engineers have been working with both Ge and GaAs in the laboratory, but feel that for device development Si is still the best material.

Siemens says silicon’s strong point is its ability to perform adequately at higher temperatures. Experiments done with GaAs at Siemens indicate that, as far as Siemens’ engineers are concerned, it’s not well suited for bipolar transistors—especially power transistors—primarily because of the difficulty in obtaining the required purity levels.

As far as the 1970 market for r-f power transistors goes, it appears to be a fast growing one. In Japan, the people at Mitsubishi see a $10 million market this year—more than 300% over 1969—while those at Nippon Electric and Toshiba are not quite so optimistic, they see $6 million and $3 million, respectively. In Germany, marketers at Siemens forecast $4 million in sales for West Germany and $14 million for Western Europe while TI sees the European market at $9 million.●
'Make-before-break' mode improves FET switch

By Leonard F. Halio

When field effect transistors are used to switch analog signals spikes often are transmitted with the signals. And a finite resistance associated with the FET varies with temperature changes, introducing further inaccuracies. These problems can be overcome with a circuit scheme using two FET's operating in a make-before-break mode. Here, one of the transistors is switched on prior to gating the analog signal. This provides a low-impedance path to ground which minimizes the spikes.

Since the channel resistance of the FET and its input capacitance are inversely proportional, merely attempting to lower the channel resistance will not eliminate the problem. Input capacitance would increase when channel resistance is decreased, allowing the gating signal to be coupled through the FET's channel and into the analog system.

In the circuit configuration, a positive gating signal turns on transistor Q1 while Q2 remains off, thus allowing the analog signal to be transmitted to the output via the operational amplifier LM 302. Since Q1's channel resistance is in series with the 1,000-megohm input impedance of the operational amplifier, variations in the smaller channel resistance over wide temperature ranges can be ignored.

When the analog switch is opened by a negative-gating signal, the time constants of the associated drivers of Q1 and Q2 are adjusted to allow Q2 to turn on before Q1 turns off—hence, the make-before-break scheme. Any spike coupled through Q2 sees a low-impedance path through Q1, effectively reducing the spike to a negligible value.

This process is repeated when Q1 turns on and Q2 turns off. The coupling signal will see a momentary low-impedance path through Q2 to ground, reducing the spike to a negligible value.
Short-circuit protection for voltage regulators

By W. Granter
Central Research Laboratories, Shortland, Australia

A short circuit occurring at a voltage regulator's output could destroy the regulator's series pass transistor. But if a transistor network is added across the series pass transistor terminals the increased current due to a short circuit will be partially drained off by the added transistor. This would hold the power dissipation of the series transistor within acceptable limits.

A typical voltage regulator, the µA 723, provides a 15-volt output with a load current of 40 milliamperes. If the load terminals of the device (RV0 and ground) are short-circuited, the output voltage adds to the normal voltage drop across the series pass transistor, Q1, in the output stage of the regulator. The new 24-volt supply voltage combined with the 40-ma current increases power dissipation to 960 milliwatts, overloading the regulator by 160 mw.

The modified circuit works as follows. During normal operation, V0 is approximately equal to 15 volts. Transistor Q2 is biased off by an appropriate selection of values for R1 and R2 and has a standby current drain of 10 ma. When RV0 is short-circuited to ground, the voltage across the series pass transistor (Vc to Vo) rises, causing the voltage across R2 to rise to a voltage exceeding the 0.6-volt turn-on value for Q2. Current then is drawn through Rs, reducing the load carried by Q1. Rs is selected to allow Q1 to draw 10 ma while 85 ma flows through Q2 and R3. Now the power rating of the series pass transistor is not exceeded and the voltage regulator has a built-in short-circuit protection.

The modification protects any series pass transistor when the values for the resistors and the current limit for Q2 are calculated to share the load with the transistor to be protected.

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Series bypass. An additional transistor circuit connected across the series pass transistor protects the series transistor from overloading if a short circuit occurs across the regulator's output. Transistor Q2 is biased into conduction during a short circuit, draining off the extra current.
Two op amps simplify design of oscillator

By Dennis J. Knowlton
University of Wyoming, Laramie

A voltage-controlled oscillator, of the type commonly used in analog-to-digital converters, can be quickly built with readily available components. The frequency, which is linearly proportional to an input voltage, varies from 0 to 1 kilohertz over an input voltage range from 0 to 5 volts.

The oscillator comprises three parts: an integrator, a voltage detector (Schmitt trigger), and a reference voltage network. The integrator, built with operational amplifier $A_1$, converts a positive input voltage to a negative-going ramp at its output terminal. Op amp $A_2$ acts as a Schmitt trigger with thresholds—determined by resistors $R_4$ and $R_5$, and by the voltage reference attached to $R_4$—of -2 volts and -7 volts.

The output of the integrator is initially at 0 volt. The Schmitt trigger's output sits at -12 volts. When a positive voltage is received at the input, the integrator charges negatively until it reaches -7 volts, whereupon the Schmitt trigger fires and produces an output voltage of 12 volts.

The 12-volt output is fed to the gate of field effect transistor $Q_1$, which immediately turns on. This action causes the integrating capacitor, $C_1$, to discharge and clamp the integrator's input to -7 volts. The result is that the integrator now begins to deliver a positive-going ramp at its output terminal.

The integrator's output voltage continues to increase toward 0 volt until it reaches the Schmitt trigger's second firing voltage of -2 volts. At this voltage, the output of $A_2$ returns to -12 volts, shutting off $Q_1$. When the integrator's output voltage reaches 0 volt, the cycle is completed and a new cycle begins.

Diodes $D_1$ and $D_2$, and resistor $R_6$ stabilize the -7-volt trigger point against variations in the -12-volt supply. The reference network supplies the voltages for the trigger points and provides a common input bias compensation for the op amps.

The oscillator remains linear to within 0.1% over a frequency range of 0 to 1 kHz. The output frequency remains stable to within 0.1% for variations in the supply voltage up to ±15%.

Voltage-controlled. The frequency is linearly proportional to the input voltage. When the integrator reaches -7 volts, the detector switches to 12 volts. $Q_1$ turns on, discharging $C_1$ and the integrator begins to charge toward 0. At -2 volts, the detector again fires and produces a -12 volt output.
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Taking a look at color EVR from the inside out

Headed by Peter Goldmark, a team from CBS Labs explains how electron-beam recording allows the use of very fine-grain film that has a high capacity of data storage despite a very small frame size.

Electronic video recording (EVR) is the Columbia Broadcasting System’s video counterpart of the long-playing record. And just as the lp is a low-cost, high-volume method of storing and reproducing sound, EVR represents an economical means to store television programs and to play them back over a black-and-white or color-tv set.

During EVR’s 10 years in development, circuits were built to translate standard National Television Systems Committee signals into a format suitable for driving an electron-beam recorder; other circuits were needed in the player to translate EVR signals back into the NTSC format. Also required was a real-time electron-beam recorder to produce 60-frame-per-second masters, scanning each frame with 525 lines. Film with high resolution and low cost had to be found. And a completely automatic playback unit had to be designed.

In the EVR system the suitcase-size player is connected directly to a tv set’s antenna terminals. An EVR cartridge, which looks like a thick 45-rpm record, fits onto the player’s spindle; the program stored on the cartridge’s film is played on the tv screen. With the current thickness of film, one cartridge holds 25 minutes of color video or 50 minutes of black and white, plus associated sound tracks (In Europe, because of the 50-field tv standards, a cartridge holds 30 minutes of color or 60 minutes of black-and-white.). Provision is made for stopping the film to view individual frames.

EVR’s picture quality on a tv receiver is equal to the best broadcast picture. On closed circuit, monochrome resolution can reach 500 tv lines; color resolution is limited by a tv tube’s phosphor-dot structure—usually 400 lines. EVR will find applications in home entertainment, education, and marketing. And since an EVR cartridge can hold 100 billion dots of varying shades of gray it has potential in the information-storage area.

Making an EVR film begins with translating a standard NTSC tv signal, usually from a video tape recorder or film, and driving a two-gun electron-beam recorder with the new signal. Silver-halide film, made especially for EVR, moves under the two beams, each scanning half of the film’s surface. In black-and-white programming, frames of images 0.10 inch high and 0.13 inch wide are formed on both halves, each representing 25 minutes of video. In color programming, images go onto only one half; the other half holds encoded color information. This master film, made by electron-beam recording, is turned into copies through high-speed printing and processing. The copies are wound on cartridges, ready for playback.

A suitable recording method had to meet many requirements. Electron-beam recording offered a number of advantages over laser and cathode-ray tube recording. Extremely fine-grain films sensitive to high energy present in an electron beam have a high capacity for data storage and can be used to make copies, which then can be played back by a black-and-white or color tv.
storage. Fine-grain film leads to very small frame sizes, making it economically possible in EVR to print 60 frames per second, instead of the 30 frame/sec rate standard in tv. This leads to a simpler playback unit since the film can be run at a constant speed. Besides, this higher film speed integrates the effects of spurious signals, such as those caused by grain and other film imperfections.

Another advantage: electron-beam recording makes it possible to modify the original tv signal and pre-compensate for subsequent distortions by tailoring the electron-beam recorder’s input to give a predetermined frequency boost and to introduce grayscale correction.

A video tape is the first step toward a master. Although the EVR recording system could operate with real-time signals from tv cameras, video tape or film, editing and color balancing can be made better by prerecording all program material onto video tape.

For color EVR recording, the NTSC color signal from a video tape recorder goes into two parallel channels, one for the color signal and the other for the luminance. In black-and-white recording, only one channel is used.

At the beginning of the color network a filter passes the standard NTSC color signal, composed of an in-phase component, $E'_i$, and a quadrature component, $E'_q$, and sends it to the NTSC-to-EVR translator. There, the signal modulates a 1.8-megahertz carrier, $f_c$, producing a signal $E_c$, which is given by $E_c = E'_0 \sin (2\pi f_c t) + E'_1 \cos (2\pi f_c t)$.

The translator adds two other signals to $E_c$: a pilot signal $E_p$, and a correction signal, $E_v$. In the EVR system it’s essential to record color signals so they can be reproduced independent of the recording and the playback unit’s scanning linearity.

To provide a reference carrier for the demodulation of $E_c$ in the player, $E_p$, an unmodulated signal with half the frequency of the color carrier’s frequency is added to $E_c$. This way nonlinearities, raster-size changes in the player, and film shrinkage, don’t interfere with proper demodulation because the phase relationship between the color and pilot carriers stays constant.

The translator’s output, $E_M$, is simply the linear sum of these three signals; i.e.,

$$E_M = E_c + E_p + E_v.$$

$E_M$ goes through a gamma corrector, which ensures that the entire EVR system, from recording through playback, approaches the performance of a unity gamma.
... the synchronization signal produces a small, clear window in the film between the frames. On replay, the synchronization is sensed with a lamp and detector ...
for the film magazine, one for the film drive, and the third for the electron guns and the beams. To ensure that the beams exposing the film are properly focused, and to provide for adequate filament life, this third chamber is kept at a pressure less than \(10^{-5}\) torr. There's virtually no loss of resolution in the recording process—the beam's diameter is held to 0.0001 inch, allowing the recorder a resolution of 200 line-pairs per millimeter.

The two guns simultaneously scan the film moving through the chamber. Their horizontal scan rate is the standard tv horizontal rate of 15,750 Hz. The film moves vertically through the chamber at 6 inches per second, or 60 frames per second; the beam's vertical scan rate is 12 inches/sec. A beam begins scanning at the top of a frame; by the time that frame has moved a distance of one-frame length—0.10 inch—the beam has reached the bottom of the frame. A short blanking period occurs, and the beams fly back to the top of the next frame.

The beam modulated by the color signal produces what appears to be rows of dots on the half of the film it scans. Since the horizontal scan frequency, the pilot frequency, and the color carrier frequency are integral multiples of each other, the dots occur at the same spots on each scan line as long as the colors are the same. Therefore, the color-encoded half of the EVR film appears to be made up of thin vertical stripes, varying in spacing according to the color information.

The luminance side, which contains images just like a standard piece of monochrome film, is scanned by the second beam; it's controlled by pulses coming from the 14-MHz video sampling gate. Superimposed on its 12 inch/sec vertical scan deflection is a narrow vertical deflection caused by a 14-MHz wobble signal. It's adjusted so that the video information associated with the delayed luminance signal is recorded when the beam is at the top of the wobble, while the undelayed information is recorded at the bottom. Thus, during one horizontal scan two dotted lines are traced, each corresponding to the information contained in a different field on the original piece of video tape.

The film drive in the electron-beam recorder very accurately maintains the film speed of 6 inches per second (5 inches in Europe). The drive, an electronic servo type, controls film velocity by locking onto the vertical scan frequency and the synchronization signals from the video tape recorder. Interlocking of the EVR film drive with the tape recorder drive is accomplished at the beginning of the recording process by counting the vertical sync intervals on the video tape and the perforations on the edge of the film. The film drive compares the counts and varies the film's velocity until the counts coincide. Interlock between the two drives occurs within 12.5 seconds of the time the film starts to move through the electron-beam chamber. During the 12.5 seconds 750 perforations are counted. A solenoid-actuated punch in the electron-beam recorder puts a cue hole at the beginning and at the end of the 750-perforation interval. For the first 650 counts the two guns are unblanked and vertical scan is turned off. While the next 100 perforations pass, the guns are blanked and the vertical scan is turned on to prepare for recording the first program frame. The black-to-white transition after the 650th perforation is used as a reference cue for recording sound during the printing operation. Sound will be placed on the copies as magnetic stripes down both sides of the copies. In monochrome recording, each stripe can carry the sound for one strip of frames. In color recording, the two can produce stereo sound.

The silver-halide film used by the electron-beam recorder to make a master is doped to make it more sensitive to the beams. This film, developed by Ilford Ltd.,

**Playback.** The player sends EVR film through a pair of rasters taken from the crt's face. The two beams modulated by the film go to photomultipliers, whose outputs are converted into a composite NTSC signal. Put onto an r-f carrier, the signal then can go directly to a tv's antenna terminals.
England, for EVR, has a resolution of 800 line-pairs per millimeter; crystal size is less than 1 micron.

The film in the recorder is 40 millimeters wide and has four separate two-track masters. The gun chamber sits on two trunions and can be indexed to four horizontal positions. In practice, one master is made; the film is rewound, the gun chamber is moved over a notch, and the second master is shot on the same film. Two more copies of the program master film are made in a similar manner.

The four-master film and real-time recorders have been completed, one for Europe and another for the U.S. and will be used in actual production this summer. EVR films made until now have been produced on lab-model recorders which put out one-master film. Since they don't have the video-sampling capability, they don't run at real time.

A multideck wet-gate printer, built for EVR, turns out copies of the master in a contact process. Through use of multiple heads this printer generates 16 copies—four copies to a 40-mm wide film strip—complete with sound tracks each time the master passes through. A 25-minute color program is generated every 18 seconds (14.5 seconds in Europe). The copies are developed by standard photographic methods. Since EVR films containing color programming are themselves monochromatic, developing of all EVR film can be done in a black-and-white machine.

The developed strips are slit into four 8¾-mm wide copies. The beginning of each copy is attached to a plastic leader, which is used to unwind a cartridge in the playback unit. The copy then is wound onto a cartridge.

Right now, copies are made on a high-resolution, low-cost silver-halide film. But when it's available in quantity, Diazo film may take its place. Here, a light-sensitive dye, completely grainless, is used instead of a halide emulsion. Diazo offers much greater resolution—1,000 line pairs per mm—than silver-halide film, and could cost about half as much. Diazo film also is thinner, which could increase the length of programs on a cartridge. High-quality prints already have been made with Diazo film.

The playback unit displays the film by moving it through light beams coming from a crt. The light passing through the film is sent to photomultipliers which generate luminance and EVR color signals; the color signal is converted to NTSC form; both signals are put onto a radio-frequency carrier; and this modulated carrier is sent to a tv set's antenna terminals for relay to the tv screen.

When a cartridge, 7 inches in diameter and ¾-inch thick, is placed in the player and the play button is pressed, the film's leader is threaded through the deck and attached to the player's takeup reel; then the player...
Those who served in developing EVR

The development of EVR was a team effort headed by Peter Goldmark at CBS Laboratories. Robert Castri­gnano, William Glenn, Donald Ridley and Andrew Tarnowski contributed to the development of the electron-beam recorder. C. Russell Dupree, Barnard Erde, and Ivan Purt helped to design the film and optical system. Abraham Goldberg was responsible for the player; Patrick Grosso developed its cathode-ray tube; and Robert Rhoades headed the mechanical design. John Christensen, Dennis Gabor, John Hollywood, Renville McMann, and John Wistrand contributed to the overall design of the system.

automatically begins showing the program. A large knob on the player's front allows the user to manually move the film when looking at individual frames in a step-by-step mode.

The crt generates an ultraviolet raster with 10-line-pair-per-mm resolution on its 2¾-inch-diameter flat faceplate. The raster's intensity is held constant by a photoresistor which controls the beam current. As the Pl6 phosphor ages, the current needed to maintain the rated light output goes up from 10 to 80 microamps over the life of the tube.

The twin tracks of the EVR film are scanned by a dual optical system, which consists of two lenses, two rhomboidal prisms, a lens mount combined with a film gate which holds the film in a cylindrical image plane. Each lens focuses the raster from the crt onto an area on the image plane, forming two identical rasters on the film, one for each track.

The collector optics on the other side of the film consist of two lucite pipes. These collect the modulated light coming through the color and luminance frames, and transmit each light channel to its own photomultiplier. A film transport, similar to the type used in magnetic tape recorders, moves the film through the optical assembly. Since the timing of the crt beam depends on film speed, this speed can vary a little.

Film speed and vertical scan rates were chosen so that the playback unit scans each frame in much the same way that the frames are scanned by the electron-beam recorder. As a frame moves through a distance equal to one frame length the raster is completed on that frame. Flyback occurs, and the raster is traced out over the next frame. The original scan lines on the frame aren't retraced by those rasters as they move across the frame.

The modulated light beams are converted into EVR video signals by the photomultipliers. This signal from the color photomultiplier contains the color and the pilot signal; it goes to the EVR-to-NTSC translator. There, the color signal is separated from the pilot by two filters, a 1-to-2.5-Mhz bandpass filter in one channel and a 1-to-2.5-Mhz band-reject filter in another. The pilot signal's frequency is doubled to 1.8 Mhz and mixed with a locally generated 3.58-Mhz signal. The resulting 5.38-Mhz signal is selected by a bandpass filter and applied to a second mixer together with the EVR color signal, whose carrier frequency is 1.8 Mhz. The frequency of the translator's output remain constant at 3.58 Mhz, regardless of any shift in EVR color frequency.

The phase-versus-frequency characteristics of the color and pilot channels are kept equal to prevent hue errors due to scan-velocity changes. If the two signals undergo equal phase shifts in the translator, the errors cancel.

Following the mixer, combined blanking, sync and burst signals are added to generate the composite NTSC signal. A color killer in the player disables the color network whenever a monochrome film is played. Otherwise spurious beats would show up in the final picture on the tv screen.

The most convenient way to feed pictures to a tv set is through the antenna terminals. In the EVR players this is done with a miniature transmitter operating on an unused vhf channel. Double-sideband video modulation is employed for economic reasons, but television receivers accept signals modulated this way just as they'd accept the vestigial sideband signals received in broadcasts. A crystal-controlled oscillator generates the r-f carrier, which is applied to one input of an analog four-quadrant multiplier. The NTSC composite signal and the 4.5-Mhz sound carrier are applied to the other input.
Camera’s infrared eye focuses on new vistas for ranging

New device focuses a movie camera by shooting pulses of infrared light at an object and controlling detector’s position with reflected energy, says Giovanni Odone of Switzerland’s Paillard S.A.; some important new applications could result with an accuracy that’s in the nanosecond range.

In photography, it’s easy to measure short distances when the object being ranged is a point source of light, and when other light sources don’t interfere. Since the image-to-lens distance is a function of the lens-to-object distance, the point source range can be determined by measuring the distance between the camera’s lens and the spot where the lens focuses the light from the point source—an easy job for a photodetector.

In practice, such ideal conditions don’t occur. The object rarely resembles a point source, and ambient light reduces accuracy. However, if the camera projects a narrow beam of light onto the object, the portion of the object struck by the beam appears as a point source to the camera. Furthermore, if this beam is chopped at some known frequency, reflected light can be distinguished from ambient light. Theoretically, the chopped beam can have any wavelength, but for many applications, particularly photography, the beam should be invisible; that’s why the AIR uses infrared.

The first application for the AIR will be as an attachment to the Bolex 16 PRO, a popular newsreel camera. The camera’s focusing ring will be mechanically coupled to the attachment’s detector assembly; as the assembly moves toward an equilibrium point, the camera’s lens brings the object struck by the projected infrared light into focus. The entire procedure for focusing the camera...
takes no more than a few hundred milliseconds.

In the AIR built for the Bolex camera, the i-r projector consists of a 1-watt tungsten lamp sitting at the focal point of a curved mirror. An optical filter between lamp and mirror absorbs visible light and passes infrared, which the mirror focuses into a narrow beam. The beam is only 6 inches in diameter 60 feet from the projector.

A multivibrator drives the lamp with a square-wave voltage whose frequency varies between 20 and 70 hertz. The exact value depends on the outputs of the rangefinder’s detector assembly; as the output goes up, so does the frequency, in an effort to decrease the output. At higher frequencies, the projector’s output energy is low and, because of their slow rise time, the photodetectors put out less voltage for a given input.

If frequency weren’t controlled in this way, the rangefinder’s sensitivity would vary with the object’s range.

When the projected beam strikes the object, some of the incident energy is absorbed; the rest is reflected. Back in the rangefinder a second curved mirror, a bit smaller than the first, collects part of the reflected light, forming it into a ring-shaped image that encircles an axis running through the centers of the two mirrors. The image’s location on the axis depends on the angle at which the reflected energy strikes the collector mirror. This angle, in turn, is controlled by the distance measured between the object itself and the mirror.

The rangefinder locates the image with a feedback loop consisting of a stepping motor, its input circuitry, and a detector assembly. The last item comprises two photoresistors facing each other, a ring-shaped mirror between them, and a carriage that holds the resistors and the mirror. The photoresistors’ outputs run the motor, which moves the assembly back and forth between the collector mirror and the projector. The detector mirror’s inside diameter is slightly larger than the image’s inside diameter. When the detector mirror and the image plane coincide, half of the incoming beam—a ringshaped portion whose inside diameter equals the inside diameter of the mirror—strikes the mirror and reflects onto the photoresistor between the detector and collector mirror. The remainder of the beam goes through the detector mirror and strikes the other photoresistor.

If the detector mirror isn’t at the same spot as the image plane, one of the photoresistors receives more energy than the other. This activates the motor, which moves the assembly to the equilibrium point.

The feedback loop’s input circuitry removes noise from the photoresistors’ outputs and then turns them into d-c inputs to the motor.

Noise, a product of ambient light sources—sunlight or light bulbs, for example—is a source of error in two ways. Just by sending radiation to the photoresistors raises the resistors’ output levels, obscuring the reflected-beam signals. In fact the outputs’ signal-to-noise ratio can be as poor as —140 decibels.

In addition, ambient sources also may send more radiation to one photoresistor than to the other, causing the detector to come to rest at a false null point.

But the rangefinder’s design incorporates several features that lessen the effects of ambient light. First the rangefinder itself—the resistors, the detector and collector mirrors, and the projector—is built to be as symmetrical as possible.

Close to the photoresistors is a lamp which sends to them a constant level of radiation, more than would result from ambient sources. This holds the resistor’s d-c outputs constant by reducing the effects of changes in ambient conditions.

Finally, the collector mirror, being smaller than the projector mirror, reduces the relative amount of ambient light reaching the detector assembly. At first glance,
this may seem incorrect because where there’s no ambient light the optimal approach is to make both mirrors the same size. The amount of projected infrared that gets back to the detector assembly goes up when the area of either the projector or the collector mirror increases. But the amount of ambient light that reaches the detector assembly isn’t affected by anything that happens to the projector mirror.

The input circuit overcomes the very low signal-to-noise ratio with synchronous detectors, controlled by the same multivibrator that chops the projector’s drive voltage. In the circuit, the photoresistors’ outputs go to a differential amplifier whose output is filtered and then sent through two channels. One contains just a synchronous detector; the other has a 180° phase inverter and a detector. By using two channels instead of one, the rangefinder can update commands to the stepping motor at every half cycle of the multivibrator’s frequency instead of once every cycle.

The output from the differential amplifier contains the reflected-beam signal and noise. The signal has the same frequency as the projected beam—it’s the same frequency as the multivibrator’s. In addition, the signal and multivibrator output are either in phase or 180° out.

The figure shows one of the rangefinder’s synchronous detectors. Two transistors, Q₁ and Q₂, act as switches. During half a cycle Q₁ is open and Q₂ is closed; the

**No noise.** This synchronous detector separates the photoresistors’ signals from noise. Since the signals and the multivibrator’s output, which switches Q₁ and Q₂, have the same frequency, the detector responds to the signal but not to noise.
Null-point search. The signals from the photoresistors circle back to control the motor that positions the carriage on which the resistors are mounted. These signals also fix the repetition rate of the voltage that drives the projector.

Focus. First application for the rangefinder is this Bolex 16 PRO movie camera. The AIR's photoresistor carriage and the camera's lens are mechanically coupled. As the carriage moves toward a null point, the camera comes into focus.

reverse occurs during the second half of the cycle. The detectors can be understood by using superposition and first considering the effects of the reflected-beam signal coming from the amplifier.

Suppose capacitor C is uncharged, and that Q1 has just opened. The signal flowing through the load resistor, R1 charges C, but not completely, because time constant R1 C is selected to have a value that's much longer than the multivibrator's period. During the next half cycle, C loses only a small portion of its charge because the resistance of the load resistor, R2, is much greater than resistor R1.

This process repeats with the amount of charge stored on C increasing each cycle until after a few cycles it reaches some maximum value. After that the amount of charge lost into R2 when Q2 is open equals the amount of charge buildup when Q1 is open.

Since the reflected-beam signal's frequency and the switching frequency of Q1 and Q2 are equal, C always adds a small amount of charge during one half cycle and loses the same amount of charge during the next half cycle.

Thus, the detector's output due to the signal are pulses whose amplitude is proportional to the signal's amplitude and whose polarity depends on the phase relation between the signal and the multivibrator's output.

On the other hand, noise isn't synchronized with the openings and closing of Q1, and Q2. Therefore, averaged over many cycles, the amount of charge the noise adds to C when Q1 is open is zero, as is the detector's output due to noise.

The pulses from the two synchronous detectors go to a triggering circuit which controls a pair of AND gates. The pair of AND gates put out square waves which are integrated. These integrated square waves are sent to the motor as a continuous signal.

The rangefinder has a third synchronous detector, which is used to adjust the multivibrator's frequency. In addition to going to the input circuit, the photoresistors' outputs are added in a high-gain amplifier sent to the third detector. Its output pulses are rectified and sent back to the multivibrator. If the signals from the photoresistors are strong, indicating that the subject is close, the multivibrator's frequency is boosted. If the signals are weak, the frequency drops. ●
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There's a better way to design a character generator

Gene Carter and Dale Mrazek of National Semiconductor found the way by teaming up MOS read-only memories and shift registers; the memories shape the characters for CRT readout while the registers handle refreshment.

In the design of a cathode-ray tube alphanumeric display system, the portions that are most often taken for granted are those that store information concerning the shape of each character and refresh the character during subsequent sweeps of the electron beam. Yet decisions that relate to font storage and character refreshment have a great bearing on total system cost.

In remote computing, for example, if fonts are stored in a central processor, the user would continuously pay a stiff price in the form of central-system overhead and communications costs. On the other hand, if the fonts are stored in the remote terminal's circuitry, costs can be reduced. The latter is true largely because of the advent of memory techniques rooted in large-scale integration.

Storage capacities of metal oxide semiconductor chips have increased at least tenfold during the last few years. And an attendant lowering of threshold voltages makes MOS memories and shift registers compatible with bipolar logic and standard power supplies. More importantly, a combination of MOS and transistor-transistor logic can generate characters faster than they can be handled by moderately priced CRT video circuitry or printer mechanisms. And inexpensive MOS shift registers can be used to recirculate data in order to refresh characters on subsequent sweeps. What makes MOS/TTL character generators even more attractive is that they cost about half as much as those built exclusively with bipolar monolithic circuits and devices.

The latest read-only memories (ROM), such as the MM 5240 and the MM 5241 manufactured by the National Semiconductor Corp. of Santa Clara, Calif., represent the equivalent function of some 3,000 diodes and 50 packages of bipolar gates; the 5240 stores 64 characters in an 8-by-5-bit configuration, while the 5241 stores the same number of characters, but in a 6-by-8-bit configuration. With either ROM, characters in the standard 5-by-7 matrix can be generated with a single chip, the extra capacity being deployed for unusual special needs, such as dropping comma tails below the other characters. Each chip contains decoding logic and sense amplifiers. And the chips can be operated in tandem to generate higher-resolution font matrices.

The input-output configurations of these chips, shown on page 108, are for a standard ASCII-addressed font. The six-bit ASCII code words will address any of 64 characters. The control logic generates the three additional address bits needed to select the individual lines or columns of dots that form the characters in the 5-by-7-by-64-dot matrix. The output bits forming each dot line or column are presented in parallel, then serialized by a TTL register and used to control the CRT beam or the printer mechanism. To simplify the selection process, ROM's are programmed to generate the lines or columns in the correct sequence when addressed by the sequential

Through the hoop.
Getting a quick fox to jump over a lazy dog is one thing; writing about the event on the face of an oscilloscope is quite another. However, with MOS read-only memories and shift registers, the latter problem can be reduced to logic design. And with low-threshold MOS, which is compatible with TTL logic, the whole thing is as easy, well... as jumping over a lazy dog.
outputs of a TTL counter.

The job isn’t finished when the character is produced, however, for the image on a CRT must be regenerated, or refreshed, rather frequently—a minimum of 30 times a second—unless the CRT has a storage phosphor. A suitable device for this purpose is the MOS shift register, which can recirculate incoming data, then readdress the ROM, thus refreshing the display.

Refreshment of CRT images has recently become quite a popular application of MOS shift registers, since registers capable of regenerating up to about 5,000 bits—enough for a display of over 800 characters—can be built less expensively with MOS dynamic registers than with delay lines, which are often painfully temperature sensitive. In the past, MOS shift registers of high capacity (say 512 bits) as well as high speed were not easily obtainable. But registers, like National’s MM 5016, can now easily run at clock rates above 2 megahertz—double the speed of early mass-produced registers. And some, like the MM 5081, can drive neon displays directly.

The basic refresh mode, shown on page 109, limits the number of characters that can be displayed. A better way of generating and refreshing raster-scan displays, especially those with many rows of lines of characters, is on top of page 112. The timing and logical implementation for a multiple-row system is seen on page 110. The last two circuits require high-speed recirculation of only

**Two contenders.** By June, National Semiconductor will have on the market two 64-character read-only MOS memories that were specifically designed to generate characters for CRT displays. The MM5240 (above) supplies 8-by-5-bit characters upon receiving information in the form of the standard ASCII six-bit code. The MM5241 (below) is suitable for vertical-scan applications and for driving line printers.
m bits of information at a time.

As before, coded data from a communications link or the console keyboard passes through the registers and addresses the character generator. In these examples, the six-bit ASCII input and the three-bit control-logic input generate raster-scan character formats that allow a conventional TV monitor to be used as a display. Communications codes other than ASCII can be used if the ROM's are reprogrammed, or if a code-converting ROM is inserted in data line.

If the ROM contains a 5-by-7 font, each five-bit character-line output will form up to five horizontal bright spots on the CRT. That is, each ROM output generates one-seventh of each character in a row of displayed characters. The output is serialized by the TTL register and used to modulate the intensity of the CRT beam as it sweeps across the screen.

The refresh-memory registers are divided into M-N and N sections to facilitate page displays. M is the total number of characters displayed in several rows (lines of the page), and N is the number of characters in each row. To form such a display with single-loop registers, as in the basic refresh mode, would take seven recirculations of all M data words during each refresh cycle of the CRT.

Assume that on the first sweep of the CRT beam, the ROM is being addressed by the six register outputs representing characters N1, N2, N3, etc. The first horizontal five-dot line of each character in the display row is displayed in sequence. Then the line-address inputs to the ROM from the control logic change to a second state preparatory to displaying the second row of dots. At this time, N1 has completed its recirculation to the N register's outputs. Thus, on the second CRT sweep, the second series of five-dot lines are displayed horizontally for all N characters. At the end of seven recirculations, the complete row of N characters is on the display.

Now the contents of the N register are fed back to the input of the M-N register, which is clocked to load the N register with a second group of N characters. The M-N register is then held still while the N register recirculates seven times to generate the second row of characters on the display. This cycle repeats until all M characters are on the display, whereupon the first group of N characters is reloaded into the N register, and the entire process repeated to refresh the display.

Besides generating the line-address inputs (that is, the number of recirculations of the N register), the control logic keeps track of the number of dots and space in the output bit stream. The spaces between characters in a display row are inserted as "0" bits when the ROM outputs are materialized by the TTL register. The counters also control the loading and recirculations of the MOS registers in the refresh-memory subsystem.
A multiple-row raster scan display could be generated with the M-loop technique, but the implementation is difficult and impractical. In fact, the M-loop technique is more appropriate for single-row displays. Using this method of display, all M characters to be displayed must recirculate seven times to generate a 5 by 7 horizontal-scan. Therefore, all stages of the registers must operate at the full character rate.

To form several rows with a single-loop memory requires an interlaced scan rather than an ordinary raster scan. The first series of five-dot lines is generated by the first N character outputs as before, but the next set of N inputs to the ROM will generate the first group of five-dot lines in the second row of characters on the display. Therefore, the beam must jump to the new line position. To display four rows of 5 by 7 characters, for instance, would require a staircase generator that could step the beam by the height of nine scan lines (seven dot lines, plus two blank spacing lines between rows) three times after the initial scan. Then, as the second of the seven recirculations begins, the beam would have to be shifted an additional line to start the second series of the line scans—and so forth.

The M-N/N technique does not require any more register stages than the M-loop technique and significantly reduces control and drive-circuit requirements—again producing a lower cost per function.

The technique employed in the M-N/N refresh memory is called "clock modulation." In other applications, it has already been found to significantly reduce total storage cost.

Furthermore, the technique allows very high-density MOS circuits, produced by relatively inexpensive low-threshold (bipolar compatible) processes to operate at very high character rates.

The raster-scan system uses nine clock intervals to generate a row of characters on the display. Seven are for the high-speed recirculations. During the other two intervals, the first N characters are fed back from the output of the N register to the input of the M-N register while the N register is loaded from the M-N register with a new row's worth of characters. Since two intervals are used for this operation, the registers operate at only half the character rate. The rest of the time, the M-N register is quiescent. Its average clock frequency is only about 11% of the character rate.

In other words, most of the refresh memory (perhaps 90% in a large-display system) operates at only half the character rate (say 1 MHz instead of 2 MHz) two-ninths of the time. The savings in the drive network alone can be judged from the power-frequency plot for a typical MOS dynamic register, below, on page 112. In addition, the designer can increase the number of characters generated per refresh cycle, for a larger display, or

... the job isn't finished when the character is produced. Unless the crt has a storage phosphor, the image must be refreshed frequently ...
Nitty gritty. A specific logic diagram for implementing a multiple-ROM raster-scan display system contains parts needed to generate multiple-character lines. Separate clocks and counters handle data housekeeping functions. All MOS registers and ROM's are compatible with TTL. Output can be used to drive either oscilloscope or CRT monitor inputs.

Character size—every bit counts

Human requirements have considerable bearing on a number of decisions in the design of a cathode-ray tube alphanumeric display, specifically those that relate to the selection of the font—that is, the style and shape of letters and numerals. A crucial decision is the determination of the number of bits that define a character, inasmuch as the size of the matrix used to form characters profoundly affects the amount of memory needed to store each character. Naturally, the more elements in a matrix, the more legible the characters will be. A good working minimum is the standard 5-by-7 array, which requires a storage capacity of 2,240 bits. But with the advent of modestly priced large-scale integrated read-only metal oxide semiconductor memories, designers are starting to gravitate toward an 8-by-10 matrix, which looks rather convincing. Ultimately, a 12-by-16 matrix will become practical, allowing almost optimum legibility and sharp definition, even of small lower-case letters.

At present, system designers considering the use of complex fonts for low-cost CRT displays run into a serious problem. The least expensive CRT's are those used in the ordinary black-and-white television. However, they have limited bandwidth, making it necessary to hold down not only the number of bits in the font matrix, but also the number of characters that can be displayed simultaneously. Ordinary CRT's have a bandwidth of about 4 megahertz, of which only 2.5 Mhz is generally useful. But the number of characters per page is proportional to the bandwidth (and inversely proportional to the bits per character and refresh rate). Using a 5-by-7 font with one spacing bit (6 by 7, total) at a 60-hertz refresh rate, each displayed character needs 2.52 kilohertz. Thus, the limit for a TV CRT for 5-by-7 resolution is around 1,000 characters. But present MOS read-only memories can take as little as 700 nanoseconds to generate a dot line, or about 5 microseconds per character. That's 200,000 characters a second, or more than 3,000 characters at the same 60-hz refresh rate.

Luckily, there are alternatives to the use of oscilloscope-quality CRT's or storage tubes, both of which are fine, but expensive. One technique is to lower the refresh rate. For an obvious reason, 60 hz is convenient. But the eye can tolerate a rate as low as 30 hz without undue discomfort due to flicker. New CRT's with longer-persistence phosphors should facilitate further reduction of this parameter. Meanwhile, manufacturers of CRT monitors are trying to penetrate the new terminal market by improving bandwidth, and moderately priced video systems may soon sport bandwidths up to 10 Mhz. Finally, an ingenious approach could update techniques used in analog displays to form characters with "strokes,"—or sections of characters—rather than rows and columns of dots.

Bigger characters. Increasing the number of bits that form each character improves legibility, to be sure. But complex fonts require added circuitry. For example, a 5-by-7-bit character (left) requires only one MM5241; an 8-by-10 matrix (center) needs two—one for the upper half, one for the lower; while a 16-by-12 (right) needs four—one for each quadrant.

Keep 'em straight. For certain applications, such as line printing and simple CRT display, vertical-scan characters can be generated easily. But two distinct generating techniques exist: conventionally, a sawtooth deflection is used (left); an alternative is the pedestal display (right).
A bit of power. Power dissipation of a dynamic register used to refresh CRT character displays varies directly with frequency. The minimum-frequency-of-operation curve also has a similar shape.

Big boy. Larger raster-scan displays require a large "page" memory as well as a character-line refresh memory. With such a system, whole pages on the CRT can be displayed and refreshed. Other manipulations, such as roll up, roll down, and data insertion can be accomplished by slight additions to the circuitry. Increase the number of dot lines, for a larger font—or he can increase both.

Remember, though, that dynamic registers must be clocked to retain data. Nevertheless, the M-N register can be turned off long enough for practical applications. The guaranteed minimum frequency is temperature-dependent, since temperature affects charge-storage time. The minimum for National Semiconductor’s MM-series registers for example, is 500 hertz at 25°C, but that is not a display environment. At room temperature, the registers can safely be quiescent for as long as 2 milliseconds. Suppose the N register stores 40 characters and operates at 2 Mhz. The quiescent period can be as short as 40 by 7 by 0.5 microseconds, or 140 µsec. If standard TV raster timing is maintained, then the quiescent period will be 7 times 63 µsec, or 441 µsec. Obviously, the designer has great leeway in a number of parameters such as character rates, operating temperatures, and register capacities.

Other applications in displays for this technique include input-output buffering of data during reception and transmission, or during display editing and formatting through the console keyboard. The register rates can be adjusted via control logic to accommodate differences between input-output and recirculation rates. Note that the typical dynamic register permits data entry under TTL control into either register section.
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Electronic systems to strengthen the long arm of the law

Burdened by rising crime, law-enforcement groups are slowly turning to computerized communications and data systems to catch more criminals more rapidly

By Lois Vermillion

Electronics staff

Arrests for minor traffic violations will become a tool for solving major crimes if electronics-minded law-enforcement officials have their way. In one recent example, a speeder in the nation's Capital found himself charged with assault with a dangerous weapon and failure to appear in court. The speed cop had radioed the driver's license and vehicle tag numbers to his headquarters, where an operator keyed the data into a computer console for a search. Within seconds, the felony data appeared on the CRT display, and it was immediately radioed back to the traffic patrolman, who then put away his tickets and brought out the handcuffs.

The Washington Area Law Enforcement System (Wales) is one of several systems operating in major U.S. cities that are heralding a new era in the application of electronics technology to law enforcement. Wales and similar systems, many criminologists believe, are the answer to a soaring crime rate; by producing more and speedier solutions to crimes, electronic systems ultimately could provide an effective deterrent.

Resistance. Until the late 1960's, application of technology to law enforcement was handicapped by money shortages in most police departments and resistance among many old-line law enforcement personnel. But with the creation within the Justice Department of the Law Enforcement Assistance Administration, the funding problem is being attacked. Part of the LEAA program is to make matching block grants, usually on a 60-40 basis, to states and localities for systems improvements. Funding for these grants has risen from $11.3 million in fiscal 1969 to a planned $285.9 million in fiscal 1971.

Resistance to technology also is declining, as evidenced by increased use of equipment. When a policeman becomes convinced that a computer, for example, is not merely a gadget, but a device which actually can help him, he will accept it.

Wales is based on an IBM 360/40 computer, with a 360/50 backup unit tied into the Department of Motor Vehicles, and its sanitation, health, and welfare agencies. Right now the system is used for statistical work and some command-control functions; resource allocation is planned for the future. The system includes 39 IBM 2740 communications terminals, 12 IBM 2260 CRT displays, and four IBM 2911 switching units. The Motorola communications equipment was installed for about $700,000, and another $270,000 went to System Sciences Corp., which supplied the software requirements.

The Capital's police—or any other police agency with computer capa-
bility—can get data on wanted criminals and stolen items from the FBI's National Crime Information Center, (NCIC).

The need for instant nationwide data transfer is behind Project Search, LEAA's System for Electronic Analysis and Retrieval of Criminal Histories. Search, which will connect police departments, courts, and correctional institutions around the nation, has twin goals. First, by interconnecting all components of the criminal justice system, it will provide the capability to follow offenders as they pass through the system. Also, it will have an index at the national level with limited personal data on each offender and a summary of his criminal history. The central data bank would direct queries from states for more detailed information to the state with the most thorough "rap sheet." Without the central data bank "it's very difficult to determine whether a person has a previous criminal record," says Paul Sylvestre, director of the Search program. "A person could look like a first offender in one state, and have a record a mile long somewhere else."

Still in the planning stage, Search will begin a two-month demonstration beginning in July. For the demonstration, the central index will use a Burroughs 5500 at Michigan State Police headquarters. Ten states, each given $100,000 in LEAA funds, will participate, with five others observing. Evaluation of the system and possible redesigning will begin in September, and the first design should be out by the end of the year, says Search project coordinator Paul K. Wornell of the California Crime Technological Research Foundation. April 1971 is the target for Search to become an operational national system, and during calendar 1971, he says, the pressure will be on the states to tie into the Search network.

There is some question of the future value of NCIC and Search. One industry source believes that within 10 years, Search will have to go over completely to a central data bank, instead of the index system now planned. Eventually, he predicts, Search will end up at the FBI. However, another expert claims both programs will be obsolete in a few years, supplanted by a network in which the state computers talk directly to each other.

**Private problems.** Search also may run into trouble on Capitol Hill from privacy-conscious lawmakers. Sen. Samuel Ervin Jr. (D., N.C.), chairman of the judiciary subcommittee on constitutional rights, fears that the data bank, although "innocently created by behavioral scientists, can evolve into monsters of surveillance." Ervin accuses the Federal Government of a "boundless curiosity about private citizens." Because of the privacy issue, Sylvestre says, the Search project group is considering a provision for expunging certain records.

In St. Louis, a system provides an example of the capability sought by police agencies across the nation. St. Louis' was among the first major police agencies to make use of computers for municipal police management, emphasizing computer mapping of crime and allocation of manpower. The city uses an RCA Spectra 70/40 computer and an IBM 77/40 communications control system. Originally intended to store information on wanted persons, automobile licenses, and serial numbers of stolen items, the system has expanded to do such jobs as:
- Crime-mapping the city. The maps show the relationship of crime between districts, and give some indication of the times crime is most likely to occur, which beats need the most patrolmen, how many patrolmen are needed at certain hours of the day, and days of the week, seasonally, and under different weather conditions. The computer predicts the number of calls for service in a specific area during a specified time interval.
- Radio-event mapping. This is used to predict the number of calls and the number which will be serviced immediately under a given number of assigned units. Calls are broken down into eight priority classes, ranging from capital crimes to rescuing cats from trees.
- Car activity reports. These indicate how often and for how long patrol units are out of their assigned areas to handle calls for service in other locations.

Police morale in St. Louis, says Philip H. DuBois of Washington
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University, who did a study for the St. Louis Police Department and LEAA, has not been adversely affected by computerized command and control. In fact, he says, it is quite possible that morale has improved. His survey found that officers generally were more satisfied with their supervisors, who performed more efficiently after resource allocation was implemented.

Crime costs. Charles H. Rogovin, 39-year-old LEAA administrator, estimates that some $6 billion is spent annually by the entire criminal justice system, and various sources estimate the police portion at between 50% and 80% of the total. Equipment expenditures range between 5% and 10% of police budgets now, and the percentage will rise with greater acceptance of equipment and extra funds becoming available.

Rogovin, who many of his peers say is largely responsible for LEAA's success, has submitted his resignation to the White House effective in June, because of his discontent over the agency's three-part bureaucratic administration. Though there are likely to be no major program changes initially, there's a good chance that Congress will rearrange LEAA's administration, which could affect program orientation.

Under the 1968 Omnibus Crime Control and Safe Streets Act, LEAA was to be directed by three persons who must agree on major programs before any money is spent. Rogovin, a Democrat, has shared leadership with Richard Velde, a former aide to Sen. Roman Hruska (R., Neb.), and Clarence M. Coster, a former Bloomington, Minn., police chief. Both Velde and Coster are Republicans. Sources say disagreements over policy and funding are frequent. Velde, who helped write the Safe Streets Act, is considered the best bet as Rogovin's successor.

Congress may scrap the troika setup for a single-leader arrangement. There's talk on Capitol Hill of doubling or even tripling President Nixon's $480 million fiscal 1971 budget request for LEAA, and Congress is likely to be receptive to any change which promises better administration of funds.

The police have been the biggest users of advanced equipment so far, taking 79% of LEAA's matching block grants in 1969. Courts took 6%, and 14% went to reformatories and prisons. "We were not surprised that there would be a major commitment to the police," says Rogovin, "because the police are the largest component of the criminal justice system in terms of manpower and expenditures." The commitment to courts and corrections, though, will begin to rise in 1971, he says.

Growth. The size of the police market for electronics is called "tremendous" by most sources in the field, but none are able to give concrete estimates. The market is a viable one, and can grow, but "industry has to make the market," says Walter Key, electronics and communications program manager at LEAA's research unit, the National Institute of Law Enforcement and Criminal Justice. The market can be manipulated, says Key, according to the interest of the supplier.

An industry representative must go to a police chief, he says, point out the advantages of a product which may be new to him, and explain to the chief how he can orient his procedures to take advantage of the new technology. If industry will make this effort to deal with departments on an individual basis, says Key, a strong market for a device or a family of devices can be created. Key also notes that many law-enforcement systems managers may find a problem working in the highly fragmented law-enforcement field after dealing with a single procurement office at the Department of Defense.

Key makes an analogy between the law-enforcement and the color-tv markets. It's a next-door-neighbor type of situation, he says, in which a police chief says, "Let the next guy try out a new device, and if it is successful, we'll use it."

This problem has been compounded by the fact that police agencies rarely have exchanged ideas and information on their own technology applications simply because they rarely exchange personnel. This obstacle originated in a fact of police life: city and state lines of promotion usually are open only to the membership. Chiefs have historically come up through the ranks. In such a closed system, information exchange with other agencies can be retarded.

But through LEAA-sponsored technical sessions and conventions, such as the recent Law Enforcement Science and Technology Symposium held in Chicago, police departments are able to exchange information about their experiences with new equipment.

--- INFORMATION FLOW TO AND FROM CONTROL INDEX
-- INFORMATION FLOW TO AND FROM STATE FILE

**Ready for tests.** This is Search, an information-handling system which will be tried out this summer. As part of the study, the Justice Department's LEAA will make its first tests of electronic-facsimile transmission of fingerprints from computer files at the agency of record directly to inquiry terminals.

![Diagram](image-url)
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Communications is top police priority

Two roadblocks stand in the way of engineers who want to apply electronics to law enforcement. The biggest barrier is spectrum space: the police contend they don’t have enough, particularly in large metropolitan areas. Police barely have enough channels for the radios they use now; new communications, data-transfer, and command-and-control systems would demand an even larger share of the spectrum. But even if the Federal Communications Commission resolves this problem, engineers face another kind of communications barrier. It’s the difficulty police and most other non-engineering customers have defining their problems.

Nevertheless, electronic firms are nibbling at the police market. Various companies are developing and testing such gear as car-mounted teleprinters, computer-based control setups, and automatic vehicle-monitoring gear.

But law-enforcement officials often are blind to what electronics can do for them. “Police are relatively new to this game,” notes one analyst in the Justice Department’s Law Enforcement Assistance Administration. “They’re learning, but they need help. Understand that they don’t have to be electronics experts—though they should at least have a knowledgeable consultant if not a man in-house—but they do have to be able to tell industry the nature of their problems; what they want to achieve; precise terms; and the exact environment in which they operate.”

On the other hand, engineers are being urged not to forget—if they have learned at all—that exotic sub-systems derived largely from military programs have little use in or cost too much for police work. For example, applications for night vision equipment, acoustic and seismic detectors, and other surveillance gear touted as “crime countermeasures” are far too limited to justify their high cost in the eyes of most enforcement agencies. In many cases, simpler approaches serve at least as well, often better. One obvious case: better street and off-street lighting systems beat night vision equipment almost every time. Lights not only cost less, but also are a crime deterrent. It’s possible to envision electronic gear helping to cut down the crime rate. “Take the kids who ‘hot wire’ cars and race them until they run out of gas,” says an LEAA official. “Not many get caught; it’s not a high risk. But if, for example, you can build a better alarm system into tomorrow’s cars and substantially increase their chances of getting caught, far fewer kids are likely to take the risk.” One such system might be a tracking beacon built into cars and triggered by an unauthorized user. However this type of alarm isn’t feasible, because it would be too costly, and would take up too much space in the law-enforcement spectrum.

Even now police departments have too little spectrum space and this hurts their performance. Delays of more than 5 seconds in gaining access to a police channel can cut a foot patrolman’s or a squad car’s arrival at the scene of
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a crime by minutes, perhaps permitting a criminal to escape or allowing witnesses to disperse. Studies show that a 6-minute response is associated with a 49% arrest rate compared with 63% for a 1-minute response.

Reducing response time is one of the most important advantages of computerized police communications equipment. Response time of the complaint phase—the time it takes to receive a call for service and pass the information on to a dispatcher—is reduced by over 50% in a fully automated command and control dispatch communications system, says San Anzelmo Jr. of Sylvania Electronic Systems-West. More calls can be handled by the same number of lines and operators. And error rates, as well as delays, are reduced through computer data entries formatted by time, type, and priority of call, and exact location. The dispatcher's role is upgraded to a "dispatch commander," says Anzelmo. He's freed from many routine chores and supplied with decision-making information.

The high cost of such systems can be offset by manpower savings, says Anzelmo. Police departments now divide the number of men equally by the number of shifts and beats to determine requirements. But with computer determination of priority beats and times of day when crimes are most likely to occur, the inefficiency of stationing men when and where they're not needed is eliminated.

The Illinois Institute of Technology Research Institute (IITRI) has some ideas on how good a police communications network should be. Commissioned by LEAA to devise plans for improving existing networks, IITRI suggested that average delay in gaining access to a link during a normal busy period shouldn't exceed 5 seconds; there should be no delays of more than 10 seconds in transmitting messages; the information shouldn't be degraded by interference; and the network should be able to handle busy-period loads.

The sad fact is that most city networks can't begin to meet these standards. Bernhard Ebstein of the IITRI project group estimates police spectrum needs at three times the current capacity. Ebstein's group studied the Lake Michigan area as a representative urban sprawl. Of the area's 43 police "networks"—defined as a haphazard arrangement of many different police users on the same and interconnecting channels—31 have channel-access delay time of more than 5 seconds.

And no improvement is in sight. Currently, the area has 98 radio links on 2.4 megahertz; and 313 links on 12.5 Mhz are needed. By 1980, the area will need 550 links on 22 Mhz, says Ebstein.

Whether or not police will get immediate relief from the crowding largely hinges on the outcome of two cases now before the Federal Communications Commission. Probably within the next few months the FCC will make decisions on land-mobile sharing of the lower seven uhf channels and allocating the 900-Mhz space released from government use.

Coming. The next piece of electronic gear most likely to get into police cars will be digital teleprinters. The best guess on when they'll be in wide use is five years. With current technology it "could be done tomorrow," says Walter Key, program manager for electronics and communications at LEAA's National Institute of Law Enforcement and Criminal Justice. The value of a teleprinter, says Key, lies in making it possible for a field officer to query the central computer directly—bypassing the dispatcher. Besides, having hard copies of such information as the license numbers of stolen cars frees policemen from the tedium of continuously listening to their radios.

But teleprinters won't be here tomorrow. They're expensive—about $1,000 for a read-only unit. In addition to initial cost, police departments must consider the expense of training people to use a hard-copy system. Another problem is that teleprinters use voice channels.

But despite Key's confidence in teleprinter technology, there is some question about whether the devices are ready for police work. Last summer Capital Scientific Corp. ran a test of four makes of car-mounted teleprinters—Motorola, Codamite, Ferranti, and Kleinschmidt—for the city of Milwaukee. None got outstanding marks across the board. The test was conducted on 150-, 450- and 950-Mhz bands, and 450 Mhz proved to be the best in terms of antenna gain, receiver sensitivity, and transmitter power. Error rate is 1 in 1000 at this frequency, and it improves with a narrower band, says Capital Scientific president E. Ray Kniekel, but use would be impractical in cost and frequency allocation considerations.

The errors encountered in the Milwaukee tests were classified as propagation errors due to interference, low signal strength, etc.; mechanical errors due to malfunction of the teleprinter; and encoder errors. The Codamite units caused a "large number" of machine errors, and fell second in the test group's rating, behind Motorola. Codamite's equipment had the best propagation-error record, the test group says, but mechanical errors negated this quality. The Motorola technique, the group says, "appears to have a distinct advantage" in overcoming effects of multipath signal dropouts. But high error rates experienced on the 150-Mhz tests indicate that the equipment is sen-
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Perimeter detection: Vietnam to the White House

Civil applications are falling out from the electronic system developed to detect troop infiltration in Southeast Asia. Following heavy industry funding of seismic and acoustic detection systems in the mid-1960's for the controversial "McNamara Wall" along 17 miles of the demilitarized zone dividing North and South Vietnam, contractors are finding a variety of new customers, ranging from Federal penologists, who want to keep criminals in prison, to the U.S. Secret Service, which wants to keep them out of the White House.

Typical of such systems is Westinghouse Electric Corp's Periguard, installed last November at the Federal Youth Center in Ashland, Ky., and now reportedly undergoing tests at the White House.

The Secret Service won't talk about the White House installation or Westinghouse's role. But details of the Ashland system are available.

Each Periguard detector has two hoses, up to 600 feet long, filled with equal parts of water and antifreeze. These are buried 15 to 18 inches deep. The detector detects electrical signals from pressure waves transmitted through the hose liquid. According to Westinghouse's Warren Drumheller, this is achieved by a piezoelectric ceramic disk or crystal transducers at the end of a hose. "These two transducer crystals are wired to have opposite polarities so that signals of equal amplitude are cancelled," Drumheller told a recent law enforcement symposium.

The differential analog signals are amplified and placed on a line-surveillance network for transmission to the control center, which can monitor many detectors from a central control panel.

Ashland uses two patrol cars with armed officers and remote displays at each end of the institution, plus a central control room with a main readout console. When an alarm is triggered, a horn warns the officers to check the remote display panel which shows where the intrusion took place. The vehicle then speeds toward the zone. Periguard signals travel at 300 feet per second.

Ray Connolly
bookies beware, here comes NASA... some military hardware. Night vision devices pose a “classic problem in use,” says William B. McMahon, technical director of IITRI’s Law Enforcement Center, an information clearing house. The number of places night-vision units can be used “is almost nil,” he says.

LEAA’s Key says even though there might be some applications, such as in stakeouts, police “won’t buy unless the price is right.” But one unit demonstrated by RCA at a recent LEAA technical symposium costs $1,950, and prices at other companies range up to $8,000. Even RCA admits its night-vision devices “have not had great acceptance” among police. Part of the problem for all makers is technical, and includes the poor quality of the images produced. Police need positive identification.

Sometimes, however, spinoffs do prove useful. For example, NASA’s technology transfer team wants to help catch bookmakers. One of the most important pieces of evidence uncovered during a gambling raid could be a writing pad used to record bets, from which the original page might have been destroyed. What is needed, says IITRI’s Robert Hand, is a device sensitive enough to measure indentations on succeeding pages. Hand says that NASA is working on a solution that involves a fiber-optics surface roughness gauge which was originally developed to examine surfaces of flared tubing used in special valve fittings on the Saturn rocket.

Viet Cong. Many devices are offered to police on the erroneous assumption that street crime is the same as guerilla warfare, says IITRI’s McMahon. A Washington policeman points up this fallacy: “You can’t really kill the kid who holds up an ice cream stand like you can the Viet Cong.”

Though American crime and Southeast Asia’s guerilla warfare have little in common, many Federal officials do believe that many military systems developed for Viet Nam can be modified for police use. One example cited is a tactical radar in a moving vehicle which can determine the speed of...
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Circle 127 on readers service card

Another vehicle. Present police systems require fixed installations—the familiar "speed trap." Police researchers also are looking at vehicular tracking systems and hands-free communication systems.

Testing. Engineers developing systems for law-enforcement should be prepared for rigorous field testing, says George W. Saunders of Underwriters' Laboratories. In surveillance systems, for example, power input should not exceed 110% of the market rating, but they should be able to operate without readjustment at 85%-110% of rated voltage. The system should not, says Saunders, require readjustment or signal false alarms.

Saunders says detection systems must be built to be fail safe by providing standby capability and recharging features, an idea similar to NASA's "zero-defects" plan.

There is a fundamental conflict between the roles of the technologist and the policeman, says IITRI's McMahon, because too often the technologist does not recognize his role in law-enforcement as one of support. Instead, he says, the engineer will "come up with beautiful solutions when we just don't have a problem." Engineers must develop an understanding of the practical problems in fighting crime, he says.

McMahon cites Chicago's experience with its remote/mobile personal communications link as an example of engineers' limited appreciation of police problems. The link initially was designed to place a radio repeater in each patrol car to pick up the officer's transceiver signal when he was away from the vehicle. Chicago bought the concept. Later, Chicago discovered that two far less costly alternatives—installation of five large repeaters throughout the city or one large-capacity repeater near headquarters—would be equally effective. The city chose the five-repeater plan. However, the fact remains that the options were not volunteered initially, making the systems designers' judgment suspect.

Another example is a gun detector developed for use on airlines. Essentially it's a metal detector, says McMahon. The device is impractical for police, because it can't differentiate between a knife and a nail file. There "really is no gun detection system today," says McMahon, and the impact on the airlines' hijacking problem, if any, has been psychological.

Some new approaches to crime fighting are in the works. LEAA is funding experiments with voiceprints as an investigative aid in cases where the only clue may be a telephone call. Voiceprint, a picture of the spoken word received on a sound spectrograph, originally was developed and tested during World War II and was worked on in the 1950's by Bell Telephone Laboratories on the premise that a person's voice is as unique as a fingerprint. But even after voiceprint is developed to the point where it will work in a completely uncontrolled environment with a minimal error rate, it probably would not be useful as evidence in court.

A lawyer at the U.S. District Courthouse in Washington says voiceprints probably would not be accepted as evidence, especially when there is doubt in the court's mind as to its probity and scientific use. And, he says, a conviction probably could not be obtained "on that evidence alone."

And even with electronic fingerprint matching there is some doubt as to its value as hard evidence. The Search project group [see p. 116] will experiment with facsimile transmission of fingerprints this summer, though actual matching will be done manually. Both fingerprint and voiceprint identification techniques will have to wait for computer identification capability.
Voilà!

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New Products

April 27, 1970

OCR fits minicomputer size and price

Desktop reader with cathode-ray tube display for editing to be introduced next month at Spring Joint Computer Conference

By James Brinton

Electronics staff

Now that even large businesses are decentralizing data processing and lowering its cost by using minicomputers, peripherals like optical character readers begin to look expensive when price tags run into six figures.

In a business environment like this, Infoton Inc. anticipates an enthusiastic welcome for its Challenger document reader, priced at $30,000 to $35,000, or $1,000 monthly rental. The Challenger is hand-fed, reads a page at a time (though the page can be as large as 12 by 14 inches), and includes a cathode-ray tube display for error correcting, editing, and verifying. The whole system, which fits a desktop, will be introduced at the Spring Joint Computer Conference May 5-7 in Atlantic City.

Its reading speed compares well with more costly devices at an average maximum rate of 1,000 characters per second. Most other OCR’s average about 500 characters per second. Flat out, Infoton’s Challenger reads a standard page in about 5.5 seconds; competing machines take from 4 to 20 seconds per page, except for the $500,000 Philco-Ford OCR which achieves a 1-to 2-second-per-page rate.

The Challenger sells for a price formerly associated only with card or mark readers. As late as last fall, $42,000 was a low price for a reader capable of recognizing marks, four special symbols, numbers, and holes in cards [Electronics, Sept. 29, 1969, p. 165].

Design of the Infoton machine revolves around large-scale MOS shift registers, linear IC’s, and high-speed TTL/MSI logic. The registers perform as memory in data buffers, line buffers, and display refreshment applications. Widespread use is also made of the linear IC’s. And TTL has been applied to the machine’s hardwired control computer, which in turn allows a simpler scanning mechanism that is inexpensive to construct and has low power requirements. The whole machine dissipates only 700 watts—less than an electric iron—and this in turn allows use of a low-cost solid state power supply. These factors plus some proprietary circuitry, make the price tag possible.

Feed costs. Among the first costly OCR features to disappear was the automatic paper feed. Infoton engineers say the feed not only added to cost, but to volume and maintenance as well. Also, there was the added risk of the automatic feed destroying irreplaceable documents. Nor could an automatically fed machine deal easily with folded, ragged, stapled, or paper-clipped documents.

But reading page by page could be slow. So to design a machine with enough throughput to compete with automatically fed OCR’s, “we eliminated as many post-OCR steps as possible—editing, error correction, etc.—and made them part of the reading process,” says George G. Pick, vice president for engineering. “Thus, we added an alphanumeric CRT display to show material as read, and added a rejection display to catch ambiguous characters.”

The rejected character is shown in a small section of the CRT and also blinks on and off in its text location. The machine resumes reading when the operator punches in the appropriate character.

To speed editing and correcting,
Infoton redesigned the traditional operator's keyboard. The key layout of a typewriter is based on the frequency of letters in English text and on the relative strengths of the fingers. But errors in OCR are nearly random—letter frequencies are meaningless. So Joseph G. Wohl, vice president, information sciences, organized the keyboard so that any number or letter could be found quickly, systematically. He arranged the alphabet in three arrays of keys similar to an adding-machine layout. For numbers he used two rows, one through five and six through 0.

The arrangements are based on the results of some human-factor studies sponsored by the Navy. Says Infoton, it takes less than an hour to train an operator.

The Challenger's optical front end differs from that of most other OCR's in that the reading mechanism moves and the paper is stationary. The operator opens a sliding door, inserts the document which is aligned automatically, and closes the power-assisted door. The door presses the document against a glass platen shaped like a section of cylinder.

Within the cylinder spins a y-shaped holder for condensing and projecting lenses; in the vertex of the y is the light source—an incandescent lamp. And at the axis of the cylinder, at a point even with the tips of the horns of the y, is a 126-diode photodetector array. About 50 diodes are used, the remainder are for expansion capability. The light reflected from the document is brought into focus at the array with enough accuracy to allow resolution of lines 0.008 inch wide.

**Spinning.** "The sensor optics are continually spinning," says Pick; "there's no reciprocal movement to deal with. Nor do the light source or detector array rotate, saving us the cost and unreliability of slipping connections."

The diode array's output is amplified and passed to a circuit which makes the so-called black-white decision. Pick says that a new proprietary approach to this has lowered cost. The circuit makes the decision based on a continual tracking of "local white."

"We electronically arrange the character being read on an array of 10 by 14 cells," says Pick. "OCR-A is designed to have a large difference between the number and arrangement of black and white cells that describe a letter. If one superimposed any two characters displayed in such a matrix there would be gross differences noticeable. And it is these differences in black and white which help make the matching and character-selection decisions, the next steps in the OCR process.

**Three tests.** After the black-white decision, the signal passes through a data buffer and into the character matching and selection decision subsystems. These use three criteria to determine what letter is being read; first, the digital equivalent of the 140-cell matrix is compared with those for other OCR characters and the best match is taken; second, the circuitry also demands a minimum difference between the best match and the second best; third, a circuit looks for an absolute minimum match. All these operations are done in parallel with character identification taking some 10 microseconds or less. Each operation enhances the error rejection capability of the reader.

From here the deciphered characters are displayed in the CRT as are the rejected or doubtfully identified characters. After editing or correction, the data passes to output coding and interfacing circuitry and then to a modem, data processor or tape deck.

Infoton Inc., Second Ave., Burlington, Mass. 01803 [338]

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Details in our catalog
William Slover of Ampex Corp., is what emerged from a market survey of the increased use of cassettes in computer equipment.

The result of the survey is a heavy-duty digital cassette tape drive with a mean-time-between-failure of 2,000 hours and a maximum error rate of one in $10^6$ bits, says Slover, who is marketing manager of the Ampex Computer Products division.

The model TMC comes with a standard tape speed of 12 inches per second, and optional speeds down to 2 ips. Data rate at 12 ips is 9,600 bits per second, permitting up to 350,000 characters on a 300-foot-capacity cassette. There are five operating modes: read forward and reverse, write only, fast forward, erase data, and a 55-ips fast reverse. Five solenoids are used to permit remote control of the brake, the two reels and each of two pinch rollers. Packing density is 800 bits per inch, and the unit writes on two redundant tracks in NRZI format. Output is serial-by-bit, and the clock is derived from the two-track complementary recording.

The purpose of the redundant two-track recording is detection of data errors, usually resulting from small mechanical defects in the tape, during recording. With the NRZI format, each time there is a 1 in the bit stream on the first track, the magnetic status of the tape is reversed. Complementary recording of the same data is done on the second track, except that the reverse is true: when a 0 appears in the bit stream, the polarity is reversed. The two tracks are on separate areas of the tape. Information from the two, considered jointly, provides a clock because there has to be a polarity change on either the first or second track for each bit position. The time between the polarity changes is uniform, giving a square wave output. If a gap occurs in the square wave, an output signal is generated and sent to the controller indicating that an error has occurred. The tape is then reversed by the controller and brought back to the point where the error occurred.

The price tag Ampex puts on its new drive reads $600.

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About 70% of peripheral equipment-market money is in System 360-compatible gear, says Wayne Dunlap, sales manager of Information Storage Systems Inc. That, in part, is why the company's newest product is a storage control unit that plugs into a 360 selector channel and operates a group of disk drive units. Also, Dunlap and his associates at the California company came from IBM and had helped to design disk drives there.

Since ISS does not have an extensive field sales or service network, it signed a contract with Telex, which does. Telex sells ISS's six-high 701 disk drive under its own label as the 5311, and ISS's 11-high 714 disk drive as the 5314. Both are high-speed units. The new controller, the model 728, operates up to nine 714 disk drive units. It will be introduced at the SJCC.

Designing the 728, says Dunlap was complicated by the fact that IBM integrated the design of the 11-high disk drive and its controller, the 2314.

The answer, says Dunlap, was to design a controller that would plug into the 360 selector channel—the same channel used by the IBM 2314. "But this is a difficult interface problem," says Dunlap, "because of the format of the data, the data transfer rate, and the special data fields IBM uses." However, ISS had an edge: its own people designed the IBM 2314.

To increase reliability of the controller/drive system, the 714 has only one electronic adjustment. And the disk sector transducer, which is magnetic on competing equipment, is optical on the ISS unit. A phototransistor and a light-emitting diode are used.

Information Storage Systems Inc., 10435 N. Tantau Ave., Cupertino, Calif. 95014 [340]
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Data handling

Versatile keyboard has a light touch

Equipment uses Hall-effect keys; hard copy to replace tape in future models

When he took over at Fairchild in 1968, Lester Hogan was confronted with a diversity of product lines, some of which were losing money. To determine which lines would stay and which would be sold off, Hogan set up a rule: if a line could turn profitable by using the products of the Semiconductor division, it would be retained. And since Fairchild's keyboard-terminal line met this condition, it was kept. A new terminal from the Graphic Equipment Division uses digital MSI circuits from the Semiconductor division and a Hall-effect keyboard purchased from Honeywell's Micro Switch division.

Called the Electro/Set-430, the terminal is intended to be used with Fairchild's Comp/Set-330 typesetter and its photo textsetter. But the keyboard is not limited to typesetting applications. It can produce a six, seven, or eight-level perforated tape either ½-inch or one inch wide. The code structure can be changed by replacing a single printed-circuit card; thus the terminal can easily be used as a computer peripheral. Future models will include magnetic-tape and hard-copy output.

According to Richard Robinson, general manager of the graphics division, "The Hall-effect keys not only eliminate mechanical contacts, and reduce maintenance problems, but they also provide a consistent, light touch."

Standard features include a choice of teletypesetter or secretarial shift, a band of mode-indicator lights, and a job key that backs up the tape a single code at a time. Options include a function enabling the operator to delete the entire last word.

The unit is priced at $2,865.

Fairchild Graphic Equipment Division, 221 Fairchild Ave., Plainview, N.Y. [430]
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See the new Datacraft DC 6024/3 Computer in booth 24007 at Convention Hall during the SJCC in Atlantic City.

Electronics | April 27, 1970
New components

Making a circuit stick

Any network can be assembled directly from sketch and quickly tested; elements withstand soldering-iron temperatures, and design is easily modified.

It's frustrating for a circuit designer, in the midst of putting together a breadboard, to find that he doesn't have all the necessary connections. A technique called Circuit-Stik prevents such frustration by permitting rapid assembly of prototypes directly from engineering schematics and logic diagrams.

Circuit-Stik Inc. offers a family of circuit subelements and materials designed as a packaging system. The system reduces design and drafting expenses and can save schedule time normally lost when using outside manufacturing services. Individual circuit boards can be assembled and tested from engineering sketches the same day.

The conductive subelements are supplied on thin substrates that have a pressure-sensitive adhesive backing. The elements can withstand soldering temperatures, and are easily removed for design modification.

The circuit subelements are selected from a variety of patterns available for commonly used components including flatpack, dual-in-line, and TO-5 packaged integrated circuits, transistors, diodes, resistors, test-point jacks, and plug-in relays.

Switching relays series CR300 feature typical operate and release times of 1 msec, and compatibility with logic levels. The CR372 dry-wet relay requires approximately 60 mw of power. The CR363 mercury-wetted relay requires about 340 mw of cell power. Price (1-9) is $5.94 for the 363, and $3.98 for the 372. Computer Products, 3400 N.W. 70th St., Fort Lauderdale, Fla. [341]

Pushbutton type 156 is a miniature 3-amp 4-pdt industrial relay that permits manual push-to-test actuation, without coil actuation. The feature is desirable in system debugging or maintenance, when electrical operation for effective diagnosis of the system is impractical. The basic unit is 1.22 cu in. volume. AEMCO Division of Midtex Inc., 10 State St., Mankato, Minn. [342]

Magnetic latching relay in one-half crystal can size is a 4-pdt unit that meets the requirements of MIL-R-5757E, including welded seal. It is available with 6, 12 and 24 v d-c coils and operates from a 150 mw short duration pulse. Contacts are rated low level to 2 amps resistive. Price is approximately $30. Branson Corp., Vanderhoof Ave., Box W, Den ville, N.J. 07834 [343]

Photomultiplier tube 4249B is for use in systems which are dark current limited and is a plug-in replacement for earlier types having small-area 5 type cathodes or 2 in. bialkali cathodes. Typical dark current is 200 pa at 2,000 mw/lumen giving equivalent anode dark current input of 10^-18 w at 400 nm. The Bailey Instruments Co., 5919 Massachusetts Ave., Washington D.C. [344]

Wound mica paper capacitors series GLA feature an operating temperature range of -55° to 310° C, a dissipation factor of less than 0.002 between -55° and 100° C, and flat capacitance-temperature characteristics compared to film and paper. Operating voltage is 100 v to 10,000 v d-c. Units comply with military specs. General Laboratory Associates, Inc., Norwich, N.Y. [348]
**Communications market in the 70's.**

We have just initiated "A Study of Opportunities in the Communications Markets During the 1970's." This study will provide a limited number of clients with an in-depth analysis of the effects of changing technical, economic, and regulatory factors on the markets for communications systems, subsystems, products, and components. It will also forecast the size and structure of these markets.

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**Design task.** Conductive paths, then components are attached to p-c board to form circuit.

in printed-circuit board connectors.

Circuit subelements have pre-plated, readily solderable conductive patterns. When predrilled Circuit-stik 1000 series subelements are used in conjunction with 0.1-inch punched-board material, no further drilling is required. The pressure-sensitive elements are placed on the backing board, and the result is a p-c board ready for mounting and soldering of the components in the conventional manner. The problem of wobbly terminals caused by poorly mounted stand-offs is avoided, and so is the delay usually needed for artwork, etching, and drilling a p-c board.

Available for use with wire-wrap systems are configurations that can reduce the sockets and wire-wraps required. All types may be combined on the same board.

**No terminals.** The active circuit components are soldered directly to the subelements. And the engineer using circuit boards made from these materials can expand his prototype unit to larger systems by conventional card cages and rack-and-panel hardware.

Specially developed Circuit-stik materials for use with subelements allow multilayer techniques to be used even in the prototype. Because the subelements are thin, modifications to isolate, or to make unique patterns, may be made instantly using only a pair of scissors.

Circuit-stik is available from stock. Price range is from 20 cents to $6 depending on the configuration. For example, a package of 10 dual in-lines would be 20 cents each; a continuous strip of six components in a package of five strips would sell for $6.

Circuit-Stik Inc., 1518 West 132nd St., Gardena, Calif. 90249 [349]
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New instruments

Voltage source boasts 0.0015% accuracy

With a range of 0.1 µV to 10 volts, thumbwheel supply can be used for calibration; stability is ±5 ppm over 24 hours.

Just as a thoroughbred horse shouldn't pull a plow, a standard cell shouldn't be called on for routine chores. But with ever-higher accuracy levels in instruments, engineers often have no choice but to use their primary standards for calibration.

Standard cells may be getting a rest. General Resistance's new voltage source has an accuracy of ±(0.0015% + 3 microvolts), good enough to calibrate most laboratory instruments. Called the DAS47A, the source has thumbwheel switches for setting output, and delivers up to 30 milliamps.

The 47 is the newest member of the Dial-A-Source family, initiated by General Resistance two years ago. Aside from its value as a calibration source, the 47 can handle the same jobs as the older Dial-A-Source, including delivering voltages to systems, acting as a digital-to-analog-converter reference, and performing reference tasks for stability checks.

The 47's rated stability is ±5 parts per million per day and 15 ppm per year. The actual stability is even better, says Lloyd Silverman, the designer.
High current. With its op amp output, the 47 delivers up to 30 ma.

A major computer maker, the 47 showed short-term stability of 1 ppm, he says.

One reason the 47 is versatile is its range-0.1 µv to 10 volts. In addition, output isolation is 1,000 megohms and its temperature coefficient is 0.5 ppm per degree centigrade.

Like the older Dial-A-Source, the 47 has three main parts—a reference built around a zener diode, a Kelvin Varley divider, and an operational amplifier at the output.

To reduce noise in the 47’s reference output, new zeners were used. The 47 has two 6-volt zeners instead of one 11-volt unit. Each of the new diodes has a larger junction area, and hence generates less noise, than the older 11-volt device.

Delivery time for the 47 is 30 days. Price is $1,190.

General Resistance, 500 Nuber Ave., Mount Vernon, N.Y. 10550

New instruments

Synthesizer sheds spurious signals

Phase-locked loop in programable source keeps noise 70 db down

Plenty of customer feedback went into Monsanto Electronic Instruments' new frequency synthesizer, the 310B. According to Hirsh Marantz, engineering manager in charge of signal sources, some users of Monsanto's older synthesizer, the 3100A, reported that its signal wasn't clean enough for their applications; the spurious content of the 3100A's output is 50 decibels below the fundamental. In response, Marantz and his engineers put into the 310B a phase-locked loop that keeps the spurious signal level 70db down.

Marantz points out that this approach has a number of side benefits. The 310B always delivers a signal, even when the output's frequency is being changed. In addition, this output has an "almost constant" amplitude during switching. The amplitude is adjustable in 10-decibels-above-1-milliwatt steps from -70 dbm to 2.23 dbm.

Another happy effect is that the phase-lock oscillator's output is available at the front panel. This satisfies customers who asked for an offset frequency.

The results of phase locking aren't all to the good. Decreasing spurious signals means increasing switching time. In the 310B it takes 300 milliseconds to go from one frequency to another. However the user can opt for a spurious level of -40 db in exchange for a switching time of 20 microseconds.

In most respects the 310B resembles the 3100A. The maximum frequency is 1,299,999.99 hertz and the output’s harmonic content is -40 db. Binary-coded decimal signals can control the 310B, with 0.3 volt to -3.6 volts acting as "1" and 1.0 volt to 3.6 volts as a "0." An internal generator can sweep the output's frequency over a decade at a rate of either 1 hz or 100 hz.

One thing that the new synthesizer doesn’t share with the older unit is serial numbers. Why does Monsanto call its new source the 310B, and not the 3100B? According to Marantz, it’s the 3100A that was misnamed. Four-digit serial numbers at Monsanto are supposed to be reserved for plug-in units.

The new synthesizer is the same size as the 3100A—5¼ by 16¼ by 16¾ inches—and the same weight—35 pounds. The price, though, is $1,000 heavier; the 310B goes for $4,950.

Monsanto Electronic Instruments, 620 Passaic Ave., West Caldwell, N.J. 07006

146 Circle 146 on reader service card
"Government contracting for the engineering executive."

In the 1970's, technical expertise is no guarantee of success in the government marketplace. Technical executives are also being measured by what they contribute to cost reduction and profit. As a result, an increasing number of contract terms, clauses and regulations now affect the technical performance of the contract.

In recognition of these demands and their impact on executive time, the Electronics/Management Center has developed a two-day seminar program designed to provide the engineering and production executive with an understanding of government contracts as they relate to the execution of his job responsibilities.

This seminar is designed to lead the defense and aerospace executive through the maze of contractual legalese. Our professional staff of experts will translate the substantive portions of the long and detailed regulatory information into a useful body of everyday working knowledge. The seminar faculty will be composed of consultants from Peat, Marwick, Mitchell & Co. with broad experience in government contracts. In addition, the personnel of Peat, Marwick, Mitchell & Co. will be joined by a prominent attorney, who is a member of the New York Bar and graduate engineer.

The combined skills of the teaching faculty represent a wide range of practical experience and developed skills in the defense and aerospace industry.

The seminar program will consist of the following five sessions:

I. The basic contract. This portion of the program will provide a general description of the various types of contracts and reasons for their use. Also contract goals vs. absolute performance requirements; the interpretation of ambiguities and discrepancies; differences between plans and specifications.

II. Contract changes. Under this section the following topics will be fully discussed: The changes clause; technical assessment of the change to determine the most appropriate method of recovery; inspection and acceptance; constructive changes; cardinal changes; responsibility of performance; identification and acceptance of the change; change control procedures.

III. Defective pricing. Recent legal decisions have increased the responsibility of the contractor's engineering and production personnel in defending the defective pricing claims made by the Government. An explanation of the statute and its implementation will be given together with the technical responsibility in the preparation of the quotation; identification of relevant data and submission of data.

IV. Warranty and correction of deficiencies. This section will discuss how changes in the warranty clause and the correction of deficiencies clause have significantly increased the importance of the role of engineering management. The financial responsibility which must be assumed will also be explained.

V. Terminations. Engineering management's participation in a terminations claim is essential to prevent financial loss. Coverage will be given to the following subjects: suspension of work; termination for default; termination for convenience; effect of excusable delays; effect of legal, economic, technical impossibility of performance; termination of subcontracts; the termination settlement.

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Electronics | April 27, 1970

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CONTROL PRODUCTS DIVISION
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BELLE. HOWELL
Data handling

Low-cost alphanumeric terminal prints all

It transmits or receives full 128-character ASCII code at a rate corresponding to a 110-bit-per-second bandwidth; printer has 5-by-9-dot matrix

A data transmission terminal that is capable of handling full alphanumeric data, prints out all its transmissions, and can replace the most widely used teleprinter, plug for plug, yet sells for less than $2,000, is shortly to be made available by Transcom Inc., a subsidiary of Hi-G Corp. While the terminal’s engineering design is complete, Transcom is still investigating the market to determine what minor features should be included, perhaps as options, before formally announcing the terminal.

The new terminal, called the CT-264, connects to a telephone line with the standard Bell System modem, so that another terminal at the other end of the line sees it as a Model 33 Teletype machine. It can either transmit or receive the full 128-character set of the American Standard Code for Information Interchange (ASCII), at a 10-character-per-second clip. Since each character consists of 11 bits, including a parity bit and asynchronous start and stop pulses, the character rate corresponds to a 110-bit-per-second bandwidth. Actually, the terminal is capable of more than twice that

Newly-designed programable digital printer model 2000 can provide the entire history of an on-line test for extended periods of unattended operation from start of test in hours and minutes and a code to indicate what event occurred to cause each print. The unit features simplicity of operation. International Data Sciences Inc., 100 Nashua St., Providence, R.I. 02904 [421]

Data acquisition instrument designated Mod F Minverter offers a throughput rate of 100 khz. It is a 12-bit analog-to-digital converter with sample-and-hold capability and 16 channels of multiplexing. It also contains controls for interfacing with digital processors or control systems. Price is $1,950. Raytheon Computer, 2700 S. Fairview St., Santa Ana, Calif. 92704 [422]

Data word generator model DG-32 is a two-channel, 16-bit-per-channel unit using TTL/DTL medium scale IC's throughout. Data rates of 10 Mhz or faster are typical. Clocking is done externally, utilizing a positive pulse of 3 to 5 v or a sine wave of 5 v p-p, of the frequency desired. Price is less than $1,500. North American Systems Inc., 383 Putnam Ave., Hamden, Conn. 06114 [423]

D/A converter series DAC-HB features hybrid thick film construction, small size (1.5 x 2 x 0.4 in.), 12 binary bit resolution, fast output settling time (5 usec), and low cost ($65). The output slew at 100 usec/v and can be either bipolar (+5 v at 5 ma) or unipolar (+10 v at 10 ma). Operating temperature range is 0° to 70° C. Datel Corp., 943 Turnpike St., Canton, Mass. [424]

Time division multiplexer TM-8 can service simultaneously up to 8 terminals of intermixed data speeds from 1,200 bits per sec to 4,800 bps. It operates in conjunction with the AE-96 9,600 bps voice band modem. Features include the capability to interface to switched network moderns like the Western Electric 202C. Codex Corp., 150 Coolidge Ave., Watertown, Mass. [425]

Desktop crt display terminal can display 1,760 characters on 32 lines of 55 characters each or 1,920 characters on 24 lines of 80 characters each. Screen size is 9 in. or 12 in. tv monitor. Character repertoire is 64 standard ASCII upper case alpha, numerics, special characters or 96 standard ASCII characters excluding only lower case. Hazeltine Corp., Little Neck, N.Y. [426]

Hard copy output peripheral devices in Matrix series use a non-impact (electrostatic) printing technique. High quality records, silent operation, speed and flexibility in printing both graphics and alphanumerics, and operating reliability are offered. The products are for use with mini- and mid-computers, and crt terminals. Versatec, 10100 Bubb Rd., Cupertino, Calif. [427]

Nova computers can sort, match, collate and merge data when fitted with CartriFile 4096 digital magnetic tape system. The system is cartridge-loaded and combines four magnetic tape transports and controller in a single unit. A soft ware package included in the $6,400 price provides block read/write and interrupt response. Tri-Data, 600 Maude Ave., Mtn. View, Calif. [428]
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Data handling

ROM expands throughput

Modular system can operate either as remote terminal or as stand-alone computer

Although the programmer is indispensable to any data-processing system, his responsibility for getting information into and out of a computer system is lessened considerably when much of the burden is put on the computer itself. This is what Hetra Corp. has done with its S-series data-processing systems. The result is a much improved throughput capability and ease in handling.

The key to flexibility is in a read-only control memory that stores 3,000 16-bit words.

Many of the instructions that would ordinarily appear in the software package are wired in on logic cards as microprograms that execute data transfer and character and data manipulation faster than programed instructions that are entered separately. The ROM, however, does require a great deal of hardware—13 logic cards—more than half of the total in the system's processor. Nevertheless, Hetra gets compactness and low cost by employing medium-scale integrated circuits throughout. Furthermore, the system, which is built with Fairchild 9,000-series transistor-transistor-logic gates, operates with a 15-megahertz clock.

To alter the firmware, any of the ROM cards can be replaced with others, or individual diodes on the cards can be added or removed. Thus the ROM can be programmed for any of a variety of functions. Moreover, the ROM can be used to expand Hetra's smallest system, the S/I minicomputer, into larger units—the S/II and the S/III—with faster throughput rates.

Up to four processor units can be connected to a central processor; their individual read-only memories cause them to act as input-output controllers for peripheral devices. Each of these processors is identical except for its read-only memory; the number of processors and the particular collection of peripheral devices define whether a particular system is S/I, S/II, or S/III. The additional input-output devices can obtain access to the main memory through these controllers, or processors, under the control of the ROM. And when a multichannel adapter is used, a number of input-output channels can have simultaneous access to the main memory.

Half the price. Engineers at Hetra, comparing their models with similar-sized IBM types, claim that the S/I reads cards and prints out
Handy & Harman, leader in brazing for 35 years, will tell you when not to braze. We know the limitations of brazing. We also know the limitations of soldering, welding, mechanical fastening, and adhesives.

The Materials Age seldom allows the design engineer time to bone up on all the limitations, however. The designer may very well suspect, for instance, that brazing is appropriate to a fabrication on the board. Handy & Harman stands ready to offer counsel. We can start out clearing up uncertainties right here by listing some brief guidelines on When to Consider Brazing, beginning with the illustrated dilemma:

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When joining conductive assemblies. The very thin (.002" or so) layer of brazing metal alloy causes very little increase in resistance.

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data faster than the System 3, but costs only $22,000—compared to $44,000 for a System 3. And the S/III can handle 400 cards per minute, and print 600 lines per minute with a memory cycle time of 1 microsecond. The IBM 360/20 can do the same—but has a memory cycle time of 3.6 µsec.

The basic S-series stand-alone unit consists of a tape cassette input, a central processor with a 4,096-byte memory, a high-speed printer, and external disk storage. The unit can also be bought as a terminal for a remote batch-prec-
new micro-miniature inductors break price and space barrier

For the first time, ultra-miniaturization hasn't resulted in price escalation. In fact, the new Cambion micro-miniature inductors are competitive with much larger conventional units in terms of price. In terms of size, there's no comparison.

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The SP20 is $995 with power supply; $895 without. Both versions are substantially less in quantity. Complete specs, delivery information and a demonstration are yours by calling or writing for Data File SP-E-4.

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Circle 154 on reader service card

Electronics | April 27, 1970
New subassemblies

Low-light-level tube is supersensitive

Unit for tv camera jobs achieves brightness magnification of 150,000; rugged silicon-diode target withstands damage from light and heat

Improvements in sensitivity and ruggedness are speeding development of low-light-level tubes—particularly silicon-target types—for industrial and military jobs.

Fast on the heels of the Tivicon [Electronics, March 16, p. 150] comes the product unveiling of RCA's silicon intensifier tube.

Another development is the tube's inherent ruggedness and ability to withstand high-intensity light sources. Westinghouse, which supplies the secondary electron conduction camera for the moonwalks, had to develop a wire mesh to guard the camera's target after most of the target burned out from exposure to the sun's rays or their reflection from the spacecraft on the Apollo 12 mission.

The SIT camera tube consists of a vidicon-type scanning electron gun and image intensifier section separated by a special silicon tar-
Cryogenic testing?

Emerson would have liked that.

Old Ralph Waldo said it: "Men love to wonder, and that is the seed of our science."

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Sensitive family. With a diameter of 40 millimeters, the C21117C (left) exhibits the highest resolution among this SIT trio.

get. The target features an integrated circuit array of more than 600,000 p-n junction diodes. The image intensifier section converts the light at the scene into a flow of electrons that are greatly accelerated and imaged on the silicon target. This target provides the gain which results in the tube's high sensitivity at low light levels.

A key factor is the impact ionization-gain mechanism in the target when using electron imaging instead of light imaging. Photoelectrons from a photocathode in the front of the tube are imaged onto the silicon target after being accelerated through about 10 kilovolts in the image section.

Under a design technique first developed at Bell Telephone Laboratories, the target consists of a two-dimensional array of diodes formed in an n-type silicon wafer, made thin in the active region. A thicker rim provides mechanical strength. The diodes are positioned with the p-region of each diode facing the electron scanning beam. An insulator covers the exposed n-regions appearing between the p-regions to prevent the scanning beam from reaching the substrate. During operation, the diodes normally are reverse-biased. Each diode acts as a storage capacitor because of the insulating properties of the depletion layer.

The accelerated photoelectrons interact with the silicon, creating a large number of hole-electron pairs for each primary electron. The holes diffuse to the depletion layer and discharge the storage capacitors, storing a charge pattern. The output signal is generated when the scanning beam passes over the capacitive elements and produces a displacement current during the recharging of each capacitor.

The current gain between the photoelectron current and the video output current is determined by the energy a photoelectron requires to make a hole-electron pair—3.5 electron volts for silicon. Primary resolution losses in the tube result in the image and scanning section. The amplitude response of a typical SIT tube is 30% at 400 tv lines. The figure dips to 10% at 600 lines while at 200 lines it is close to 70%.

At the normal target operating voltage of 8 volts, dark current is between 5 and 10 nanoamperes; from this target voltage maximum signal currents of 700-500 na can be obtained. The rise and decay curves show that the lag is comparable to other vidicons; lag is less than 5% in 50 milliseconds from a starting current of 500 na.

Although the SIT camera tube is currently aimed at special industrial and government needs, RCA hopes eventually to use it in color-tv broadcasting.

Clifford Lane, general manager of RCA's Industrial Tube division,
expects the military low-light-level television camera market to exceed $5 million in 1970 and to surpass $25 million by 1975. Lane foresees a variety of applications for the camera, from crime prevention and industrial surveillance to night navigation in ships and planes.

Three models, providing a choice of resolution, are available: type C21125, 16 millimeters in diameter; C21130, 24 mm; and C21117C, 40 mm.

The single-unit price is approximately $15,000. However, the price is expected to drop to approximately $5,000 each in production quantities.

The keenest competition to the SIT probably will come from the SEC tube. Westinghouse's protective wire mesh serves as the mechanical support for the aluminum oxide signal plate and low density potassium chloride layer in the SEC target. The standard tubes use an aluminum oxide film for mechanical support.

Since the thermal capacity of the mesh is much greater than that of the standard aluminum oxide support layer, the initial rate of temperature rise from exposure to light will be lower in the new target. The older models depended on radiation cooling to dissipate heat but large temperatures built up when the camera was suddenly exposed to intense light.

Just how well the new SEC tube can resist damage has not been ascertained. The new unit was not ready in time to be used in the latest Apollo launch except as a ground station camera in the gantry. The new protective device does not alter the tube's basic responses, nor the picture quality. But the photocathode voltage must be reduced to 2 kilovolts by a bleeder network, to maintain the same gain as the standard SEC tubes.

Westinghouse plans to keep its foot in the door of the silicon-target tubes market with soon-to-be-shown prototypes of silicon intensifier and silicon vidicon tubes. A feature of their new silicon family will be the complete interchangeability of sockets between the SIT's and the SEC's, with little or no modification.

New materials

Sapphire attractive for optical, IC jobs

Synthetic white sapphire to 3 in. diameter by 8 in. long, and 2¼ in. diameter by 12 in. long, is available in production quantity. The large sizes of white sapphire can be produced in several crystallographic orientations. Major optical applications include precision IR elements and windows. Other new uses are three major classes of substrates for IC work. These include: beam lead substrate carrier plates for the Bell Telephone beam lead process currently in practice by several major IC manufacturers; the silicon-on-sapphire substrate process under development; and thin-film passive substrates for the demanding tantalum and silicon technologies. The material's dielectric properties make it attractive for microwave IC devices. Union Carbide Corp., Crystal Products Dept., San Diego, Calif. 92123 [401]

Eccocote C-26 is a clear epoxy coating that maintains a surface resistivity of 10¹⁰ ohms/square up to temperatures as high as 500°F. It also has excellent environmental resistance and can be used successfully in locations where it may be exposed to solvents and high moisture. Suggested applications include coil or motor windings, p-c boards, circuit modules, and wherever a dielectric surface needs to be protected against moisture and solvents. The material sells for $36.40 in a 13-lb. kit. Emerson & Cuming Inc., Canton, Mass. 02021 [402]

Teckstik is an easily installed emi-rfi shielding knitted wire mesh gasket with a fluid seal of neoprene or silicone rubber. The sponge elastomer, which is backed with a pressure-sensitive adhesive, is protected with a release paper until ready to apply. Resilient and conductive Teckstik is highly effective in eliminating radiated emissions at the joints in all types of emi/rfi tight enclosures, while at the same time sealing out fluids or retaining cooling air. Technical Wire Products Inc., Cranford, N.J. 07016 [403]

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New Books

Making microwaves meaningful
Microwave Theory and Applications
Stephan F. Adam
Prentice Hall, Inc.
513 pp., $14

The microwave engineer finds himself in a broad frequency spectrum where elaborate measurement schemes are required to interpret the many properties of the signals he must analyze. In the past, he has had to turn to specialized periodicals, old college textbooks, and commercial equipment manuals to set up and carry out his measurements, and to interpret them. But this book should be a boon to the microwave engineer.

Drawing upon the author's seven years of teaching experience and the knowhow of a company strong in microwave measurements—Hewlett Packard—the book covers transmission line theory, basic principles, laboratory signal sources, signal analysis, and network analysis. Waveforms are well represented, both in drawing and photograph, as are block diagrams and flow charts. Test setups are presented in step-by-step form and many common measurement errors are described, as are measures to correct them.

The chapter on signal analysis will be of particular interest to engineers who spend time in the laboratory since it deals with power and frequency measurement. The latter is treated by investigating resonant circuits, coupling mechanisms, wavemeters, and slotted lines. Swept-and fixed-frequency techniques are covered in detail. The section on spectrum analysis probably will find the widest readership—it deals with both theory and applications.

The major characteristics of the spectrum analyzer, such as tuning, resolution, and sensitivity, are presented with reference to curves and diagrams. Input waveforms are seen with their corresponding analyzer displays. And common errors, such as base line lifting, pulse desensitization, and sensitivity versus envelope resolution, are clearly explained. Error interpretation and error correction are detailed by means of equations and diagrams. The section on applications of spectrum analysis employs oscilloscope photographs to display actual situations; the preselector section covers many typical modulation and distortion signals.

Radio-frequency interference measurements using the spectrum analyzer also are detailed, with particular reference to Mil-Std-826A. Five RFI tests ranging from 14 kilohertz to 10 gigahertz, are presented step-by-step with block diagrams emphasized.

The remainder of the chapter concerns itself with power measurements using various techniques. The bolometer, thermistor, and calorimeter are discussed, and particular attention is given to the types of mounts used, tuning methods, and error compensation. Peak and average power measurement methods are outlined with references to block diagrams and outline drawings.

Another important chapter deals with network analysis and is divided into three parts—scattering parameter measurements, time domain reflectometry, and noise figure measurements. For transmission measurements, particular attention is focused on attenuators. The various types are listed, while the many techniques used are put forth in detail. Among these are r-f, d-c, and i-f substitution; swept and fixed frequency approaches are described.

The section on impedance measurement deals with standing wave ratios, slotted lines, Smith charts, reflectometers, complex s-parameters, and network analyzer systems. A computerized automatic network analyzer is described through flow graphs, photographs, and block diagrams.

The chapter is concluded with a treatise on noise measurements. The methods described vary from the signal generator power meter technique to the excess noise source approach. Cascaded networks are analyzed and accuracy considerations are detailed.

In the chapter on laboratory sources for microwave signal gen-
eration, the devices covered range from the older triode oscillator to the new Gunn diode oscillator. Aside from going into detail on travelling wave tube amplifiers, klystrons, and backward wave oscillators, the chapter covers p-i-n diode modulators, harmonic generators, frequency multipliers, and signal source leveling. Also described are signal generators, sweep oscillators, phase locking of signal sources, and frequency standards. Tunnel diode amplifiers, yig tuned oscillators, and lasers also are covered.

The book is rounded out with a chapter on the principles of microwave measurements and one on transmission line theory. The former discusses signal detection and flow graph representations, while the latter deals with coaxial and waveguide transmission lines, and resonant cavities.

**Government Reports**

All these reports are available from the Clearinghouse for Federal Scientific and Technical Information (CFSTI), 528 Port Royal Rd., Springfield, Va. 22151. Price is $3 for each hard copy, or 65 cents for each microfiche.

**Design and development of an isotropic altimeter setting indicator AD 696 981 and AD 696 586**

An altimeter indicator using a radioactive alpha particle emitter as the primary sensor has been built for future use and possible developmental work by the Federal Aviation Administration. Details of the system are described in this two-volume report.

**Course line computer/pictorial display AD 696 575**

Details modifications made on an FAA-owned display to improve its utility, reliability and accuracy. They included a new type display which permits the pilot to choose any scale map, ranging from 5-to-50 nautical miles per inch, by merely "dialing" a station and scale, and activating a reset button.

**Study and evaluation of the Omega navigation system for transoceanic navigation by civil aviation AD 697 699**

Identifies the system's error sources and evaluates their effect on the ability of an aircraft to maintain the desired flight track.

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Technical Abstracts

Avalanche amp

Avalanche diode power amplifier for Ku-band
J. Noisten
Microwave Associates Inc.
Burlington, Mass.

Combining two avalanche diodes in a reflection type amplifier provides about 3 decibels of gain and 1.4 watts of output power from 15 to 18 gigahertz. The amplifier was tuned for saturated operation.

The silicon avalanche diodes were mounted side by side in a waveguide circuit with their axes parallel to the electric field. A sliding-waveguide short on one side of the cavity and an inductive iris at the input-output terminal formed the resonator circuit. The diode impedances were set to a value for efficient amplification by means of an individual-bias control. Since both diodes were strongly coupled, each diode served as part of the resonance circuit of the other, and by tuning the resonances away from each other, wideband amplification was realized. However, this adjustment was limited because resistive-diode loading changed, and the value for stable amplification might be exceeded.

The amplifier's small signal gain was constant; however, when the drive power was increased, the gain dropped to 3 db, and saturation occurred at 1.25 watts output. When the input power was reduced so the output power fell between 600 and 800 mw, the gain rose to 6 to 8 db. The bandwidth of the amplifier was strongly dependent upon the tuning conditions of the resonator.

The output power of the avalanche-diode amplifier consisted of the input power added to the maximum power that the diode is able to produce as either an amplifier or an oscillator. To maximize the efficiency of a two-diode amplifier, both diodes must be tuned so that they contribute their full available power to the output signal.

The f-m and a-m noise characteristics of the amplifier were measured within a band of frequencies extending from 100 hertz to 1 megahertz away from the amplifier's center frequency. The f-m noise of the amplifier was equivalent to that of a klystron for modulation frequencies up to 100 khz; however, at higher frequencies the amplifier noise was higher. The a-m noise was dependent upon resonator tuning and was slightly higher than that of a klystron.


Barrier pressure

Pressure-sensitive Schottky-barrier transistor and switching device for keyboard applications
Osaka, Japan
G. Kano, S. Fujiwara, and T. Sawaki
Matsushita Electronics Corp.

A Schottky-barrier transistor, sensitive to pressure, and a pressure-triggered thyristor have been built for use in keyboard switches. These devices can drive 930 series diode-transistor logic integrated circuits. Their output voltage at OFF is less than 0.1 volt and at ON is greater than 3 volts. At maximum pressure, Schottky-barrier reverse current is 50 microamps.

Both devices employ the anisotropic stress effect of a metal-semiconductor Schottky barrier, caused by the change of barrier height from the pressure-induced change of the energy gap. The Schottky barriers with this stress effect were formed on the surfaces of the base region of a npn silicon planar transistor and on the n-gate region of a npnp silicon planar device.

A sapphire stylus, whose radius is 75 micrometers, applies the pressure. In the case of the transistor, if there's no pressure, when the collector-emitter voltage goes from 0 to 5 volts, the collector current goes from 0 to 2.4 milliamperes; if the pressure is 5 grams, the collector current at 5 volts is 11.8 ma.

This pressure-sensitive transistor differs from others in that changing the pressure changes the polarity of the transistor's output current, not its magnitude.

Both devices are extremely reliable. In life tests, they've been switched more than 1 x 10^7 times.

New Literature

R-f millivoltmeter. Boonton Electronics Corp., Route 287 at Smith Rd., Parsippany, N.J. 07054, has issued a four-page brochure illustrating and describes model 92A programmable r-f millivoltmeter with digital readout and autoranging options. Circle 446 on reader service card.

Inverters and frequency changers. Nova Electric Mfg. Co., 263 Hillside Ave., Nutley, N.J. 07110, has available a catalog on a complete line of single and three-phase inverters and frequency changers. [447]


Noise-figure meter. International Microwave Corp., 33 River Rd., Cos Cob, Conn. 06907. Data sheet ANM-1 lists design features and specifications for a solid state, automatic noise-figure meter. [450]

Thermal shock chambers. Tenney Engineering Inc., 1090 Springfield Rd., Union, N.J. 07083. Mechanically refrigerated thermal shock chambers are illustrated and described in four-page bulletin 121. [451]

Custom power supplies. Hi-G Inc., Spring St. and Route 75, Windsor Locks, Conn. 06096. Bulletin 200 describes the company's capabilities and facilities for the manufacture of custom power supplies, and illustrates and describes some typical units. [452]

Molding compound. Martin Marietta Corp., P.O. Box 5837, Orlando, Fla. 32805, offers a technical data sheet describing epoxy liquid-transfer molding compound 303-13. [453]

Metal film resistors. Vishay Resistor Products, 63 Lincoln Highway, Malvern, Pa. 19355, has released an eight-page brochure on 1 ppm/°C high-precision metal film resistors. [454]

Op-amp relay. Teledyne Relays, 3155 W. El Segundo Blvd., Hawthorne, Calif. 90250. Application notes for the 501-1 universal op-amp relays are available in a 14-page brochure. [455]


Angle-position indicator. North Atlantic Industries Inc., Terminal Dr., Plainview, N.Y. 11590, has available a single-page bulletin describing the 712 series low-cost signal source suitable for laboratory or field use. [457]

Signal source. PRD Electronics Inc., 1200 Prospect Ave., Westbury, N.Y. 11590, has available a single-page bulletin describing the model 8525 solid state, angle-position indicator. [458]


Video tape. Audio Devices Inc., 100 Research Dr., Glenbrook, Conn. 06906, has announced availability of a full-color dart-board-type wall chart in connection with its new line of Audev helical-scan video tape. [460]

High-power transistor. Westinghouse Electric Corp., P.O. Box 868, Pittsburgh, Pa. 15230. An illustrated bulletin gives the operating characteristics, advantages, applications, and package dimensions for the 1401 high-power silicon transistor. [461]

Electronic glass. Innotech Corp., 181 Main St., Norwalk, Conn. 06851. Passivation glasses, solder glasses, and packaging glasses are discussed in a six-page brochure. [462]

Digital ultrasonic delay lines. Microsonics Division, Sangamo Electric Co., 60 Winter St., Weymouth, Mass. 02188, offers an illustrated, two-color brochure describing new digital ultrasonic delay lines. [463]

Operational amplifiers. GPS Corp., 14 Burr St., Framingham, Mass. 01701, has available a brochure on the 800 series of FET differential operational amplifiers. [464]

Graphics digitizer. Computer Equipment Corp., 14616 Southlawn Lane, Rockville, Md. 20850. The PFIOC coordinate digitizer for rapidly converting analog/graphic data into digital form is fully described in an eight-page folder. [465]

Coil-winding equipment. Geo. Stevens Mfg. Co., 6001 N. Keystone Ave., Chicago 60646, has released a two-page bulletin on the model WE-20 automatic turret universal coil winder and four new accessories. [466]

Triacs. Texas Instruments, P.O. Box 1271, Austin, Texas 78764. A six-page catalog features triacs and their applications. [467]

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Electronic Accuracy Through Mechanical Precision
New Literature

5012, M/S308, Dallas 75222. Application reports CA-137 and CA-138 describe triac phase-control and triac triggering techniques. [467]

Zener surge selection. Unitrode Corp., 580 Pleasant St., Watertown, Mass. 02172, has issued a zener surge-selection guide listing all its zener types from 1 to 10 watts with the specific voltage and surge rating for each individual type. [468]

Data transmission. Quindar Electronics Inc., 60 Fadem Rd., Springfield, N.J. 07081, has published a two-page technical application bulletin entitled "Wide Band vs. Narrow Band Data Transmission... and How It Affects Supervisory Control Systems." [469]

Numeric keyboards. Micro Switch, a division of Honeywell, 11 W. Spring St., Freeport, Ill. 61032, offers a new product sheet on the NW3 series numeric keyboards that incorporate an assembly of 12 or 16 stations with a Form A contact structure. [470]

P-c board connector. Hugh H. Eby Co., 4701 Germantown Ave., Philadelphia, has available a technical bulletin on a right-angle male connector for p-c boards that conforms to revision K of MIL-E-5400. [471]

Gate valves. Varian, Vacuum Division, 611 Hansen Way, Palo Alto, Calif. 94303, has released a data sheet describing gate valves for vacuum systems which are now available in 2 in., 4 in., and 6 in. configurations. [472]

Subminiature lamps. Inter-Market Inc., 312 Waukegan Rd., Glenview, Ill. 60025, offers a catalog and price list describing Rodan subminiature lamps and indicator lights. [473]

Synchro amplifier. Industrial Control Co., Central Ave. at Pinelawn, Farmingdale, N.Y. 11735, has published a data sheet describing the model 929-R precision, 400-hz synchro amplifier. [474]

Cermet trimmer. CTS of Berne Inc., Berne, Ind. 46711, has a catalog and price list describing series 360 single-turn cermet industrial trimmer. [475]

Pressure transducers. AMETEK/Instruments & Controls, 860 Pennsylvania Blvd., Feasterville, Pa. 19047, has published an eight-page, free-color pressure transducer catalog. [476]

Linear IC testing. I.C. Metrics Inc., 607 Industrial Way West, Eatontown, N.J. 07724, has published an eight-page, high-precision, 400-hz synchro amplifier. [477]
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