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Planar is a patented Fairchild process.
Planar and power are no longer contradictory terms. You can now buy Fairchild Planar power devices with up to 60W power dissipation, up to 12A current, with voltages up to 300V. Here's why: we deposit nickel-chromium resistors in series with the emitters, right on the chip. This technique inhibits secondary breakdown and keeps all the inherent advantages of Planar devices:

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One thing all these films have in common is a sharp, high-contrast image that's easy to read. Because the films are so sensitive, you can use small camera apertures and low-intensity settings.

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Most oscilloscope camera manufacturers have one. For instance: Analab, Beattie-Coleman, BNK Associates, Fairchild, EG&G, General Atronics, Hewlett-Packard, and Tektronix.

You can get complete information by writing to Polaroid Corporation, Technical Sales Department, Cambridge, Massachusetts 02139, or by writing to one of the manufacturers mentioned above.

It will probably take a little longer than 10 seconds, but we promise the information won't be a waste of time.

"Polaroid" and "PolaScope"
How would you like an anti-snivet tube for your color TV horizontal deflection circuits? That's right—an anti-snivet tube would eliminate the need for snivet-suppression circuitry with its extra components and extra costs.

The new Amperex 6KG6 output pentode is your tube! It was specifically developed to eliminate Barkhausen oscillations and achieves its snivet-free performance through the use of a Cavitrap anode and other unique design characteristics. The 6KG6 can be operated with grounded suppressor at B+ supply voltages from 240 to 400 volts and still remain snivet-free.

Life-expectancy and reliability of the 6KG6 is far above that of ordinary output pentodes in similar service due to its lower bulb temperature at maximum operating conditions; it operates about 50°C cooler than comparable tubes in similar service. But in spite of its low operating temperature, the 6KG6 packs plenty of reserve power and added safety factors. It has a max. 7,000 V peak plate voltage, 34 watts max. plate dissipation and 1.4 amps peak anode current.

The 6KG6 is but one of the growing family of new Amperex tubes for color TV that make possible lower costs, higher quality and more reliable performance. Other new tubes in this family include the 6EC4 damper diode, a matching companion to the 6KG6 for horizontal deflection circuits, the 3BH2 high voltage rectifier diode and the ED500 shunt stabilizer.

For data and applications information, write to the company still doing new things with receiving tubes: Amperex Electronic Corporation, Semiconductor and Receiving Tube Division, Dept. 371, Slatersville, Rhode Island, 02876.
This Wilcox Model 914 ATC transponder uses Allen-Bradley Type CB 1/4-watt and Type EB 1/2-watt fixed resistors, Type G variable resistors, and Type R adjustable fixed resistors. The Model 914 transponder is for aircraft operating under ground control radar.

"No failure ever" is an impressive record, especially since Allen-Bradley fixed and variable resistors have been used in Wilcox transponders for around ten years.

The reason for this consistently high performance is the unique hot molding process developed and used only by Allen-Bradley. In fixed resistors, it produces such complete uniformity that long term A-B resistor performance can be accurately predicted. Catastrophic failures don't occur with Allen-Bradley hot molded resistors.

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Our experience shows no failure ever of an A-B resistor unless subjected to overload caused by a tube or transistor failure

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J Package
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UT-4001—Ladder switch for driving resistor ladder networks
UT-4024—Buffer amplifier

*Series SE100, NE100, CS700, SU300, LU300, SE400, NE400, SE500 are all available from Sprague Electric under technology interchange with Signetics Corp.

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For data sheets on the microcircuits in which you are interested, write to:
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Sprague Electric Company
347 Marshall Street
North Adams, Mass. 01247
Communications gap exists in medical electronics PAGE 17
New process boosts gallium arsenide devices PAGE 21
Microwaves used to study rocket plumes PAGE 24
Data and voice time-share a telephone system PAGE 22
Systron-Donner's system for direct readout of microwave frequencies from 0-15 GHz provides an unprecedented simple and low cost solution – features no other Ge counting system offers. Further, S-D's "think ahead" design provides even greater flexibility. As you can see, an ever-growing number of plug-ins can be used interchangeably in the basic 50 and 100 MHz counters. When your digital measuring needs change, you change plug-ins, not counters. That way you always have a state-of-the-art-counter.

EXCEPTIONAL STABILITY—Another exclusive feature of S-D's counter line is a high stability oscillator with an aging rate of better than

1 part in 10⁹ per 24 hours!

NEW INSTRUMENT STYLING—In addition to unmatched performance and flexibility in counting instrumentation, new styling refinements have been added to the entire line of S-D counters: die cast front panel, wrap-around cabinetry, tilt stand, and a simplified method for bench or rack installation.

*ACTO: Automatic Computing Transfer Oscillator plug-ins. For fully automatic microwave measurements with counter accuracy and instantaneous direct readout.
Adding fuel to fuel cell

Army develops air-breathing fuel cell

An air-breathing fuel cell for use by front-line troops was demonstrated by the Army at the recent Annual Power Sources Conference at Atlantic City, N. J. The cell uses the nitrogen-hydrogen compound hydrazine for fuel and extracts its oxygen directly from the atmosphere.

The new power source was built by Monsanto Research Corp. under the technical direction of the U.S. Army Electronics Command. It weighs less than 12 pounds, has an output of 60 watts and can operate for 12 hours on one pound, or pint, of fuel.

According to Army spokesmen, the cell's introduction into field use is expected to have a major impact on military portable electrical power equipment. This is because of the fuel cell's silent operation, simple maintenance, low operating temperature and high efficiency for converting fuel into current. In demonstrations, the cell has powered an Army AN/VRC-12 vehicular radio, an Army AN/PPS-4 ground surveillance radar and a Marine Corps manpack radio AN/PRC-47.

Northeast Corridor may acquire branch line

The state of Pennsylvania is investigating the feasibility of a high-speed ground transportation system that would link up with the Federally-sponsored Northeast Corridor high-speed rail system. An $80,000 contract has been signed with the Westinghouse Air Brake Company to study the possibility of creating a Keystone Corridor, as the east-west rail link would be called.

Westinghouse will investigate and analyze the effect of the various systems under consideration for the Northeast Corridor (ED 6, March 15, 1966, pp. 22-26) on a Keystone Corridor. The company will also study possible routes for 150-mph and 300-mph service across the state, as well as possible rates and schedules for both 150- and 300-mph service.

The Westinghouse contract represents only part of the $200,000 that Pennsylvania has made available for transportation studies under the Urban Mass Transportation Assistance Act. According to Pennsylvania

News Report

Secretary of Commerce John K. Tabor, "This is a unique Pennsylvania accomplishment, and marks the first such state sponsored program in the nation."

Thick-film transistor announced

A new technique for manufacturing solid-state active devices has been announced by the National Aeronautics and Space Administration. The exploratory work, performed jointly at NASA's Langley Research Center and RCA's Defense Microelectronics Activity, involves the use of deposited thick films to produce majority-carrier field-effect transistors (FETs).

An array of 50 individual FETs, manufactured at RCA's Sommerville plant by this process, was recently evaluated at Langley. NASA physicist Robert Stermer reported that, though the devices did not have the performance of diffused FETs, the practicality of the process had been demonstrated and further research would be undertaken. Stermer indicated that the measured transconductance was greater than 800.

He said that the devices were at present being made with a "wet spray" technique. Powdered cadmium sulfide is first doped and then made into a slurry with water. The slurry is sprayed on to an insulating substrate and deposition of insulating material and aluminum metallization follow. Stermer pointed out that the devices are actually formed by applying voltages to the metal electrodes and accumulating charge carriers along the surface. The cadmium sulfide's inherently high impedance was what made the technique possible, he said.

The original idea was jointly conceived by Stermer and Dr. Franz Huber, Group Leader at the RCA Activity, but it was the RCA group that did much of the actual work. As a result, RCA was awarded a $35,000 contract to continue the materials investigation and further refine the fabrication technique.

The first phase of the contract, Stermer said, will concentrate on study of the doping method (consistency, predictability, etc.) and the possibility of screening the semiconductor material into the substrate, rather than spraying it on.

June 7, 1966
High-speed logic slows IBM production

Production difficulties with their advanced solid-logic technology (ASLT) circuit packages will delay initial delivery of IBM’s supercomputer, the System/360, Model 90. Deliveries of the Model 90, which is the largest and fastest member of the System/360 family, were planned to begin in 1967. According to the company, earliest shipments will now be delayed two to three months because of a “production bottleneck”.

The ASLT circuit packages (ED, Nov. 8, 1965, pp. 12-15) have delay-time speeds of 1.5 ns, compared with 5 to 30 ns for the solid logic technology (SLT) circuit packages used in the other models of the System/360. The increase in speed is achieved by use of current-steering circuitry and high packaging density. The ASLT packages are designed to be produced on the same automated production line used for the SLT packages.

Klystron amplifier delivers 21 kW cw

The most powerful klystron amplifier known to date has been successfully tested at Varian Associates. The tube puts out 21 kW cw at 18 GHz over a band of 50 MHz with a gain of 60 dB.

“I don’t know what the power limitations of the tube are,” designer senior engineer Bill A. James said. “100 kW cw seems extremely difficult, but not impossible, with present-day technology”. He explained that cryogenic cooling and other improvements could boost the output power considerably. “There is no magic in the design. We simply optimized the well-known design of the beam optics and the electron gun,” he added.

The tube’s power capabilities are especially important in plasma research, in space communications and in radio astronomy, James claimed.

Nimbus II scores several firsts

The Nimbus II experimental weather satellite, launched recently by NASA, embodies several features not included in any of the other weather satellites now in operation. The two most significant are its capacity for transmitting night-time infrared pictures to simple ground stations, and its inclusion of a code on each transmitted picture to allow ground stations to determine the time it will be in position and angle to beam in the station’s antenna.

Besides photographing the Earth’s cloud cover, Nimbus II will measure for the first time the heat balance budget (albedo) of the entire 200-million-square-mile area of the Earth every day. Although the primary purpose of Nimbus II is research and development, data collected will be forwarded to the Commerce Department’s Environmental Services Administration for operational weather-forecasting purposes.

Sylvania will light up stamps

A phosphor that has particles of unusually minute size has been developed by Sylvania Electric Products, Inc., and will soon be incorporated in the ink used to print all U.S. postage stamps. The phosphor will make it possible for machines to differentiate between regular stamps and air-mail stamps and thus facilitate the automatic processing of mail.

Six different phosphors were tried during a two-year test by the Post Office Department at Dayton, Ohio. On the basis of this test, two of the phosphors were chosen for nationwide use: an orange-red phosphor for airmail stamps and a green phosphor for regular stamps. There are 204 machines, currently in use in 40 cities, capable of identifying the “tagged” stamps.

Mariner IV back in range

The Mariner IV spacecraft, which took the world’s first close-up pictures of Mars last year, is again in contact with Earth, reporting on the space environment and its own operating performance after 18 months of flight. On October 1, 1965 telemetry contact with the spacecraft in its orbital path around the sun was lost at the then unprecedented distance of 191 million miles. Since then NASA has put into operation its new 210-foot diameter antenna at Goldstone, California, and on May 21, 1966 the 210-foot dish began receiving Mariner telemetry signals from a distance of 197.5 million miles.

The new signals indicate that all spacecraft systems are operating properly. Mariner IV is now transmitting over its low-gain antenna, since the high-gain antenna used during the Mars flyby is no longer pointing towards the Earth.

A preliminary analysis of the signals received by the Goldstone antenna indicate that Mariner IV has exceeded its designed life by more than 100 per cent.

An updated “Who’s who” in electronics manufacturing in the western states has been published by the Western Electronic Manufacturers Association (WEMA). Copies can be obtained for $5 from WEMA, 3600 Wilshire Blvd., Los Angeles, California 90005.
Relay Reliability in 3-Gram Packages

You can cut problems down to size with the Bourns TRIMPO T® Model 3100 SPDT and Model 3101 DPDT relays. These proven units give you MIL-Spec reliability and 160 mw sensitivity in a package size of less than 1/20 cubic inch. Punish them with 150G shock or 40G, 3000 cps vibration, and you still get the performance that’s on the published data sheet. Features include highly efficient magnetic circuit, rotary balanced armature, hermetically sealed case and self-cleaning contacts. Environmentally they exceed requirements of MIL-R-5757D.

With these relays, there is little chance of “infant mortality” in your circuits. Bourns double-checks each unit with a 5000-operation run-in followed by 100% final inspection for all important characteristics, including mass-spectrometer leak testing. Furthermore, Bourns tests samples quarterly in the famous Reliability Assurance Program that it created for TRIMPOT potentiometers. In short, you can trust the name TRIMPOT in relays, too.

Other TRIMPOT relay products available from Bourns: Model 3105 subminiature AC DPDT and a full line of miniature adjustable time-delay and voltage-sensing relays and modules. Delivery is immediate. Write today for complete technical data.

<table>
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<tr>
<th>Feature</th>
<th>Specification</th>
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<tbody>
<tr>
<td>Size</td>
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<td>Contact rating</td>
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<td>Pickup sensitivity</td>
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<td>(Model 3100)</td>
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<td>Standard coil resistances</td>
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<td>65 to 2000Ω (Model 3101)</td>
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The new Sorensen QSA Series offers the only modular power supply line in the 0 to 35 volt range that combines ± .005% regulation line and load, 20 μs. response time, 71°C operating temperatures, 300 μV ripple—all at prices below other lines having lesser performance specifications. Sorensen's QSA Series modules are ideal for OEM, lab or system applications. They can be used as bench models (mounted in any position) or mounted in combinations of 3 or 4 in an optional 19" (3½'' high) rack adapter. Other design features include: Load current vs. temperature, 110% @ 40°C—100% @ 50°C—85% @ 60°C—66% @ 71°C • Temperature coefficient 0.01%/°C • Stability 0.025%/8 hrs. • Models QSA10-1.4, QSA10-2.2 and QSA10-3.7 permit operation of up to 20 units in series; other units permit operation of 2 units in series; All models permit operation of 4 units in parallel • No turn-on/tum-off overshoots • Remote sensing • Remote programming • Ripple voltage peak to peak 3mV. All Sorensen power sources conform to proposed NEMA standards. For additional QSA Series details or for data on other standard/custom DC power supplies, AC line regulators or frequency changers, call your local Sorensen representative, or write: Raytheon Company, Sorensen Operation, Richards Avenue, Norwalk, Conn. Tel: 203-838-6571, TWX: 710-468-2940.

<table>
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<th>MODELS (RANGES)</th>
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<th>QSA10-3.7 (0-10V, 3.7A)</th>
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<td>$89</td>
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Medical electronics: design deficiencies cited

Communications gap between engineer and physician discussed at biomedical symposium

Ralph Dobriner
West Coast Editor

Though the role of electronics in medicine continues to grow apace, so too does the communications gap between physicians and electronic designers.

This apparent contradiction was evident at the recent Biomedical Sciences Symposium in Anaheim, Calif. There, a variety of new bioelectronic monitoring systems, sensory devices and specialized computer systems to aid in medical diagnosis and research were discussed. Yet, many of those present felt that much of the equipment—such as the large automated physiological monitoring systems—are expensive, rather complex to operate and often designed by engineers unfamiliar with medical requirements.

Only 1% of patients benefit

Dr. David Davis, Professor of Surgery at the University of North Carolina School of Medicine, said: "In spite of fantastic improvements in instrumentation and considerable publicity attending the use of such instrumentation in patient care, it is doubtful that more than 1% of patients deserving this 'improved' care actually receive it."

"What could be done has not been done," he observed, adding that the number of people that benefit from modern instrumentation was questionable. The principal cause for this lack of clinical application of instrumentation, according to Dr. Davis, has been the failure of the clinician and designer to get together on the design requirements.

It is simply a lack of education, he said, noting that physicians, physical scientists and engineers simply do not speak the same language: they don't think alike and there is no contact in the literature of each profession.

The surgeon said that the average practicing physician, no matter how skillful and successful, is absolutely unfamiliar with such terms as strain gauge, spectrophotometer, analog or rectifier. On the other hand, the physical scientist and engineer has usually had no exposure to biological systems, and, for example, the variable responses of a cell, Dr. Davis said.

Reasons for lack of use

Why have so-called monitoring systems not become widely used? Dr. Davis gave the following reasons:

- The instrumentation is incompatible with clinical situations. The designers of medical instruments have no place to go to test their prototypes. Mostly their hospital connections are few and the family physician may be their only source of friendly advice. For example, many transducers function beautifully in aqueous solutions. Blood is principally an aqueous solution, but it also possesses the important and life-saving property of clotting. A surprising number of designers have neglected this well-known fact.

- The instrument does not present information in an intelligible or useful manner to the physician. The well-known electrocardiogram, for example, provides the instrument designer with a signal relatively easy to obtain, being essentially nothing more than a low-frequency series of pulses in the millivolt range. But the recording represents only a brief period in the patient's life and marked and significant changes may take place in seconds as well as over years.

Techniques for the continuous display of the electrocardiogram on oscilloscopes have been developed and many devices are on the market. The information, however, is significant only if observed and interpreted in a patient who is under constant surveillance by a physician.

Microminiature temperature transmitter, designed to be implanted in a human body, is said to be capable of detecting temperature changes of 1/100 to 1/1000°C. Developed by NASA's Ames Research Center, the transmitter contains four hybrid integrated-circuit chips mounted inside a hermetically sealed can for protection from body fluids. The circular portion contains the battery and antenna and is usually left outside the body.
(biomedical, continued)

trained in electrocardiographic interpretation.

What this means, then, is that, regardless of improved data collection techniques, better means of data storage and recall are needed and, more important, methods of cross-correlation data. Attempts have been made to use large multipurpose computers for this, but little has been done with small, specialized computers.

*The information presented is not particularly important to the existing clinical situation.* Many instruments have been developed which display information in which the physician is not particularly interested. For example, few physicians require minute-by-minute measurements of body temperature in clinical situations. Also, many monitoring devices are developed around a physiological function that is easy to measure but which may mean little.

*Many medical monitoring instruments are unreliable.* The requirements of instrumentation used in clinical care may exceed even those imposed by military specifications. Any instrument the size of a cigarette package is surely, within a week, dropped on the floor, sent to the laundry or steam-sterilized. An instrument that cannot withstand these stresses is useless.

**Equipment must meet needs**

Drs. Stewart Wilber and William Derrick of the University of Texas cited the great need for reasonably priced instrumentation for simple, straightforward monitoring of patients in critical care areas, including all major operating rooms. Apparently, the only way to get this type of equipment, they said, is through a joint effort by the whole electronic industry and medical profession to establish standards.

They referred to studies that show that there would be buyers for equipment that met the biological, clinical and economic needs of doctors and hospitals. The medical care dollar will be spent on purchasing instrumentation that is basic in function and adaptable to several uses. Hospitals and research institutes are not willing to pay large sums of money for equipment that does not fill medical needs, they said.

Another important consideration mentioned by the doctors is simplicity and ruggedness of design.

"There is not time for calibration and setting of the base lines and plate currents, especially in what is considered the minimum monitoring equipment required for clinical critical-care use. If this is necessary, the cause is lost and the equipment is not used," the physicians said.

They also stressed the need for explosion-proof miniature electronic systems that are compatible with all types of biological signals and free from interference from X-rays, cauteries and ground loops of numberless variety.

This includes telemetering systems for data-acquisition in the operating room. The doctors suggested that wireless telemetry of data directly from the patient into the computer would be useful.

The toughest instrument problem in clinical investigation, they felt, was that of transducers. These simple and sometimes delicate devices may be affected by noise created by the subject or the instrument itself.

"The confounding noise created by modulation of the desired signal by other biological activity of similar frequencies creates severe problems," the doctors said.

**Army studies biological armor**

Among the newest and most unusual applications of medical electronics discussed at the symposium was a current program sponsored by the Army Medical Material Div. to develop a transportable type of biological equipment.

The instrumentation, referred to as "biological armor" by Moses Berlin of Sylvania Electronic Systems, Needham, Mass., is designed to help the soldier to combat the exotic condition he is liable to encounter in various parts of the world, such as weather extremes and endemic diseases.

Instead of using medical equipment originally designed for fixed hospital uses—and therefore bulky and fragile—the Army is focusing on the design of special-purpose, compact transducer and telemetry gear. Equipment carried by the soldier would constantly radio back to a central computer the soldier's physiological responses to varying

---

Apollo bioinstrumentation system to be worn by astronauts records and radios back to earth physiological measurements, such as body temperature, respiration and heart rate. Developed by Spacelabs Inc., Van Nuys, Calif., the system is now undergoing qualification tests.
SPRING-BACKED JEWELS (left above) dissipate much of the shock energy on a movement's moving part. This prevents damage to the jewel and avoids subsequent "stickiness" or inaccuracy. In contrast are the two constructions shown above right... so-called cushion-backed and fixed jewels. The fixed absorbs no shock at all. The cushion-backed protects a little, but still won't do the job under the rugged use to which most of us put meters and test equipment. On the other hand, a spring-backed jewel permits full deflection with less than 20% increase in pivot pressure. Simpson makes a complete selection of spring-backed meter movements as well as fixed. The spring-backed type costs only about 20¢ more than a cushion-back, and 40¢ more than a fixed jewel. Is the spring-backed jewel worth the small extra amount? You bet it is, say most of our customers. Write for Stock Catalog No. 2073 which lists 1400 sizes and types. It may well be the cure for meter headaches and complaints in your equipment.

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June 7, 1966
6 Reasons Why SPRAGUE is a Major Resistor Supplier

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Combine the best features of both precision and power wirewound types. Resistance tolerances to ±0.05%. Unusually tough encapsulation protects against shock, vibration, moisture, fungus. Meet MIL-R-26C requirements. Smaller than conventional wirewounds, yet greater in stability. Send for Bulletin 7450.

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Approach precision wirewounds in reliability and stability, yet are smaller in size and have lower self-inductance. Low, controlled temperature coefficient. Dense molded case provides outstanding humidity protection. Send for Bulletin 7000A.

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*coated*

**GLASS-JACKETED POWER WIREWOUND RESISTORS**

*deposited-carbon, molded case*
Approach precision wirewounds in reliability and stability, yet are smaller in size and have lower self-inductance. Low, controlled temperature coefficient. Dense molded case provides outstanding humidity protection. Send for Bulletin 7000A.

**ON READER-SERVICE CARD CIRCLE 891**


**ON READER-SERVICE CARD CIRCLE 892**

**ON READER-SERVICE CARD CIRCLE 893**

**ON READER-SERVICE CARD CIRCLE 894**

**ON READER-SERVICE CARD CIRCLE 895**

**ON READER-SERVICE CARD CIRCLE 896**

---

**NEWS**

*(biomedical, continued)*

conditions in the field. The computer would analyze and interpret the information to help predict the soldier's performance and to improve self-aid and first aid.

**Astronaut behavior monitored**

Berlin also described an unusual application of a computer that would determine the psychological reactions and behavior of astronauts during long space voyages and so prevent failures that might jeopardize the trip.

A preprogramed digital computer would be put aboard the spacecraft and, with adequate inputs derived from previous experience and known psychological behavior patterns, help to predict the astronauts' reactions. The psychological environment can then be programmed to make the pilot's adaptation easier and his performance more reliable, Berlin said.

For example, he observed it may be necessary to provide a diurnal-nocturnal cycle to simulate night and day in the space vehicle so that the pilot could sleep. The computer would program the duration of each phase of the cycle and control the conditions which constitute night: darkness, less humidity, cooler temperature, lower noise.

The occurrence of fatigue in the astronaut could be determined by measurement of blood sugar content and periods of rest scheduled optimally. If more than one spaceman was aboard, the relative psychological condition of each could be evaluated and an alternating assignment of command established.

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**Accuracy**

The author points out that in "Bootstrap action yields high Z1n wideband amplifier" (ED 10, Apr. 26, 1966, p. 93) the last line of the third paragraph should read: "... The 1.1-μF and 33-μF (not 3-μF) capacitors."

The two photographs appearing on pp. 21 & 22 of ED 11, May 10, 1966, were wrongly captioned. The caption under photograph 1 applied to 2 and vice versa.
New process boosts gallium arsenide devices

Vapor phase growth makes possible GaAs devices that cannot be easily produced by other processes.

René Colen
Microelectronics Editor

"Vapor phase growth will do for gallium arsenide what alloying did for germanium and diffusion did for silicon," said Dr. Fred D. Rosi, associate director of the Materials Research Program at RCA Laboratories, as he announced RCA’s development of a practical process for manufacturing gallium arsenide solid-state devices.

He added that experimental devices had already been made by this process that could not be made by any other. These devices are: an extremely bright photoemitting diode (red); a visible-light laser operating at room temperature; a high frequency Gunn-effect oscillator (up to 40 GHz); an electro-optical modulator for visible laser beam communications; and varactor diodes possessing high voltage and high cut-off frequency ratings.

Process uses gaseous sources

Rosi, speaking at an RCA news conference late last month, stated that this development involves the use of gaseous sources to form complete semiconductor devices in crystals of gallium arsenide and its alloys.

Rosi pointed out that both the impurity doping and the crystal growth is accomplished simultaneously in a single continuous operation. Beside being inherently simple, he said, this process avoids the contamination that is often found in alloying and diffusion technologies, where processing is accomplished in a number of discrete steps.

Describing the process, Rosi said that a gallium arsenide substrate is placed in a glass tube through which the various vapors are passed. Connected to this tube are a set of gated inlets that provide any combination of gases desired. The accompanying table summarizes Rosi’s description of the gases used and their purpose.

He pointed out that by adjusting the gas flows, any sort of growth may be accomplished: phosphide may be grown on gallium arsenide and doping may be changed from n-type to p-type. In addition, he noted, it is possible to control the crystal concentrations very accurately as a function of time, and thus form graded impurity concentrations and alloy growths.

This is highly desirable, he explained, since, in the former case, any sort of junction types may be made, and, in the latter case, the two crystal structures can be closely matched at their interface to prevent any crystal strain.

Dr. James Tietjen, developer of the vapor-phase process, claimed that hyper-abrupt junctions, where the impurity concentration goes from high to low to high, could theoretically be manufactured by this process. He wistfully added, however, that such work had not been performed yet.

Experimental devices produced

Tietjen, and his supervisor, Dr. Leonard Weisberg, demonstrated and showed a number of the devices built by this process. Among these were some of the photo-diodes, mounted in a standard pocket flashlight case, which emitted a bright red light, even in the presence of a high ambient incandescent light. RCA specifications on these devices indicate that the measured brightness at room temperature is over 500 foot-lamberts (@6000 A) with a current density of 1 amp/mm². Weisberg pointed out that a number of these devices had been made and that by varying the dopant gradient, a continuous improvement in efficiency had been achieved.

Also shown were some cut-away samples of the varactor diodes that were made by this process. Weisberg pointed out that 1000 of these diodes had been made and that the over-all yield, including handling, had been about 40%. The diodes, composed of a 5-mil heavily doped (selenium) N⁺ region, a 0.2-mil undoped N region, and a 0.5-mil heavily doped (zinc) P⁺ region, have a breakdown voltage rating of 60-to-100 volts, a cut-off frequency of 120-to-220 GHz, and a junction capacitance of 0.3-to-0.7 pF.

Rosi described the laser device as the first injection laser ever to emit visible light at room temperature. He claimed that the laser had a peak power output (@7260 Å) during a 20-ns current pulse of 60 amperes. He noted that the device was made from a gallium arsenide-phosphide alloy with a 33% phosphorus concentration.

According to Rosi, the opto-electronic modulator has one basic advantage over already existing types of optical modulators: it can be transversely modulated. Since the quality of modulation varies inversely with length, the longer the crystal is, the better it is, he pointed out. By being able to modulate transversely, an extremely long crystal can be used without the need for inordinately high driving voltages. Aside from this advantage, the device is also claimed to be rugged, insensitive to temperature and moisture, and to have a rela-

<table>
<thead>
<tr>
<th>Chemical used in growth process</th>
<th>How formed</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gallium (Ga)-Ga growth.</td>
<td>Passing HCl vapor over molten Ga.</td>
</tr>
<tr>
<td>Arsenic (As)-GaAs growth.</td>
<td>Cracking arsine gas (AsH₃).</td>
</tr>
<tr>
<td>Phosphorous (P)-Cracking gaseous GaP growth.</td>
<td>Cracking gaseous phosphine (PH₃).</td>
</tr>
<tr>
<td>Selenium (Se)-n-dopant.</td>
<td>Cracking gaseous selenide (H-Se).</td>
</tr>
<tr>
<td>Zinc (Zn)-p-dopant.</td>
<td>Cracking zinc vapor.</td>
</tr>
<tr>
<td>Hydrogen (H₂)-carrier.</td>
<td>( ultra-pure)</td>
</tr>
</tbody>
</table>
Get immediate delivery on Vector 2N3866 and 2N4012 RF power transistors

Vector's 2N3866 NPN silicon power transistor is available now in production quantities. This unit can also be ordered in a TO-18 alumina channel or stripline package.

Also, Vector's new 2N4012 transistor is in stock for immediate delivery in quantities up to 1,000 units. The 2N4012 features a JEDEC TO-60 package with isolated electrodes, and stud mounting for maximum thermal capability.

Vector offers a wide range of RF power transistors with guaranteed performances as shown in this chart.

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![Performance Chart]

For additional information, price quotations, or immediate delivery, call Vector Solid State Laboratories (215) 355-2700. TWX 510-667-1717.

**Data fits into lulls in telephone talk**

A new system stores data, compresses it and then transmits it at high speed over telephone lines during the pauses and silences in normal conversation.

Studies have shown that lines carry signal only 36% of the time in ordinary conversation. The remaining 64% is dead time. Silence between speakers accounts for 48% of total transmission time, and normal speech pauses comprise 16%, according to ITT, New York, creators of the system.

They call it Automatic Alternate Voice/Data. Its heart is a rotating magnetic drum. The telephone conversation is actually recorded and played back in transit between talker and listener, but this takes place so rapidly that an utterance is delayed only a small fraction of a second.

The device detects an utterance during this delay and switches the line from its data channel to its speech channel. As soon as the speaker pauses, the data transmission is resumed. Speech is always given priority over data.
3 ways you can use the Raysistor® to improve your product, cut costs

1. Use the Raysistor® as a simple remote or automatic volume control in SSB suppressed carrier receivers. Feeding part of the audio output into the control light source varies the resistance of the Raysistor’s photocell, making it usable in place of a normal volume control.

2. As a remotely controlled linear potentiometer. The Raysistor can be used as a remotely controlled linear potentiometer when used in the circuit shown above. Here the Raysistor forms a voltage divider between the positive and negative voltages.

3. As a photochopper stabilized D-C microvoltmeter. Raysistors, used as photochoppers in both modulator and demodulator circuits, enable d-c levels to be measured to a fraction of a microvolt. They facilitate synchronous detection and demodulation with simple electrical coupling, have less noise than transistor choppers, while avoiding maintenance problems of mechanical choppers. Other photochopper applications: photochopper relay, series or shunt chopper, modulator circuit, and as a stabilizer to reduce long-term drift.

Many more ways you can use the Raysistor. Send for The Raysistor Applications Manual which describes ways you can use this unique optoelectronic component as a photochopper, variable resistor, solid-state switch, relay, voltage or signal isolator, nonlinear potentiometer, etc. For complete specifications and prices, call your Raytheon distributor or regional sales office. For a copy of this 28-page manual, circle the reader service card or write Raytheon Company, Components Division, 141 Spring Street, Lexington, Mass. 02173.

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June 7, 1966
VOLTMETER WITH A MEMORY ...
...DC TO 20 MEGACYCLES

The Model 5201 memory voltmeter is a dc to 20 mc instrument which measures and stores indefinitely the maximum peak voltage applied, including continuous or one shot pulses as short as 50 nanoseconds. A memory reset-switch on the front panel allows the 5201 to monitor peak values of a varying waveform, either positive or negative going.

The solid-state 5201 is also available with a 4-digit in-line Nixie® tube readout. The voltage range may be extended to 30 kv with optional high voltage probes. For complete technical information, contact the Micro Instrument representative near you or write directly to us.

Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>VOLTAGE RANGE</td>
<td>0-3, 10, 30, 100, 300, 1000 volts. Can be operated up to 1000 volts above ground.</td>
</tr>
<tr>
<td>INPUT IMPEDANCE</td>
<td>100 k-10 megohms (depending on range).</td>
</tr>
<tr>
<td>PULSE WIDTH</td>
<td>DC to 50 (typically 30) nanoseconds.</td>
</tr>
<tr>
<td>OPERATING MODES</td>
<td>+, −, ± (DC or AC coupled).</td>
</tr>
<tr>
<td>READOUT</td>
<td>5” mirror-backed 1% meter.</td>
</tr>
<tr>
<td>PRICE</td>
<td>$695.00.</td>
</tr>
</tbody>
</table>

Microwaves used to study rocket plumes

Microwaves are being employed to measure the intensity of radio interference and noise generated by rocket-motor exhaust combustion by-products.

The work is being conducted at the new Microwave Interference Test Facility at Lockheed Propulsion Corp., Redlands, Calif.

Interference caused by combustion by-products may interrupt communications between a missile or spacecraft and ground control stations, according to a Lockheed spokesman.

Two lens-corrected horn antennas, built by DeMornay-Bonardi, Inc., Pasadena, Calif., focus the microwaves through the heart of the rocket exhaust plumes.

The horns are mounted on opposite sides of an equipment ring through which the exhaust is passed. One of the horns transmits the energy, concentrating it within a selected area, while the other horn acts as a receiver. The difference between the signal transmitted and that received is detected by an interferometer. The effect of the exhaust upon the microwave signal helps to determine the degree of ionization of the exhaust plume.

Other antennas, high-speed cameras and infrared detectors are mounted on a movable cart and propelled down the length of the plume.
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In addition to flexibility, Radiation matrices provide other important benefits: System reliability is increased by monolithic construction. Space requirements and weight are slashed through use of fewer packages. And, assembly of discrete diodes is eliminated.

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Radiation RM-30 Diode Matrix

<table>
<thead>
<tr>
<th>CHARACTERISTIC</th>
<th>TYP LIMITS</th>
<th>TEST CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Forward drop $V_{f}$</td>
<td>1.0 v</td>
<td>$T_{a} = +25^\circ C$</td>
</tr>
<tr>
<td>$V_{f}(1)$</td>
<td>0.7 v</td>
<td>$I_{f} = 20$ ma</td>
</tr>
<tr>
<td>Reverse breakdown $B V_{r}$</td>
<td>60 v</td>
<td>$I_{r} = 100$ µa</td>
</tr>
<tr>
<td>Reverse current $I_{r}$</td>
<td>7 na</td>
<td>$V_{r} = 25$ v</td>
</tr>
<tr>
<td>Reverse recovery $t_{r}$</td>
<td>7 nsec</td>
<td>$I_{r} = 10$ ma to</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{r} = 10$ ma Recover to 1 ma</td>
</tr>
<tr>
<td>Crosspoint capacitance $C_{cp}$</td>
<td>1.9 pf</td>
<td>$V_{r} = 5$ v, $f = 1$ Mc</td>
</tr>
<tr>
<td>Coupling coefficient $k_{c}$</td>
<td>20 na</td>
<td></td>
</tr>
</tbody>
</table>

*Supplied in 10-84 packages.
A BRUTE
THE SIZE OF A MIDGET!

READ HOW IT CAN SOLVE
YOUR SPACE PROBLEMS

CASE HISTORY

Bob has the job of designing power supplies for his company, an electronic equipment manufacturer. Requirements vary considerably from one piece of equipment to the next, with ratings all the way from 1 to 10 amps in both single and three phase configurations. He had a dilemma--what should the physical arrangement of his bridges be:

Both were bulky, and--worse yet--each was a custom design requiring long vendor lead times, not to mention the expense. One day, a Compar salesman suggested that if Bob used Unitrode UT 4000 series diodes, he could make his own bridges with only this comparative space required...

...TO GET 10 AMPS AT 500 VOLTS without additional heat sinking.

Space and lead time were both saved. In addition, Bob's production department could produce the bridges using individual diodes for far less than vendors were charging for special bridge assemblies ...and the work stayed in-house.

Also Bob could supplement his small laboratory stock by drawing on Compar's substantial inventory of Unitrode semi-conductors. No more weeks of waiting. Now he could instantaneously breadboard any bridge he needed.

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Electronic Design
Electronic system brings help

Maryland tests Hoffman road alarm

Hoffman Electronics is supplying equipment for an experiment that may lead to installation of a motorist-in-distress alarm system along all 41,000 miles of federal interstate highways within five years.

The Commerce Department's Bureau of Public Roads has contributed 90% of the $379,298 awarded by Maryland's State Roads Commission to set up the Hoffman Highway Emergency Call System and Safety Satellite alarm system.

A total of 324 of the coded-signal transmitters will be erected along the 43-mile section of the Washington, D.C., Capital Beltway, I-495, that lies in Maryland. The post-mounted, spherical, aluminum call boxes are powered by storage batteries recharged by solar cells. They each contain a transmitter that registers on a central emergency switchboard as soon as a distressed motorist touches a clearly labeled button. The signals are coded according to the type of aid needed, the location of the box and the side of the median on which it is located.

The Highway Emergency Call System contains buttons for police, ambulance, fire or maintenance service. About 100 will be installed. The boxes of the Safety Satellite system contain only police and fire buttons. They will form the bulk of the call boxes, which are to be set up and operational by September.

Hoffman also received a $96,000 contract to conduct a program of research into traffic patterns and the response time of the emergency equipment. The Baltimore Beltway, an interstate circumferential highway very similar to the Capital Beltway, will be used as a control. No automatic emergency devices will be erected on the Baltimore loop.

Industry will bid for federal contracts

A Department spokesman said the Maryland experiment may well be the forerunner of similar systems to be set up nationwide. He pointed out that, while Hoffman has an edge in this field, it will have to be opened up for competitive bidding from other firms. The Commerce Department or a new Department of Transportation will have to set standards and specify the system to be used.

The Chairman-Director of Maryland's State Roads Commission, John B. Funk, said that highway alarm systems will have to be a government responsibility. "Private enterprise, operating on uncertain schedules and without public control, could make a farce of a motorist distress call system," he said. He also believes that, "states rights" notwithstanding, this is a field that must be governed by the Federal Government. The Federal Bureau of Public Roads would act jointly with state roads units: the Bureau to set standards, the states to award contracts.

Hoffman outlines details of systems

Hoffman has been circumspect about technical side of its systems, but some detail was revealed at the time that the Maryland contract was announced.

The Satellite system boxes have no transmitters, but are wired to the nearest Highway Emergency Call System boxes. These generally use HCA-1 non-directional antennas; their silicon solar cells are HCS-1 models.

Under the contract, Hoffman will provide a service engineer on 24-hour duty for one year and will train state maintenance engineers.

Nike-X raises MOL issues

As everyone in Washington except the Administration pushes for establishment of a missile defense system, the question of the real status of the Air Force manned orbiting laboratory (MOL) has again come up. In the competition for the limited funds available for military missile and space programs, the Army—developers of the Nike-X missile defense system—has won out over the Air Force—rather uncommitted developers of a military MOL.

The White House and Defense Department still insist that they do not want the money that Congress is pressing on them for missile defense. But they will probably spend more on engineering development and long-lead production than was planned when the budget for fiscal 1967 was originally taking shape.

June 7, 1966
A modified Nike-X system to cope with an accidentally fired ICBM or a small-scale attack from an “Nth Country” is beginning to look attractive to Administration advisers who a year ago opposed deployment of any system. If for budgetary reasons a choice has to be made, the MOL is the clear loser, they say. There is far wider popular and political support for a system that is billed as protection for the citizens than there is for a surveillance satellite, the mission of which seems ill-defined to the public. Defense Department officials concede that this is all the more true when an MOL looks as though it duplicates the work of NASA and the unmanned Samos reconnaissance satellites.

Administration thinking is that a further advantage of an anti-missile system over the MOL is that development of the former would channel money into a much broader segment of the electronic industry than the MOL would.

Yet despite the current upsurge of interest in a Nike-type system, the MOL was obviously in difficulties, despite assurances to the contrary from the White House.

What of MOL’s future?

It would be facile to say that only the President or Secretary McNamara know what the future holds for MOL. But it would be misleading, for even they do not know. There is no question that MOL has already been greatly downgraded in size and mission. The next step depends upon the vagaries of war and the economy. But MOL appears to be heading for a merger with NASA post-Apollo projects and some other programs.

The Office of the Director of Defense, Research and Engineering, an influential Pentagon power center, is on record as favoring a “multipurpose satellite” system. It would combine communications, navigation, early ICBM warning, weather surveillance and nuclear test detection. Daniel J. Fink, deputy DDR&E director for strategic and space systems, recently said: “We have concluded that these functions are compatible and could be married into a single, newly proposed satellite, thereby increasing the cost-effectiveness of any one alone. Consequently, during the past year we have reoriented each of these programs toward such a common goal.”

Fink did not mention intelligence-reconnaissance in the list of functions that an all-purpose satellite would carry out, but it is understood to be included. Neither did he state whether the satellite would be manned or unmanned, nor whether it would substitute for MOL. Many observers believe, however, that it will initially be unmanned and use existing sensors. Later, they believe, any Defense Department manned space program may take the shape of a manned multipurpose satellite program.

New DOD agency in the air?

Yet another blow for the Air Force may come out of this sort of Defense Department thinking: “An Army company is pinned down and needs support from a missile battalion 85 miles to the rear. The C.O. radios his request via tactical communications satellite.”

Whose should such a satellite be? Air Force because it is in space? Or Army because it answers an Army need? Is it a strategic system because it was originally launched half a world away by a large booster? Or is it tactical because it supports front-line elements? DOD is beginning to say it is both and belongs to neither. The Department has begun to take a very preliminary look at the possibility of a new DOD agency to serve all the services, rather as the Defense Communications Agency, Defense Intelligence Agency and Defense Supply Agency do. It would control intelligence-weather-communications-navigation satellites, launch them and design them. The nucleus of such an agency would likely draw on major elements of the Air Force as well as some elements of the Army and Navy technical organizations.

DOD publicly denies that this is in the offing, but officials privately admit that it is in the thinking stage. A high DOD official commented: “Some years ago I said categorically, but in good faith on my part, that the Army would never lose the von Braun team.”

Airlines communicate by satellite

An airplane can now communicate to distant ground stations even while flying over open ocean. The transmission is received and rebroadcast by the Syncom satellite.

The system was recently displayed by its manufacturer, the Bendix Corp., at a Communications Satellite Seminar held at the U.S. Department of State.

The simple, light equipment carried aboard the plane is powerful enough to reach ground stations directly if it is in direct line of sight with them.
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this silicon
transistor

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*DuPont's trademark for polyester film.
E D EDITORIAL

What's happening in computer-aided design?

The digital computer promises to become one of the most powerful tools available to the electronic design engineer. Like any other tool, however, its functions, capabilities and limitations must be understood before the prospective user can employ it effectively and intelligently.

Only a small proportion of the design community has made extensive use of this design aid to date, and as a consequence there is a paucity of general information about it. The designer looking for material written about it will not have an easy time.

Why this lack of information? There are several reasons. For one, computer-aided circuit design—unlike circuit analysis—is still in its infancy. For another, many organizations engaged in research and experimentation are unwilling to publicize their work in this field. And perhaps most important, there has been no common meeting ground for engineers using the computer as a design tool.

Time alone should take care of the first of these as the use of computers for designing becomes more widespread. To solve the second problem, we can only urge those organizations involved to bear in mind the crying need for some hard facts to be published on the successes—and failures—of computer-aided design.

Happily, the first steps have already been taken toward eliminating the third reason for lack of data. An initial exchange of ideas and experience took place at the Conference on Computer-Aided Solid-State Circuit Design held at the University of Wisconsin in early May. Dr. William W. Happ, Chief of the Design Criteria Branch at NASA's Electronic Research Center, Cambridge, Mass., stressed to the delegates the need to discuss common problems and compare ideas. Happ believes that this engineer-to-engineer communication is one of the most useful means of spreading knowledge of semiautomated design techniques. And to help foster and supplement person-to-person exchanges, NASA is conducting a survey of available computer programs used for electronic design. The results of this survey can't come too soon.

We believe it is time for engineering schools and institutions to follow the lead given by the University of Wisconsin and NASA. The continuing professional-studies courses of engineering schools would fill a great need by adding computer-aided circuit design to their curriculums. Industry, for its part, should boost in-house training in this field.

The rapid pace of modern technology demands more effectual and accessible dissemination of information than has hitherto been the case.

Joseph J. Casazza
This new computer quality transistorized digital readout costs you 30% less!

For integrated or discrete component circuits

Identical electrical characteristics, identical driver-decoder functions, identical 1-inch mounting centers—yet these new digital readouts using NIXIE® tubes cost you 30% less than fully enclosed (with metal case transistors) TEC-LITE TNR-10 and TNR-30 Series models. New simplified single board design and use of plastic encased silicon transistors substantially reduce assembly time, allowing this far lower price.

TNR-40 Series—Provides decimal readout from decimal input signals of low level. Internal circuits of four standard models are controlled by input signals as small as 3.5 volts. Special versions can be controlled with signals as small as 2 volts or less. High voltage to fire the neon tube's numeral elements is confined to the unit and to the panel area.

Price (0-9 display) in 100-299 quantities: $18.85 less tube.
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TNR-50 Series—Decimal readout is available in 8 standard models to handle 8-wire and 4-wire binary coded decimal input as small as 3.5 volts. A variety of other input codes and signal levels can be accommodated on special order. All-transistor circuitry eliminates diode decoders to reduce the number of components and increase reliability.

Price (0-9 display) in 100-299 quantities: $24.90 less tube.
For fully enclosed readout, request data on TEC-LITE TNR-30 Series.

Indicating Devices
Transistor Electronics Corporation
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SIGNETICS USER-ORIENTED PACKAGING

All integrated circuit users face the problem of reliably testing, sorting, and handling these products during assembly without incurring damage to the leads of the device. Endeavoring to ease this problem, Signetics provides device carriers which are designed with the user in mind. Ease of test, storage, and minimum carrier cost to provide the "throw away" feature are all part of this program.

The outcome of this program is a group of throw-away plastic carriers designed for each type of circuit package. The flat package carrier, Figure 1, will accommodate either the ¼ by ¼-inch 10-lead flat package (TO-91), or the ¼ by ⅜-inch 14-lead flat package (TO-88). The package body opening in this carrier is under-cut so that the package snaps in and is retained even if the carrier is accidentally turned over. The flat, ribbon-like leads are fully protected within the grooves of the carrier body which also serve to guide the wipers of a multi-point test head into contact with them.

A similar approach was taken in designing the carrier for the familiar modified JEDEC TO-5 can. In this case the carrier is a grooved plastic cylinder. Carrier and circuit plug into a Signetics-designed test socket as shown in Figure 2.

The carrier designed for Signetics' new "A" package, a solid plastic 14-lead dual in-line type, serves, as do the others, to protect the leads and guide the test head. It has one additional outstanding feature. This new carrier also serves as a throw-away alignment jig for insertion of the circuit into a PC board. As indicated in Figure 3 a slight downward pressure on the package body is used to extend the leads slightly beyond the bottom of the carrier. The leads can then be easily registered in the hole pattern in the PC board. A final downward push seats the circuit in the board, Figure 4. The carrier is then discarded.

This carrier is equally useful whether hand or machine insertion is involved. It should be noted that for machine insertion, the normal shipping box for Signetics dual in-line packages serves as a throw-away magazine for loading automatic machinery.

Shipping containers for each type of circuit package are shown in Figure 5.

Signetics Data Circle 250
NEW SAGE I ELECTRONIC CALCULATOR
USES SIGNETICS INTEGRATED CIRCUITS

A new, low-priced electronic desk-top calculator designed for general, purpose business and engineering use has recently been introduced by Dero Research and Development Corporation of Huntington, New York. Called Sage I, the 12-pound portable-typewriter-sized unit performs computations in fractions of a second, in complete electronic silence. Featuring a simplified 10-key keyboard, the machine has a 20-digit capacity and displays results in \( \frac{7}{8} \)-inch high numerals on a brightly illuminated screen.

The self-contained Memory of Sage I permits storage and recall of entries and results, facilitating continuous calculations and the accumulation of products and quotients. A number need only be entered once for raising to powers or for repetitive use. The device has four registers—entry, answer, memory, and accumulate—and its typical speed is 0.008 seconds for addition and subtraction, and 0.25 seconds for multiplication and division.

Sage I operates in the same sequence in which a normal problem is expressed and thus no special instructions are required to learn how to operate the machine. The unit "remembers" both the last number and command entered, permitting an automatic repeat of either or both. Sub-totals are automatically indicated and any result may be further operated upon without re-entry. Automatic reciprocal division is also possible.

A special "double precision" feature of Sage I permits calculation capacity to 20 digits on its 10-digit display, with the 10 most significant digits presented first, and the remaining least significant digits presented when the "D" key is pressed. A second press of the "D" key returns the most significant digits to the display.

SIGNETICS
8-BIT MEMORY ELEMENT IN
SDS SIGMA 7 COMPUTERS

The remarkably high Input/Output rate of Scientific Data Systems' new Sigma 7 family of computers (up to 160 million bits per second) can be attributed in large measure to the liberal use of very high speed scratch pad memories assembled from Signetics 8-bit memory elements. These new monolithic I/C's incorporate 8 flip-flops, decoding and write networks, and an output buffer all on a single chip.

The principle function of the scratch pad memories is to speed up system response to multiple inputs by reducing dependence on the much larger and slower main memory. The basic scratch pad building block is a PC board carrying 16 of the 8-bit memory elements, and drive circuitry to form a 16 byte module of 8 bits per byte.

Sigma 7 features a total capability for both business and scientific data processing and is uniquely designed for real time computation while operating in time sharing, multiprocessing and multiprogramming environments. Because of its extensive use of monolithic integrated circuits, Sigma 7 is considered a third generation computer.

A 12-page reprint of an article describing the SDS Sigma 7 and the function of the Signetics 8-bit memory element in it is available on request.
VITRO LABS TIMING AND CONTROL SYSTEM
FEATURES SIGNETICS DTL IC'S

High reliability, compactness and low cost are the major advantages of a new Event Control Sequencer manufactured by Vitro Laboratories of West Orange, New Jersey. Designated model 3219, this device was designed by Vitro to fill the need for more reliable data timing and control systems. It provides means for separate on-off control of three independent events to a resolution of 0.1 sec., operating on a time base of one hour. A seven-segment incandescent display indicates elapsed time in minutes, seconds and tenths of seconds. Outputs consist of three independent relay closures. These have mercury-wetted contacts rated at 28 volts, two amperes DC.

The 5½" x 19" x 17" 30-pound unit, designed for rack mounting, is assembled using Signetics DTL integrated circuits in the new dual in-line plastic package on standard Vitro microcircuit board assemblies. All components are mounted on one side of 3X5-inch epoxy-glass plug-in printed-wiring boards for standardization and ease of manufacture and maintainability.

INTEGRATED CIRCUITS
REDUCE SIZE OF MINUTEMAN PORTABLE TEST SET

A portable automatic digital test set using Signetics integrated circuits has been designed by Sylvania Electronic Systems Division for part of the Minuteman ground electronic system. By using IC's, a volume reduction of 30 times and a weight reduction of 6 times has been achieved. If the test set were designed for conventional solid state printed circuit construction it would weigh 90 pounds and occupy 5 cubic feet of space. At 14 pounds, this set is light enough to be plugged directly into a rack connector, eliminating the need for cables.

Designed to limit operator decision-making (and thus minimize personnel training requirements) the test set is an automatic go/no-go readout unit capable of isolating a malfunction in a communications subsystem to one of seven drawers by simple indicator lamps. The unit automatically tests itself and then proceeds to program a series of tests on the subsystem. It is capable of performing the self test plus analysis of 42 test points required to check the subsystem in 20 seconds. Two techniques are used for subsystem analysis. One is direct comparison of digital type signals from the subsystem with similar signals generated by the test set. The second technique is the checking of analog signals with a threshold level detector which indicates a no-go condition if signal voltages fall below a minimum preset level.

A block diagram of the test set is shown in the accompanying figure. Test start and stop signals, gating functions and synchronization are provided by the "Test Set Control," clock pulses and pulse rates are developed in the "Driver Generator," and the "Operational Programmer" provides duplicating and controlling of the subsystem operations. The "Self Test" function checks for proper basic timing rates in the test set and assures that all analysis and readout circuits are capable of a no-go indication.

Signetics Data Circle 255
Vitro Data Circle 256

INTEGRATED CIRCUITS
SIGNETICS
SIGNETICS INTEGRATED CIRCUITS

Signetics SE100-Series DTL circuits in "G"-type package are used extensively in this test set, and are shown on a typical circuit board in the accompanying photograph.

Signetics Data Circle 257
Sylvania Data Circle 258
DUAL J-K FLIP-FLOP ADDED TO SE800-SERIES TTL FAMILY

Signetics has added a 35 Mc dual J-K flip-flop to its TTL family. Introduced at the IEEE convention in March, the new monolithic element, designated SE826 provides opportunities for greatly reducing component counts in TTL systems. Each of the two flip-flops has independent preset, J, K, Q, and clock inputs. Average power dissipation is 40 mW per flip-flop, and 1.0-Volt noise margins are typical.

The 800 series also includes six different NAND gate configurations, single, dual, triple and quad; a gate expander, an Exclusive-OR, and a 30 Mc single J-K Binary Element that offers unusually great input logic flexibility. The series is offered in Signetics glass-Kovar 14-lead TO-88 flat package, and is available in two temperature ranges: the SE800 series for −55°C to +125°C application, and the SE880 series for a range of −55°C to +70°C.

SIGNETICS LINEAR FUNCTION SET

As shown in the accompanying graph, Signetics SE500-Series Linear Function Set provides all of the most frequently required circuit functions over a broad spectrum of gain and bandwidth combinations. The most recent additions to Signetics linear family are the SE506 Differential Operational Amplifier featuring an open loop gain of 17,000 and bandwidth of 170 kHz and the SE518 Analog Comparator which provides 5 MHz bandwidth and an open loop gain of 1700. All SE500 series elements are available in two temperature ranges (−55°C to +125°C and 0°C to 70°C).

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For further information on the Signetics products discussed in this insert, please circle the appropriate reader service number. For information on the products which use Signetics Integrated Circuits, please circle the reader service number indicated.

SIGNETICS CORPORATION, 811 East Arques Avenue, Sunnyvale, California

TEL.: (408) 739-7700 TWX: (910) 339-9220

Subsidiary of Corning Glass Works
Ferrite phase-shifters are best for high power PAGE 34
Select the right FET for low-noise and hf applications PAGE 40
Use thin-films for a D/A converter PAGE 48
**Use ferrite phase-shifters** for high-power phased arrays. The latching types operate without holding current and are efficient even at 1 GHz.

For powerful, long-range phased-array radars, ferrite phase-shifters are a must. The efficiency of ferrite phase-shifters is greatly improved with latching, or digital, design. This approach does not use holding currents and makes possible efficient operation at frequencies above 1 GHz.

The power levels of ferrite phase-shifters can be as high as 1000-W average and 100-kW peak. Diode phase-shifters do not even approach these power levels yet (ED13, May 24, 1966, p. 46). Another point favoring ferrites at higher frequencies is the fact that the insertion loss of diodes increases with frequency. However, there is no firm demarcation line between diode and ferrite types—careful consideration must be given to each application.

There are two ways to obtain phase shifts from magnetized ferrites in the geometry being considered:

- Keep the direction of the magnetic field fixed and change the direction of the propagating RF.
- Change the direction of the magnetic field and keep the RF field in the same direction.

In each case, the phase shift is nonreciprocal, or differential.

Nonreciprocity limits the speed of operation. Switching time between transmit and receive states is limited by the time needed to change the direction of magnetization. Recognizing this limitation, many companies are working on reciprocal ferrite phase-shifters. Some success has been reported, but insertion loss of reciprocal types is still about double that of the nonreciprocal.

Only nonreciprocal types will be discussed, since reciprocal types have not achieved practical performance yet.

The external magnetic fields may be eliminated by forming a ferrite toroid. The toroidal geometry\(^1\) reduces the demagnetizing fields of the slabs in the direction of the magnetization and preserves the desired direction of magnetization for a phase shifter (Fig. 1).

The operation of a ferrite phase-shifter is based on the ability of the ferrite or garnet to "remember" its past magnetization. The memory, defined as \(B_{\text{rem}}/B_{\text{max}}\) (the remanent magnetic moment divided by the maximum moment), is typically 0.80 to 0.50 for a toroid.

A portion of the magnetized toroid sees a circularly or elliptically polarized magnetic field resulting from the incident RF signal. The field is either positive or negative for one direction of magnetization of the toroid. Switching the direction of this magnetization reverses the sense of the polarized magnetic field and thus produces a differential phase shift. The switching field is generated by a current pulse in the wire that threads each core.

The two remanent states of magnetization, represented by the top and bottom of the loop, are the normal operating positions. The difference in the RF permeability of the two states determines the amount of differential phase shift.

To obtain digital phase shift, the toroids must be composed of good microwave materials, with square or nearly square hysteresis loops.

**Design with proper core-lengths**

By cascading appropriate lengths of toroids, each individually switched by current pulses, one can assemble a phase-shifter that has as many discrete values as required. For example, assume that a phase-shifter is needed to cover 360° in 11.25° steps. One toroidal core, or bit, is required for each phase shift of 180, 90, 45, 22.5 and 11.25°. Each successive core is one-half the length of the previous one, since the length is proportional to the phase shift:

\[
\Delta \phi = K \frac{4 \pi M}{R, L},
\]  

where the symbols represent the following:

1. A single-slab ferrite, when magnetized, shifts the phase of an incoming RF field if either the direction of the magnetic field or the RF field is changed (a). Toroidal geometry does not need external magnetic field (b) to provide phase shift.
Since the operating point in the digital phase shifter is one of the two stable remanent points of the hysteresis loop, the material is never completely saturated (single domain); therefore, the toroidal material is composed of a number of domains separated by domain walls. (Experimental results indicate that circular domains predominate.) In an unmagnetized, domain-filled ferrimagnetic material, so-called low-field magnetic losses have been predicted, and it has been experimentally verified that they occur up to a maximum frequency given by:

\[ \omega_{\text{max}} = \gamma (H_{\text{anis}} + 4\pi M_s) \]

where:

\[ \gamma = \text{gyromagnetic ratio in MHz/Oe, and} \]

\[ H_{\text{anis}} = \text{anisotropy field in Oe.} \]

The gyromagnetic ratio is nearly constant and equal to about 2.8 MHz/Oe. The anisotropy field is due to the fact that it is easier to magnetize the crystal along certain directions than others. Along these so-called easy axes, the magnetization requires the smallest amount of magnetic field. The hard axes are those directions that require the largest fields for magnetization. The anisotropy field is essentially the difference in the fields in the easy and in the hard directions.

Magnetic losses in ferrimagnetic materials are generally confined to a narrow band of frequencies which is related to the linewidth of the material.

Phase shifters operate off-resonance and in the case of most remanent phasers, above the resonant frequency. Sharpening linewidth cuts losses. To overcome linewidth broadening resulting from such factors as composition and porosity of the sample and the anisotropy field, operating frequency should be well above the value given by Eq. 2.

Magnetization is usually chosen so that the ratio makes the value of the remanent flux somewhat independent of the exact value of the peak current. With this design, no holding current is needed for either state.
\( \gamma 4 \pi M_s/\omega \) is between 1/3 and 3/4 where \( \omega \) is the operating frequency. The phase shifter’s high-power limits must also be considered.

**Spin waves limit power**

As power increases, spin waves are beginning to oscillate at both the fundamental operating frequency and at one-half of this frequency. Instabilities appear in the precessing motion of the electrons. The result is an exponential coupling of energy to the spin waves and a concurrent linewidth-broadening and decrease in output power. Since the half-frequency waves have greater effects, the material should be selected to eliminate their influence. The power-handling capability can be increased and stabilized by selecting the ratio of \( \gamma 4 \pi M_s/\omega \) to be less than about 0.4 by doping the garnet material with rare-earth substitutions. The latter step increases the resonant linewidth and the spin-wave linewidth. This also increases insertion loss of the device. Therefore, peak power can best be controlled by adjusting \( 4 \pi M_s \).

A high remanence ratio is desired in the material, because it reduces the magnetic losses and allows the use of a larger saturation magnetization.

The squareness of the hysteresis loop, \( S_n \), is the ratio of \( 4 \pi M_{sR} \) and \( 4 \pi M_s \) (Fig. 3). \( 4 \pi M_{sR} \) is the remanent magnetization at the drive field, and \( 4 \pi M_s \) is the magnetization at the drive field, as shown in Fig. 3.

Typical \( S_n \) values for better materials are in the 0.85 to 0.9 region, and typical values for \( R \), range from 0.5 to 0.8.

The switching time and the needed switching energy also depends on the drive field and on the ferrimagnetic material. Switching, or reversal, time, in \( \mu s \), is given by:

\[
\begin{align*}
\Delta (4\pi M_0) &= B(4\pi M) \\
4\pi M_{RD} &= B_D \\
4\pi M_D &= B_{RD} \\
4\pi M_0 &= B_C \\
H_{C} &= H (4\pi M_0) \\
A(4\pi M_0) &= H (4\pi M_0)
\end{align*}
\]

3. Hysteresis loop of typical ferrimagnetic material shows the magnetization, \( 4\pi M_R \), and the remanent magnetization, \( 4\pi M_{RD} \), at the drive field.

\[
t \approx \frac{6.4d \lambda (10^{-2})}{\delta g^2 M_s (H_{m} - H_o)} .
\]

where the symbols represent the following:

- \( d \) = mean distance from one domain wall to another, in cm,
- \( \lambda \) = damping parameter of material, in sec\(^{-1}\),
- \( \delta \) = domain-wall thickness in cm,
- \( g \) = g-factor of material \([g = \gamma/(e/2mc)]\), and
- \( H_m, H_o \) = drive and threshold fields in Oe.

The threshold field is approximately equal to the coercive field of the material.

The inverse switching time is a linear function of the applied switching field.

\[
S_m = t(H_{m} - H_o) ,
\]

where \( S_m \) is the switching coefficient, a constant at a given temperature; and \( H_{m} - H_o \) is the actual drive field in oersteds. Typical values of \( S_m \) range from 0.1 to 1.0 Oe-\( \mu s \).

To show how switching power depends on the magnetic properties of the material, assume that the magnetic material has a square hysteresis loop, is toroidal in shape, and is threaded with a single switching coil of \( N \) turns. (In many applications, the coil has only a single turn.) During the switching of such a core, its impedance is almost entirely resistive. The needed switching current \( I \) is:

\[
I = \left( \frac{S_m}{t} + H_o \right) \frac{5D}{N} ,
\]

where \( D \) is the average diameter of the core in centimeters. Typical switching currents range from 5 to 15 A. Using this expression for \( I \), the average resistance of the core can be computed. With all other characteristics the same, the fastest
switching material will be the one having the highest resistance.

The average power $P$ dissipated by the core is given by:

$$P = \frac{DA\Delta B}{t} r(S_0 + H_{st}) 10^{-1}, \quad (6)$$

where

- $A$ is the cross-sectional area of the core in square cm,
- $B$ is the change in flux density (similar to $\Delta M_s$), and
- $r$ is the switching rate of the material in Hz.

Equation 6 covers one complete trip around the hysteresis loop, which involves two switching actions. For a given core size and switching time, the driving power can be reduced only by choosing a material with a low $S_0$ and small flux density. The energy dissipated in the core can become very high for large cores that require fast switching time or high repetition rates, and the resulting heat can change the properties of the material. The typical core loss should be less than one watt.

**Too many materials—be careful!**

For square hysteresis loops and low coercive fields, the particle size should be large. Large remanent magnetization usually requires thin-walled toroids.

A wide range of saturation magnetization values is available in good square-loop materials. Following are some general guidelines for selection of the proper materials.

Aluminum substitution in yttrium iron garnets (YIG) produces a family of compositions having magnetization values from 1780 gauss (for YIG) down to 200 gauss (for 25% aluminum substituted YIG). Gadolinium substitutions produce compositions having stable magnetization as a function of temperature. For increased peak power dysprosium substitution is helpful in garnet materials, however lowering $4\pi M_s$ with aluminum is the preferred way.

Nickel zinc ferrites may have saturation magnetizations as high as 5000 gauss.

Lithium ferrites that exhibit square hysteresis properties have been compounded. These materials are particularly inviting because they have high Curie temperatures—in the neighborhood of 680°C. (Beyond the Curie temperature, ferrite materials lose their magnetic properties). Although their dielectric loss tangents are extremely high, they can be used for temperature compensation in composite toroids. Here, the active portion of the core is composed of a good microwave material, while the legs completing the toroidal geometry (outside the RF fields) are lithium ferrite. Nickel ferrites, too, have remanence ratios markedly lower than those of the garnets.

In applications where mechanical loads are expected, the selected material should have zero, or near zero, magnetostrictive constants. Otherwise mechanical loads can alter the hysteretic properties of the material and affect the reproducibility of the digital phase-shifter. In these areas some ferrite materials, in particular the magnesium manganese ferrites, are usually preferred over garnet types.

**Efficiency depends on switching current**

The toroid can be switched in a short time by applying a high voltage, or in a longer time with lower voltage. The energy delivered to the toroid is essentially the same, but the difference in driver efficiency is proportional to the square of the current ratio. Typical energy, time, and rate requirements are best satisfied with a transistor driver (Fig. 4). The toroid energy requirements are approximately proportional to the wavelength squared. The magnitude of the current is related to the variable permeability of the toroid during switching. A large portion of the energy is consumed during the last half of the switching cycle (mostly in the driver), but it is this part that contributes least to the total amount of differential phase shift.

Although most phasers will handle 10-kW peak power and work with average power levels from near zero to 100 W, some requirements will be as high as 100-kW peak and 1000-W average. In spite of these levels, each phaser must be insensitive to burn-out. Operation at higher power levels increases the inherent insertion loss (Fig. 5). The compensated curve in Fig. 5 represents the loss from a 60% gadolinium material that provides maximum temperature stability. The designer must choose between lowest possible loss accompanied by phase-shift variations as a function of temperature, or higher loss with relatively constant phase shift as a function of temperature. The usual compromises lead to a material with a moderate amount of temperature-compensation characteristics and a loss of 1 dB or less.

The presence of several objects—ferrimagnetic cores, dielectrics, and charging wires—in the waveguide can cause E-field arc-over if proper precautions are not observed. Arc-over is most likely to occur at the interface between the toroid and the waveguide walls and around the charging wires.

**How to build a phase-shifter**

The essential features of a ferrite phase-shifter
Spectrum for MAG 12 during first 5 secs. of warm-up test at -65°C.

A clean spectrum within 5 secs. at a temperature of -65°C.

Just one of the characteristics of the outstanding range of GENALEX X-Band Rugged Magnetrons. The MAG 12 and MAG 15 are designed to operate through a wide range of adverse acceleration, vibration, pressure and temperature conditions and are, therefore, ideal devices for all military environments.

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GENALEX X-BAND RUGGED MAGNETRONS PERFORM ANYWHERE

Use a square waveguide

The most efficient waveguide size, when loaded with toroids, is approximately square, measuring about one-third wavelength on each side. This small size makes it possible to contain the entire phaser, including the driver, in a square whose edge dimension is one half the free-space wavelength of the operating frequency. Thus the phaser can be mounted in the array in line with the radiating elements—all about a half-wave-length apart. Present drivers are too large to implement this optimum size above 6 GHz, but only slight miniaturization of the drivers and microwave housings is required to reach the higher-frequency range. It is expected that it will be possible to fabricate a microminiaturized driver from integrated circuits to allow half-wavelength stacking in the X-band or beyond.

References:

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Gain insight into FET amplifiers. Learn which parameters govern performance and how to handle the design of low-noise and high-frequency circuits.

Part 2 of a three-part article.

Filling an amplifier need with a field-effect transistor (FET) no longer need be difficult or time-consuming. In all FET-amplifier applications just a few parameters are critical. Consult these first and you quickly determine both the FET's suitability for the job and the major portion of the circuitry required.

Four major FET-application categories* remain in this theme of segregating governing parameters to facilitate selection. They are:

- Low-noise amplifiers (where signal levels are a few millivolts or less).
- High-frequency amplifiers (video and RF types).
- Differential amplifiers (conventional and low-drift types).
- Low-drift, single-ended amplifiers (including zero temperature coefficient operation).

We will now consider the first two application classes. Part 3 will cover the final two circuits, which are generally lower frequency applications.

The order of treatment for each application class will be first to consider the important characteristics peculiar to it; then to call out and use the key design parameters; finally to give the step-by-step design procedure for a typical circuit.

FET source for low-noise amplification

A low-noise amplifier is but a variation of the general-purpose amplifier. Dc and ac design procedures are the same, except that amplifier noise becomes a paramount consideration. Signal levels are typically in the range below a few millivolts. A FET amplifier (shown at right) will be used to consider the audio- and sub-audio applications.

The order of treatment for each application class will be first to consider the important characteristics peculiar to it; then to call out and use the key design parameters; finally to give the step-by-step design procedure for a typical circuit.

The FET may be described by the noise-equivalent circuit of Fig. 1a; it is characterized by an equivalent open-circuit noise voltage and an equivalent open-circuit noise current referred to the input. Both \( e_n \) and \( i_n \) are functions of frequency as shown in Fig. 1b. The specified \( r_n \) includes the effect of gate-source capacitance in the equivalent circuit. Thus, when making noise calculations due to \( e_n, C_{iss} \) is not included in the path of \( i_n \) (See Table 1).

Noise figure \((NF)\) measurements include the effects of both \( e_n \) and \( i_n \), although the effect of \( i_n \) is usually insignificant when the generator resistance is below several megohms. \( NF \) measurements must always include a generator resistance \((R_{gen})\) specification, as well as frequency and bandwidth. Either \( e_n \) or \( i_n \) need include only frequency and bandwidth specifications. \( NF \) should always be specified with an \( R_{gen} \) low enough to give an \( NF > 1 \) dB; otherwise, the \( NF \) measurement is not of sufficient accuracy to be meaningful.

The fact that the FET exhibits a minimum \( NF \) with a specific \( R_{gen}(R_{opt}) \) at a given frequency does not mean circuit output noise is a minimum under these conditions. It simply means that for the specified \( R_{opt} \) the FET contributes a minimum percentage of the total noise; the remainder is generated by the signal source and its internal re-

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James S. Sherwin, Senior Applications Engineer, Siliconix Inc., Sunnyvale, Calif.
sistance ($R_{opt}$). In fact, it is axiomatic that output noise increases with generator resistance.

The important FET parameters for low-noise applications are $NF$, $e_n$, $i_n$, $C_{iss}$, $g_{fs}$, and $C_{rss}$. Any two of the three noise characteristics will describe FET noise performance. However, it is best that $e_n$ be specified in the excess noise region for applications below several hundred Hertz. An additional specification at or above 1 KHz is important for audio-frequency applications. For the high-frequency region, we take note of the curves of Fig. 1b. At high frequencies, $e_n$ becomes independent of frequency. In the excess noise region below a few hundred Hertz, the noise power increases as $1/f$, hence the name $1/f$ noise. It is to be noted, although not the rule, that the noise power of FETs manufactured by some processes increases as $1/f^2$ over a significant portion of the spectrum. A single-frequency noise specification does not, therefore, necessarily provide adequate noise-performance information. It is only from curves of typical performance, such as those shown in Fig. 1b, that the FET devices can be adequately described.

In the $1/f$ region, $e_n$ increases as $1/\sqrt{f}$, and the equivalent noise resistance increases as $1/f$. FET noise voltage at any given frequency may also be expressed as an equivalent noise resistance, $R_n$. This is because the noise produced by a FET may be equated to the thermal noise produced in a resistor. Thermal noise is plotted as a function of resistance in Fig. 2a and is expressed by the relationship

$$e_n^2 = kTRB \text{ or } e_n = \sqrt{kTRB},$$

where $k$ is Boltzman's constant, $T$ is the temperature in degrees Kelvin (usually 298°C or 25°C).

**Table 1. FET Noise Parameters**

<table>
<thead>
<tr>
<th>Test conditions (must be specified)</th>
<th>Meaning of specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>$e_n$</td>
<td>Common-source equivalent short-circuit input noise voltage. Measured at the output with the input shorted, and referred to the input. Expressed as rms volts per root cycle, $\mu V/\sqrt{Hz}$. A function of frequency, so frequency value must be stated.</td>
</tr>
<tr>
<td>$i_n$</td>
<td>Common-source equivalent open-circuit input noise current. Expressed as pA/$\sqrt{Hz}$, a function of frequency.</td>
</tr>
<tr>
<td>$NF$</td>
<td>Noise figure. This represents a ratio between input signal to noise and output signal to noise. $NF$ is a function both of frequency and of generator resistance $R_s$. Both must be stated or the specification is meaningless. When properly qualified, $NF$ includes the effects of both $e_n$ and $i_n$.</td>
</tr>
</tbody>
</table>

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1. When FETs are used for low-noise amplifier applications, the noise equivalent circuit (a) must be consulted. The equivalent short-circuit noise voltage ($e_n$) and equivalent open-circuit noise current ($i_n$), as referred to the input, are functions of frequency (b).

2. Thermal noise in FETs can be plotted as a function of resistance (a) according to the relationship $e_n^2 = kTRB$. To obtain a specified noise-response characteristic, filters are used (b), particularly in measurements of sound-reproduction equipment. Curve A is an ASA recommendation; Curve B is a standard used by the Radio Manufacturers' Association; curve C is a German standard (DIN #45-405). All three display filter characteristics for noise measurements.
### Table 2. Small Signal Characteristics of FETS

<table>
<thead>
<tr>
<th>Test Conditions (must be specified)</th>
<th>Meaning of specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>$r_{ds}(0)$</td>
<td>Drain-to-source resistance when biased to full ON condition (max operating $I_d$).</td>
</tr>
<tr>
<td>$y_{rs}$</td>
<td>Magnitude of common-source forward transfer admittance. Sometimes the magnitude signs are omitted. Measured at $V_{ds} = 0$, unless otherwise specified.</td>
</tr>
<tr>
<td>$g_{rs}$</td>
<td>Magnitude of common-source forward transfer conductance. Sometimes the magnitude signs are omitted. This is perhaps a more informative term than $y_{rs}$. At 1kHz, $y_{rs} = g_{rs}$. However, at high frequencies $y_{rs}$ includes the effect of gate-drain capacity, hence may be misleadingly high. The term $g_{rs}$ should be used for all high-frequency measurements.</td>
</tr>
<tr>
<td>$g_{rs}$</td>
<td>Same as $g_{rs}$ but a particular set of operating conditions is implied. Match in $g_{rs}$ for differential pairs. Expressed as a fraction.</td>
</tr>
<tr>
<td>$g_{rs}$</td>
<td>Match in $g_{rs}$ for differential pairs.</td>
</tr>
<tr>
<td>$g_{rs}$</td>
<td>Same as $g_{rs}$ but often used to denote $g_{rs}$ when biased for zero temperature coefficient operation.</td>
</tr>
<tr>
<td>$g_m$</td>
<td>Mutual conductance. Sometimes used in lieu of $g_{ms}$.</td>
</tr>
<tr>
<td>$g_{m}$</td>
<td>Same as $g_m$, but specifically at $V_{ds} = 0$.</td>
</tr>
<tr>
<td>$y_{in}$</td>
<td>Common-source input admittance with output shorted. Important for high-frequency operation.</td>
</tr>
<tr>
<td>$g_{in}$</td>
<td>Common-source input conductance with output shorted. This must be specified for high-frequency applications as $g_{in} &lt; 1/\omega$.</td>
</tr>
<tr>
<td>$y_{in}$</td>
<td>Real part of $y_{in}$. Identically equal to $g_{in}$. Sometimes used instead of $g_{in}$.</td>
</tr>
</tbody>
</table>

**Diagram:**

```plaintext
3. The FET can be put to advantage in capacitor microphone preamplifier applications (a). Referring to the equivalent circuit (b), $C_{in}$ (across mike) must be minimized to provide high S/N ratios. Capacitance at $R_s$ is $C_s$.
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<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions (must be specified)</th>
<th>Meaning of specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>$g_{in}$</td>
<td>Same as $g_{in}$.</td>
<td></td>
</tr>
<tr>
<td>$y_{in}$</td>
<td>Common-source output admittance with input shorted.</td>
<td></td>
</tr>
<tr>
<td>$g_{in}$</td>
<td>Common-source output conductance with input shorted.</td>
<td></td>
</tr>
<tr>
<td>$y_{in}$</td>
<td>Real part of $y_{in}$. Identically equal to $g_{in}$. Sometimes used instead of $g_{in}$.</td>
<td></td>
</tr>
<tr>
<td>$g_{in}$</td>
<td>Same as $g_{in}$.</td>
<td></td>
</tr>
<tr>
<td>$y_{in}$</td>
<td>Imaginary part of $y_{in}$. Output susceptance $b_{in}$, identically equal to $1/\omega C_{in}$. Sometimes used in lieu of $C_{in}$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Common-source input capacitance, output shorted. $C_{in} = C_{gd}+C_{gs}$. (Fig. 6).</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Same as $C_{in}$ if $V_{ds} = 0$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Same as $C_{in}$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Reverse transfer capacitance, input shorted.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Same as $C_{in}$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Same as $C_{in}$, actual value of drain-gate capacitance.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Values in equivalent circuit not measurable directly.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Actual value of gate-capacitance.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Actual value of drain-source capacitance, essentially header capacitance.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Common-source output capacitance, input shorted. $C_{os} = C_{gs}+C_{ds}$. However, $C_{os}$ is essentially header capacitance.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Same as $C_{os}$ if $V_{gs} = 0$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Same as $C_{os}$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Source-to-gate capacitance, gate and drain shorted. $C_{gs} = C_{gs}+C_{ds}$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Source-to-gate capacitance with drain open. $C_{gs} = C_{gs}+C_{ds}$.</td>
<td></td>
</tr>
<tr>
<td>$C_{in}$</td>
<td>Drain-to-gate capacitance with source open. $C_{gs} = C_{gs}+C_{ds}$, but $&lt; C_{gs}$.</td>
<td></td>
</tr>
</tbody>
</table>

**Diagram:**

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As \( e_n \) is proportional to \( \sqrt{B} \), the units used are \( \mu V/\sqrt{Hz} \). The conversion from \( e_n \) to \( R_v \) becomes an easy one with Fig. 2a. The characterization of a FET by \( R_v \) is equivalent to specifying a NF. Thus, when a FET is said to have an equivalent noise resistance of 20 k\(@\) at 1 kHz, this is tantamount to saying \( NF=3 \text{ dB} \) at 1 kHz with \( R_{gen}=20 \text{ k}\@\).

How to spot noise in narrow bands

Narrow-band NF, \( e_n \), and \( i_n \) are usually specified either as NB-noise or spot-noise measurements. This means simply that the band-width is sufficiently small for a change in band-width not to affect the NF of \( e_n/\sqrt{Hz} \). In a wide-band measurement, over a frequency range of 0.1 to 5 kHz for instance, this may not be true. From Fig. 1b it is seen that the integral of \( e_n \) in this band is a function of bandwidth. On the other hand, from 1 to 50 Hz or from 10 to 100 kHz, the integral of \( e_n \) is essentially independent of bandwidth. For this reason, it is important to note the bandwidth specified for any noise measurement. Alternatively, it must be stated that the parameter is a spot noise measurement.

When selecting a FET for use in sound-reproduction equipment, a noise specification at or above 1 kHz is desirable. A noise specification at 20 or 100 Hz for audio equipment will most often result in over-specification and will increase component cost. As the ear is progressively less sensitive to frequencies below a few kilohertz, noise measurements on sound-reproduction equipment are ordinarily carried out with wide-band equipment possessing a certain specified response characteristic.

The characteristics of several filters used for this purpose appear in Fig. 2b. Curve A is recommended by the American Standards Association for low-level noise measurements.\(^{3,3}\) Curve B is most useful for noise analysis in offices and factories, although it is recommended by the Radio Manufacturers Association for noise measurement on sound-reproduction equipment.\(^{4}\) Neither of these two curves is universally applicable as each was generated from single-frequency measurements. Curve C, a standard (DIN 45-405) used for this purpose in Germany, appears to be more applicable, as it includes the effect of the high frequency's masking of low frequencies.

It is most meaningful, therefore, to specify noise measurement for sound-reproduction equipment at a frequency from 1 to 5 kHz or over a broad band, regardless of whether one of the weighting curves (A, B or C) is used.

\( C_{iss} \) affects signal-to-noise ratio

Although NF, \( e_n \), and \( i_n \) specify noise performance of the FET, there are those applications where \( C_{iss} \) can significantly affect circuit signal-to-noise ratio (S/N). For a given \( e_n \) and \( i_n \) it is desirable to have a high \( g_{rs} \) so that the output \( S/N \) is high enough to reduce the effect of second-stage noise. Consider, for example, the capacitor microphone preamplifier, of Fig. 3. As the signal source is a small capacitor, \( C_{in} \) of the amplifier and the microphone capacitance become a voltage divider. It is thus important to minimize \( C_{in} \) for a high \( S/N \). Also, the gate-return resistor \( R_o \) should be as high as practical to extend the low-frequency cut-off; therefore, \( i_n \) should be low to minimize current noise in \( R_o \) and \( C_s \). Assuming a noiseless microphone, the noise performance of the preamplifier may be determined by a step-by-step procedure.

Referring to Fig. 3, we use the following circuit characteristics:

- \( g_{fs} = 1000 \mu \text{mhos at operating point.} \)
- \( e_n = 0.05 \mu V/\sqrt{Hz} \) at 1 kHz.
- \( i_n = 0.001 \mu A/\sqrt{Hz} \) at 1 kHz.
- \( C_{iss} = 4.5 \text{ pF; } \)
- \( C_{rss} = 1.5 \text{ pF; } \)
- \( C_s = 27 \text{ pF; } \)
- \( R_o = 100 \text{ M}\@\)
- \( R_o = 10 \text{ k}\@ \)
- \( e_s = 1 \text{ mV at } 1 \text{ kHz} \)

Note that \( R_o \) is negligible in comparison to the 1-kHz reactance of the \( C_{in} \) and \( C_s \) combination. Also, the correlation between \( e_n \) and \( i_n \) is neglected. Now for the design:

1. Determine amplifier gain:
   \( A = g_{fs} R_o = 10 \).
2. Determine the input capacitance:
   \( C_{in} = C_{iss} + A C_{rss} = 19.5 \text{ pF}. \)
3. Determine the input signal level at the frequency of interest:
   \( e_{in} = e_s \left( \frac{C_s}{C_s + C_{in}} \right) = 0.6 \text{ mV at } 1 \text{ kHz}. \) (4)
4. Determine the total equivalent input noise, total \( \bar{e}_n \). Observe that \( e_n \) divides between \( Z_{gen} \) and \( Z_{in} \) in the same manner as does \( e_n \).

Thus,
\[
\text{Total } \bar{e}_n = \left[ e_n \left( \frac{C_s}{C_s + C_{in}} \right) \right]^2 + \left[ \frac{i_n}{\omega (C_s + AC_{rss})} \right]^2,
\]
and
\[
\text{Total } \bar{e}_n = 9.77 \text{ nV/}\sqrt{Hz} \text{ at } 1 \text{ kHz}. \quad (5a)
\]
5. Determine \( S/N \) in the bandwidth 1-10 kHz. Assume \( e_n \) and \( i_n \) to be constant with frequency. Use no weighting function. Note that total \( \bar{e}_n \) is due principally to \( e_n \) and not \( i_n \). One may then assume total \( \bar{e}_n \) is constant with frequency in the range of interest. Thus,
\[
S/N = 20 \log \frac{e_{in}}{\text{total } \bar{e}_n \cdot 9000} = 76 \text{ dB}. \quad (6)
\]

June 7, 1966
FETs are suitable for high-frequency amplifying applications. A cascode video amplifier (a), a 100-MHz neutralized amplifier (b) and a 30-MHz cascode amplifier (c) all exhibit good gain, low noise and little harmonic distortion.

Maximum available gain (MAG) in FET high-frequency amplifiers is calculated from the master chart. It exhibits the conductance vs frequency relationship of FET devices now on the market. Shown are the input \( g_{iss} \), output \( g_{oss} \) and transfer \( g_{rs} \) conductances.

This completes the low-noise amplifier class of FET applications. We now turn to the high-frequency type (FETs operated above 1.0 MHz).

FETs suitable for RF operation

A high-frequency amplifier is arbitrarily defined as one which is operating at frequencies above 1 MHz. Included in this definition are both video and RF amplifiers (Fig. 4). The small-signal FET characteristics are of prime importance (See Table 2). They establish the suitability of a particular FET and govern design of FETs in high-frequency amplifiers.

For the video designs the major parameters are \( g_{fs}, C_{iss}, C_{rss} \) and \( C_{a} \). Each of these terms is critical in RF amplifier design, where the values of \( g_{fs}, g_{oss} \) and \( NF \) also exert a major influence. The video amplifier is characterized by its gainbandwidth (\( GB \)) product. This is expressed as

\[
GB = \frac{g_{fs}}{\omega [C_{iss} + C_{out}]^2} = \frac{g_{fs}}{\omega [C_{iss} + (1 + A)C_{rss}]} \tag{7}
\]

For the cascode connection this becomes

\[
GB = \frac{g_{fs}}{\omega [C_{iss} + 2C_{rss}]} \tag{8}
\]

Note that \( C_{oss} = C_{rss} \) and \( C_{iss} = C_{gs} + C_{rss} \).

The \( GB \) is important for RF amplifiers; however bandwidth is ordinarily limited to a few megahertz at most, rather than the tens of megahertz required of a video amplifier. An RF amplifier is limited in power gain by the real part of its input and output impedances. When these are matched to source and load, maximum available gain (MAG) is realized. For the FET

\[
MAG = \frac{|g_{fs}|^2}{4g_{iss} g_{oss}} \tag{9}
\]

FET performance as a small-signal RF amplifier is also limited by its high-frequency noise characteristics. The value of \( C_{rss} \) is important, as its effect must be neutralized for stable amplifier operation.

An important advantage of the FET in RF circuitry is its almost perfect square-law transfer characteristic. Because of this, amplifier cross-modulation figures are at least an order of magnitude better than those of bipolar transistor designs. In most cases a substantial improvement is obtained even over vacuum-tube circuits.\(^{2,3}\)

FET transconductance remains essentially constant to several hundred MHz. Both \( g_{iss} \) and \( g_{oss} \) increase with a nearly square relationship to frequency. With available FETs, \( MAG \) is less than unity at that frequency where \( g_{fs} \) begins to decrease. \( MAG \) can be calculated from the curves in Fig. 5—which represent the characteristic obtainable with current FETs.

Recent improvements in the control of device input and output impedances at high operating frequencies has made possible the production of both MOS and FET units with acceptable per-
performance to 500 MHz. The MOS has a very low value of $g_{iss}$ at high frequencies, thus permitting excellent hf operation. However, its output conductance rises in the same manner as that of the FET, limiting the MAG obtainable.

FET high-frequency noise figure (NF) for properly designed practical circuits is 2 dB or better at 10 MHz (with an $R_{gen} = 1 \text{ k} \Omega$), and good noise performance extends to the vicinity of 500 MHz. In contrast to bipolar, these same FETs exhibit a typical NF of 5 dB at 10 Hz (with an $R_{gen} = 1 \text{ M} \Omega$). MOS high-frequency noise performance is approaching that of the FET, with at least one available unit specified with a NF of 4.5 dB at 450 MHz.

**Designing a 100-MHz cascode amplifier**

FET amplifier circuits resemble the triode-vacuum-tube types. Practical use of the cascode circuit is limited to about 100 MHz for good noise performance with maximum gain, unless cumbersome circuitry is added. Here is the step-by-step design procedure for a 100-MHz amplifier. We will use a FET with the following parameters:

- $g_{fs} = 3500 \mu \text{mho at 100 MHz}$  
  $C_{iss} = 4.5 \text{ pF}$
- $g_{iss} = 100 \mu \text{mho at 100 MHz}$  
  $C_{rss} = 1.5 \text{ pF}$
- $g_{oss} = 15 \mu \text{mho at 100 MHz}$  
  $f_o = 100 \text{ MHz}$

The procedure refers to the circuit of Fig. 4b.

1. **Calculate output-circuit component values.** The network $C_{oss} + C_o$ and $C_{co}$ match the output conductance to the load conductance. The output conductance is the sum of $g_{oss}$ plus the tuned-circuit conductance, $1/Z_p$. As $1/g_{oss} = 67 \text{ k} \Omega$, and the 100-MHz tuned-circuit $Q$ is such that $Z_p = 5 \text{ k} \Omega$, the latter determines the actual output conductance at 100 MHz. Then, $R_0 = 5 \text{ k} \Omega$ and $R_1 = 50 \Omega$. If $C_o = 1.2 \text{ pF}$, the matching capacitor value is determined by

$$ C_{oss} + C_o = C_{rss} + C_o = 2.7 \text{ pF}, \quad (10) $$

$$ C_{co} = \frac{1}{\omega \sqrt{R_o R_L}} = 3 \text{ pF}, \quad (11) $$

$$ L_o = \frac{1}{\omega^2 (C_{oss} + C_o + C_{co})} = 0.45 \mu \text{H}. \quad (12) $$

2. **Input circuit calculations are similar.** Assume input circuit $Q$ is such that $R_1 = 1 \text{ k} \Omega$ to achieve optimum noise figure. With $R_{gen} = 50 \Omega$ and $C_i = 1.2 \text{ pF}$, $C_{rss}$ is subtracted from $C_{iss}$ to obtain actual FET input capacitance because $C_{rss}$ is neutralized. Thus,

$$ C_{iss} - C_{rss} + C_i = 4.7 \text{ pF}, \quad (13) $$

$$ C_{ci} = \frac{1}{\omega \sqrt{R_i R_{gen}}} = 1.3 \text{ pF}, \quad (14) $$

$$ L_i = \frac{1}{\omega^2 (C_{iss} - C_{rss} + C_i + C_{ci})} = 0.42 \mu \text{H}. \quad (15) $$

3. **The circuit is neutralized using the "sloppy bypass" technique as follows:** The fraction of $v_o$ appearing out of phase on $C_2$ is

$$ v_f = \frac{C_{out}}{C_2} \quad (16) $$

where $C_{out}$ is the sum of $C_{oss}$, $C_o$ and $C_{co}$.

The fraction of $v_f$ appearing at the gate is approximately

$$ v_g = \frac{1/j\omega C_{rss}}{R_in + 1/j\omega C_{rss}} v_f. \quad (17) $$

This must be set equal to the fraction of $v_o$ appearing in phase at the gate. Note that the fraction is due to the feedback function of $C_{rss}$. The latter is given by

$$ v_{fb} = \frac{1/j\omega C_{rss}}{R_in + 1/j\omega C_{rss}} (v_o). \quad (18) $$

Equating (16) and (17) and using Eq. 18 yield

$$ \frac{1/j\omega C_{rss}}{R_in + 1/j\omega C_{rss}} = \frac{C_{out}}{C_2} \left[ \frac{1/j\omega C_i}{R_in + 1/j\omega C_i} \right], \quad (19a) $$

$$ 1 + j\omega C_ir_{in} = \frac{C_{out}}{C_2} (1 + j\omega C_{rss}R_{in}). \quad (19b) $$

For all practical purposes, it is only the imaginary terms which must be set equal. Doing this establishes the relationship,

$$ C_i C_2 = C_{out} C_{rss}. \quad (20) $$

This wraps up the higher-frequency FET amplifier applications. In Part 3 (ED 15, June 21) we will take up the low drift, single-ended and differential FET circuits.

**References:**

Sonotone batteries spark portable power to action!

Mite or might—if it's portable, the chances are it relies on Sonotone rechargeable sintered-plate, nickel-cadmium cells and batteries. That goes for everything from space satellites and missiles to cordless razors and toothbrushes, and even to small, delicate medical instruments. The reason is this: Sonotone pioneered in the development of high-power/low-weight rechargeable batteries. With more years of experience in this field than any other American company, Sonotone has built up an unmatched record of reliability…and an unmatched reputation for cooperation with design engineers. What new product are you working on? Let Sonotone help you solve your power problem. Write today for technical data, stating your application requirements…and we'll be happy to lend you a hand.

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Now, you can enjoy all the advantages of automatic, electronic tuning — at about half the price you might expect to pay — with new, high Q EPICAP diodes from Motorola.

For example, in Electronic Countermeasures (ECM) equipment, the EPICAP diode can be used with a "saw-tooth wave" design to automatically sweep a broad frequency range (200 to 400 MHz); and, in the process, eliminate the need for a clumsy sweep generator motor. (Drop a note to our applications group if you'd like more information on this one.)

You'll also find EPICAPS well-suited for difficult tuning jobs in other RF applications such as FM tuning, aircraft radio, etc. In fact, in every application above 200 MHz, you'll get faster, more reliable performance — the highest performance / lowest cost combination in the industry, for applications up to 500 MHz.

These new voltage-variable capacitors feature improved fabrication techniques which yield a low leakage and a Figure of Merit (Q) that's specified at a minimum of 200 at 50 MHz and a bias of 4 volts dc. In addition, EPICAPS boast a 20 nanoamp reverse leakage current, due to the good, "clean" surfaces you'd expect in a high quality diode — your assurance of maximum reliability. And, the rugged, new Ramrod* (straight-through) DO-7 glass diode package adds another measure of increased reliability.

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"HOW TO TUNE RESONANT CIRCUITS" COVERED IN NOTE

A comprehensive report that details a step-by-step design procedure and helps you select the optimum EPICAP for your specific application is the subject matter of Motorola Application Note 196. Circuit schematics, equations and a family of curves are used for determination of tuning range as a function of maximum EPICAP capacity to circuit capacity as a parameter.

Figures 1 and 2 (left) show the most common forms taken by EPICAP resonant circuits. The equations and graphs of the report apply to both. For example, a graph has been developed to predict tuning range, using the voltage tunable capacitance and external circuit parameters. The discussion and use of that graph is the major thesis of AN 196. You're sure to find this well-written technical report a valuable addition to your semiconductor library. Write for it today, to Technical Information Center, Motorola Semiconductor Products Inc., P. O. Box 955, Phoenix, Arizona 85001.

ON READER-SERVICE CARD CIRCLE 161

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June 7, 1966
D/A converter uses thin-films to get high precision and maintain the flexibility needed for low-volume production.

If your microelectronics application calls for high accuracy, look beyond the popular monolithic integrated circuits. Hybrid thin-film circuits offer greater advantages.

One such application is in the digital-to-analog converter, where hybrid circuits provide the following:

- High resistance accuracy.
- Wide selection of active devices.
- Well matched resistance values.
- Improved temperature coefficients.
- Design flexibility.
- Economical low-volume production.

Four-bit D/A converter requires three ICs

An integrated digital-to-analog (D/A) converter can be made by connecting three integrated circuits (a buffer amplifier, a ladder switch and a ladder network) in series. If each of the integrated circuits is designed to handle 4 bits, the complete circuit will perform as a 4-bit D/A converter. Two sets of each will form the 8-bit D/A converter shown in Fig. 1, and three sets can be used to form a 12-bit converter. All the circuits are mounted in flat-packs, and their leads are arranged as shown to facilitate assembly.

The converter interprets a set of digital input pulses and converts it to an analog output signal. The buffer amplifier (normally ON) is turned OFF by a positive input signal. As the output of the buffer amplifier goes more negative, a turn-on drive current is supplied to the pnp transistor of the next stage, the ladder switch. The complementary npn transistor is simultaneously biased off and the output of the ladder switch goes to the reference voltage level. This negative reference voltage feeds the ladder network where it is divided down to the correct analog output. The output of the ladder network represents the sum of the various input signals.

With no input signals to the converter, the buffer amplifier is ON. This in turn sets the output of the ladder switch at zero volts (the offset voltage can be made extremely low) by driving the pnp transistor OFF and the npn transistor ON. With zero volts into the ladder network, the output of this stage also remains at zero volts.


The circuits described here are available as standard catalog items (buffer amplifier-UD4025; ladder switch-UD4001; ladder network-UT1000), and thus do not necessarily represent the best approach to D/A conversion for all applications. They do, however, show a practical method of designing microelectronic converters that are compatible with present-day silicon microcircuits.

Buffer uses individual transistor chips

The buffer amplifier is used to convert the digital input signal (0 to V_{cc}) into positive and negative current drives to the ladder switch. Though the buffer is designed for a rather restricted range of applications, the inherent design flexibility of thin-film circuits makes modifications simple to achieve when low-volume quantities are involved. Where large-volume use of a particular buffer circuit is indicated, a monolithic approach may be more practical.

The typical buffer circuit, shown in Fig. 2a, drives the complementary-transistor-pair ladder switch. When the buffer transistor is turned OFF, the pnp-transistor-side of the ladder switch is turned ON. With V_{cc} set to −20 volts, approximately 1 mA of current will flow into this transistor. To guarantee turn-off, the input signal must be at least as positive as the V_{cc} supply; if an input high level of 4 to 6 volts is anticipated, the V_{cc} supply should be set at +4 volts. If the input signal is lower then V_{cc} supply, the buffer amplifier will remain turned ON and thus drive the npn-transistor-side of the ladder switch also ON. With V_{cc} equal to +4 volts, approximately 1 mA of current will flow into this transistor. The circuit can handle input high levels as low as 1.5 volts if the V_{cc} supply is correspondingly lowered. Base drive to the pnp transistor when V_{cc} = 1.5 volts is reduced to approximately 0.25 mA.

A photograph of the actual buffer circuit is shown in Fig. 2b. The four input resistors and eight output resistors are nickel-chromium (NiCr) material deposited on silicon substrates. The use of two separate chips facilitates the internal wiring and allows for greater flexibility. These particular resistors were specified with a tolerance of ±10% and a temperature coefficient (TC) of ±100 ppm/°C. However, NiCr resistors are available with a ±5% tolerance and ±50 ppm/°C TC, in resistance values from 15 ohms to 150 kilohms.
1. **Eight-bit digital-to-analog converter** uses three different types of integrated circuits: a buffer amplifier, a ladder network, and a buffer amplifier. Note that the circled numbers refer to the pins on each individual package.

2. **The buffer amplifier** (a) converts the digital input signals to driving currents for the next stage, the ladder network. The base resistor network is the large block at the left in the photograph (b).

3. **Complementary transistor switch** (a) is used to provide extremely low offset voltages. With the base drive at 1 mA, both the pnp and npn transistors have an offset voltage of about 1 mV at cutoff (b). If the base current is increased (c), the offset voltage will also increase and the series resistance \( r_s \) will decrease.

This buffer circuit was designed to accept inputs from most present digital monolithic circuits, and also to:
- Accept low level input signals as high as +0.5 volts.
- Accept a high level from 1.5 to 5 volts. Larger inputs could be tolerated but a new design would be required in order to limit power consumption.
- Require only low levels of drive current or sink current from the driving circuit.
- Supply approximately ± 1 mA of base drive to the ladder switch.

With the hybrid thin-film approach, it is easy to change either the base resistors or both collector resistors, or to change from a pnp to an npn stage. A circuit designer can make breadboard circuits with conventional discrete components knowing that the hybrid version will behave similarly.

June 7, 1966
4. **Four-bit ladder switch** uses four pairs of complementary transistors (a). The circuit is actually made up of eight individual transistor chips (b). The two sets of transistors (pnp's are on the left) are each bonded to a conductive substrate to achieve a common collector connection with a low collector series resistance.

5. **Tantalum thin-film ladder network** uses highly stable and precise resistors (a). An 8-bit network is made by connecting the output of one circuit (pin 6) to pin 9 of a second circuit.

This relationship holds because the individual semiconductor chips and the tantalum and NiCr resistors do not have the parasitic capacitance problems associated with monolithic devices.

**Complementary transistors make up ladder switch**

The ladder switch acts as a single-pole double-throw switch, since the ladder converter-network requires inputs which are at ground potential or at the reference voltage. The ladder switch must have negligible voltage errors in both the ON and OFF conditions. To do this a complementary transistor switch, shown in Fig. 3a, is used. The complementary transistor approach is highly desirable, since both sides of the switch can be operated in the inverted mode, thereby providing a low offset voltage. In this arrangement the voltage-drop contribution of $I_e r_s$ (where $r_s$ is the ON series resistance) is of opposite polarity to that of the offset voltage, and offset errors are minimized. The output emitter characteristics of the npn and pnp devices are shown in Fig. 3b.

They each have an offset voltage of 1 mV and a series resistance of 10 ohms, which is typical for both devices when the base drive ($I_b$) is 1 mA.

Since the series resistance of the transistors varies as an inverse function of $I_b$ and the offset voltage varies as a direct function of $I_b$, it is possible to choose the optimum base drive currents to the switches in anticipation of the required load currents. The curves in Fig. 3c show the typical variations of $V_{off}$ and $r_s$ with $I_b$ for the transistors used. The characteristics of the npn and pnp transistors are similar enough for the curve to represent both. By adjustment of $I_b$ it is possible to restrict the error of the ladder switches to $\pm 2$ mV, which in turn holds the error at the output to less than $\pm 1$ mV. For a 12-bit converter operating from a 10-volt reference voltage, this is equivalent to $\pm 1/2$ least significant bit (LSB), since

$$1 \text{ LSB} = \frac{1}{4096} (V_{REF})$$

A 4-bit ladder switch is schematically shown in...
Fig. 4a. Figure 4b is a photograph of the actual integrated circuit. Both the pnp and the npn sets of transistors have their collectors tied in common, each set on a single land area. The common emitters and bases of the npn-pnp pairs are interconnected and then brought out of the package.

**Ladder network has well-matched resistances**

The ladder network, shown in Fig. 5a, consists of five 50-KΩ and four 25-KΩ tantalum resistors deposited on a silicon substrate. Figure 5c is a photograph of the actual circuit. The network is mounted in a 0.160 in. x 0.265 in. flat pack by standard thermocompression bonding techniques. Each resistor is adjusted to a tolerance of ± 0.5% and can be matched within a 0.5% spread. As a result, the maximum analog output error, due to the resistors, is ± 0.25%. In terms of least significant bits this would be somewhat less than one LSB for an 8-bit converter. (The LSB in an 8-bit converter is 1/256, or 0.39% of the reference voltage.) This maximum error, however, occurs only when the resistor at pin 5 is high and all other resistors are low, or vice versa. In actual practice, the resistors are randomly off tolerance and the bit accuracy is much better than this worst-case situation. Bit accuracy is typically 10 to 11 bits (1/1024 to 1/2048) and results in approximately 0.05% to 0.1% error in the analog output signal. Twelve-bit accuracy devices are available on a selection basis.

Ladder networks constructed in this configuration with values as low as 10 KΩ and 5 KΩ perform nearly identically. The difference that exists is related to the temperature coefficient of the resistances. The circuit in Fig. 5 has a typical TC of -150 ppm/°C, while the 10 KΩ and 5 KΩ networks, made with thicker tantalum, have a typical TC of -20 ppm/°C. Temperature-coefficient matching for the former is guaranteed to be within a spread of 25 ppm/°C.

In actuality, the effective match of temperature coefficients for the entire circuit is much better, as in the case of resistance value matching, often reaching a few parts per million per degree centigrade; often, the steps in the output of the ladder network will vary less than 1 mV over the -55°C to +125°C temperature range for a 10 volt reference input.

The settling time of the D/A converter is approximately 4 to 5 μs. Unfortunately, since the storage time of good chopper transistors, such as the ones used in the ladder switch, is relatively long, it is difficult to construct a fast converter with low switching errors. As a result, both the npn and pnp transistors in the ladder switch will be ON at the same time for an interval of about 1 μs. During this time the switch may be at ground instead of $V_{REF}$ or vice versa, and causes spike at the output. Though these spikes are largely the result of this “storage time” error, some of this error is also due to the RC time constant of the ladder resistors and their distributed capacity.

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NASA Tech Brief

Mount improves transducer accuracy

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June 7, 1966
Vacuum photo-diode constitutes simple, accurate current source

A vacuum photo diode, excited by a variable-intensity light source, makes an excellent current source with almost perfect isolation between input and output. The system shows a negligible temperature coefficient.

A simple and accurate (5%) current source was needed, for a neuro-physiological application, to deliver a variable 0.1 to 3 µA dc current to a stainless steel microelectrode for plating out iron onto neural tissue. Consideration had to be given to the load impedance, which might range from 1.0 to 100 MΩ during application of the current (5 s), owing to metal-issue interface changes.

The type 922 cartridge photo-diode used in the circuit (see schematic) exhibits a plate resistance on the order of 10⁹ ohms over an anode-to-cathode voltage range of 40 to 400 volts. The unit has a maximum dark current of 0.005 µA (at 25°C) and a maximum average current rating of 5 µA. Rs is a current-sensing resistor used to set the initial value of i with S₁ (low-leakage reed switch) closed. With S₁ open, current i is diverted into the probe. Less than a 5% change in a 2-µA current is observed when S₁ is opened into a 100 MΩ resistor placed across the probe terminals. Thus, no feedback system is required to achieve the accuracy desired.

The exciting lamps (GE #345) are mounted in a light-tight box with the 922 and their intensity is controlled with series rheostat R₁. Battery B₁ is a nickel-cadmium type chosen for its flat discharge characteristic, portability and noise isolation.

Once a current-intensity calibration curve is obtained, Rₛ may be eliminated and the entire photo-tube side of the two-port floated. Or, Rₛ may be used in an operational amplifier circuit with the exciting lamps in the feedback loop for either precise current control or modulation.

Many optical modulation techniques can also be implemented with this photo-diode current source. The frequency response of the system is mainly limited by stray capacity rather than transit time, as far as the impedance levels discussed here are concerned.

Julius M. J. Madey, Research Engineer, California Institute of Technology, Pasadena, Calif.

Diodes ensure start of astable multivibrator

A simple, inexpensive diode arrangement prevents an astable multivibrator from “locking-up” when starting power is applied.

It is applicable to both silicon and germanium circuits, and is far less involved than competitive logic circuitry schemes designed to detect the lock-up. The design (Fig. la) uses the diodes to force the multivibrator into an unstable condition.

Given a collector current level at which the astable is to work, the value of Rs is chosen, so that the sum of the Vᵦₑ drop caused by the emitter current of one saturated transistor only and the base-to-emitter voltage of that transistor is slightly less than the forward drop of the silicon diode, CR, in its region of low conduction. Vᵦₑ = 0.3 V for a 2N404 where the base current does not exceed 0.5 mA. Hence, Vᵦₑ is to be 0.2 to 0.25 V.

At start-up time, if both Q₁ and Q₂ are to saturate, Vᵦₑ would jump to 0.4 or 0.5 volt, and the base potentials of Q₁ and Q₂ above ground would be near 0.7 volts, a region where the diodes CR would sustain a current of several milliamperes. Hence, the base currents of Q₁ and
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There's more. High shock, high vibration, better arc tracking resistance. And better sealing.

Cutler-Hammer military switches give you the truly "big" reliability you must have in aerospace and ground support. The kind you can't get in standard MIL-S-3950 switches. Our positive actions are built to MIL-S-8834.

You can choose exactly the military switch you need from the thousands we make. Just call your local stocking distributor or local Cutler-Hammer sales office. Or write us for a catalog.
VERSATILE SERVO TESTERS

100-C
- Measures transient and frequency response
- Checks carrier and DC servos
- For lab & development work

103-A AN/TPM-8
- Military-standard
- Ge, no-go test
- Production test, Q.C. and field maintenance

INDUSTRIAL CONTROL COMPANY
CENTRAL AVENUE
FARMINGDALE, L.I., N.Y.
AT PINELAWN
518-684-3000

ICs for bounceless push-button switching

The effect of the mechanical bounce of push-button switches can be eliminated from a system through the use of a portion of a readily available, inexpensive IC. This is achieved by isolating the output from the input during the interval when bounce occurs.

Using positive logic and NAND/NOR gates (see schematic), construct a “latch” flip-flop with two 2-input gates (many IC manufacturers put four 2-input gates in a 14-lead flat pack).

IC gates form flip-flop to establish a bounceless, push-button switching action.
When the switch is positioned as shown, the $G_i$ input is held at ground. When the button is depressed, the $G_2$ input is held at ground, switching the flip-flop. However, when the contacts bounce from the NO position to an open-circuit condition, the flip-flop does not change state. This action provides a bounce-free switching action at either output (A or B).


**VOTE FOR 112**

**Tunnel-diode generator calibrates frequency counter**

A common method for calibrating frequency counters is to beat a harmonic of the 100 kHz timing-crystal output with a WWV (often at 15 MHz) carrier. A simple tunnel-diode generator circuit can be used to generate the necessary harmonics.

The circuit (see schematic) generates strong harmonics to at least 30 MHz when connected to the 100-kHz output of the frequency counter. The result is much more reliable reception of easily discernible beat notes.

The harmonics are the product of a cyclic series of pulses generated by $D_1$. These pulses occur once for each cycle of the input frequency as tunnel diode $D_1$ switches from a low- to a high-impedance state. The higher-frequency components of these pulses are coupled to the receiver antenna terminals through $C_1$.

Resistor $R_1$ must be small enough to allow the input current to exceed $I_{pik}$ of the tunnel diode. $R_1$ must also be large enough to prevent the continuous current rating of $D_1$ from being exceeded. $D_1$ has an $I_{pik}$ of 0.47 mA. For this value, a $R_1$ of 1000 Ω works well for signal inputs between 0.5 and 1 volt.

Tunnel diodes with larger values of $I_{pik}$ may be used, providing the signal source can deliver pulse currents in excess of $I_{pik}$ of the diode. In most cases, the network may be left permanently attached to either the receiver or the counter.

Kenneth G. Holmes, Chief Engineer, Magnetic Circuit Elements Inc., Montrose, Calif.

**VOTE FOR 113**

IFD Winner for March 1, 1966
Larry Blaser, Senior Engineer, Fairchild Semiconductor, Mountain View, Calif.

His idea, “Dual MOS-FET simplifies FM multiplex decoder,” has been voted the $50 Most Valuable of Issue Award.

Cast Your Vote for the Best Idea in this Issue.
3 Tektronix Portable Oscilloscopes

Choose one for your own measurement requirements

<table>
<thead>
<tr>
<th>TYPE 453</th>
<th>TYPE 422</th>
<th>TYPE 321A</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Bandwidth</strong></td>
<td><strong>Deflection Factor</strong></td>
<td><strong>DC-6 MHz</strong></td>
</tr>
<tr>
<td>DC-50 MHz</td>
<td>5 mV/div to 10 V/div</td>
<td>10 mV/div to 20 V/div</td>
</tr>
<tr>
<td><strong>Channels</strong></td>
<td><strong>Delay Line</strong></td>
<td><strong>Sweep Delay</strong></td>
</tr>
<tr>
<td>Dual</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td><strong>Height</strong></td>
<td><strong>Sweep Delay</strong></td>
<td><strong>Height</strong></td>
</tr>
<tr>
<td>7 1/4</td>
<td>Yes</td>
<td>8 3/4</td>
</tr>
<tr>
<td><strong>Width</strong></td>
<td><strong>Height</strong></td>
<td><strong>Width</strong></td>
</tr>
<tr>
<td>12 3/4</td>
<td>10</td>
<td>16</td>
</tr>
<tr>
<td><strong>Depth</strong></td>
<td><strong>Height</strong></td>
<td><strong>Depth</strong></td>
</tr>
<tr>
<td>20 1/2</td>
<td>14</td>
<td>14</td>
</tr>
<tr>
<td><strong>Weight (pounds)</strong></td>
<td><strong>Power Requirements</strong></td>
<td><strong>Rack Mount Available</strong></td>
</tr>
<tr>
<td>28 1/4</td>
<td>AC</td>
<td>Yes (7&quot; Rack Height)</td>
</tr>
<tr>
<td><strong>Probes and Accessories Included</strong></td>
<td><strong>Price</strong></td>
<td>Price</td>
</tr>
<tr>
<td>Yes</td>
<td><strong>$2050</strong></td>
<td><strong>$1400 (AC Model)</strong></td>
</tr>
<tr>
<td><strong>Price</strong></td>
<td><strong>$1750 (AC/DC Model)</strong></td>
<td><strong>$3900</strong></td>
</tr>
</tbody>
</table>

A TEKTRONIX PORTABLE will fit your specific requirements. Your Tektronix field engineer will be happy to demonstrate any of these oscilloscopes at your location. Just call him, or write Tektronix, Inc., P. O. Box 500, Beaverton, Oregon 97005.
ED Products

Breadboard brings IC assembly to the bench PAGE 66
Plotter programs 127 steps with single command PAGE 82
Pnp switches in 20 µs in the 10 µA region PAGE 84

One instruction, 127 increments ... 82
Swift-switching pnp ... 84

Instant, inexpensive ICs ... 66

June 7, 1966
IC breadboard allows simple bench assembly of custom circuits with the drop of a dot

Integrated circuit design and evaluation need no longer be expensive and involved. Custom or prototype units can be produced right on the bench with the WS177 Insta-Circuit. The Insta-Circuit is a monolithic 124- x 86-mil silicon die, packaged in an open 1/4- x 1/4- inch, 14-lead (TO-86) flat-pack. It contains eight planar transistors, five zener diodes and four sets of 11 diffused silicon resistors. A “road map” with keyed call-outs and values is supplied to ease component selection.

On the surface of the chip is a set of aluminum conductors and component bonding pads. This interconnection pattern has a constant 1-mil separation. A single ball or wedge bond acts as the shorting bar across appropriate pairs of conductors and components to complete the desired circuit. This technique holds jumper bonds to a minimum, thus eliminating the conventional unreliable bird’s-nest wiring. The bond is made with a pigtail attached. Removal of this unwanted pigtail does not impair the bond’s solidity.

All that is required in the way of capital equipment is a ball or wedge bonder. The relative economy of the bonder ($2000 and up) provides a quick, cheap technique for IC design and production.

Insta-Circuits are supplied in open packages with a solder preform and lid. Lid sealing may be accomplished with a simple hot plate or soldering iron in the absence of commercial sealing equipment. For production runs, the dice should be cleaned with alcohol or similar agent, baked in an inert gas atmosphere at 200 °C for eight hours and held in a desiccator until sealed.

Each set of resistors ranges from 20 Ω to 20 kΩ in a binary-coded relationship. Appropriate series connections yield any resistance value from 20 Ω to 46 kΩ in 100-Ω increments and parallel combinations provide intermediate values. Absolute values vary less than 20% and temperature coefficients are approximately 2000 ppm/°C. Tracking between two resistors over the normal temperature range is 0.2%. Capacity to substrate of the 20-kΩ resistor does not exceed 7 pF.

The eight transistors are non-gold doped npn epitaxial planar types. Beta is typically 40 to 120 at 25 °C. Cut-off frequency exceeds 200 MHz.

The remaining components are five zener diodes. Two 5.8-V zeners are connected to the substrate, and two are floating. The fifth is a floating 8.2-V zener. All are rated less than ±5% at 25 °C. The entire die dissipates no more than 250 mW.

The flexibility of the die is typified by the design of a summing amplifier circuit (see below).
Component characteristics (25°C)

<table>
<thead>
<tr>
<th>Resistors (±20% +5% match)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4 - 50 Ω (tapped at 20 Ω)</td>
</tr>
<tr>
<td>4 - 100 Ω</td>
</tr>
<tr>
<td>4 - 200 Ω</td>
</tr>
<tr>
<td>4 - 400 Ω</td>
</tr>
<tr>
<td>4 - 800 Ω</td>
</tr>
<tr>
<td>4 - 1000 Ω</td>
</tr>
<tr>
<td>4 - 2000 Ω</td>
</tr>
<tr>
<td>4 - 4000 Ω</td>
</tr>
<tr>
<td>4 - 8000 Ω</td>
</tr>
<tr>
<td>4 - 10,000 Ω</td>
</tr>
<tr>
<td>4 - 20,000 Ω</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Transistors</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\beta = 40 - 120$</td>
</tr>
<tr>
<td>$V_{CE0} &gt; 30$ V</td>
</tr>
<tr>
<td>$V_{CB0} &gt; 60$ V</td>
</tr>
<tr>
<td>$V_{EB0} = 8.2$ V±5%</td>
</tr>
<tr>
<td>$f_T &gt; 200$ MHz</td>
</tr>
</tbody>
</table>

Zener diodes (±5%)
- 2 - 5.8 V (to substrate)
- 2 - 5.8 V (floating)
- 1 - 8.2 V (floating)

Two 8.2-V zeners are required and only one is available. A ball bond shortens the collector-to-base junction of an unused transistor and utilizes its 8.2-V emitter-to-base voltage. Signal diodes to 60 V may be obtained by the same techniques. The turn-on characteristics of every diode are the same. The designer's limits are stretched well past those seemingly imposed.

Using the Insta-Circuit, the designer may now evolve an IC from schematic to prototype in a few hours, with the inherent advantages of in-house design.

The devices are also available in a 12-pin modified TO-5 header with the same price structure.

P&A: $39.50 (1 to 9), $29 (50 to 499); stock. Westinghouse Electric Corp., Molecular Electronics Div., P. O. Box 7377, Elkridge, Md. Phone: (301) 796-3666.

Circle No. 350

Typical ball bond is circled in completed amplifier. The bond may short a component and conductor or component pairs.

June 7, 1966
**ANNOUNCING...A NEW MULTIRANGE ANALOG-TO-DIGITAL CONVERTER**

The Model 820 Series AUTOVERTER—designed and developed by DSE—is an automatic ranging, general purpose, solid state, analog-to-digital converter. It is especially ideal for a broad spectrum of applications requiring the conversion of raw information into computer input data. Application requirements of wide dynamic ranges, such as, testing and checking out laboratory equipment, monitoring aircraft on the flight-line, measuring public utility system data, converting geophysical field expedition data, on-board airborne telemetry systems, and others.

The DSE AUTOVERTER exhibits an input dynamic range in excess of 160 db. Providing an actual reading accuracy and resolution ±0.1%, over a dynamic range in excess of 100 db, the Model 820 Series offers the equivalent range and resolution of a 26 binary bit converter—using only 16 bits of data.

Available in four models, with a dynamic range from ±10 mv to ±327.7 V, the DSE AUTOVERTER is convenient for measuring a single test parameter which has a wide dynamic range, or measuring many channels of analog data which together have an extremely wide range.

The AUTOVERTER is another "state of the art" original concept from DSE to add to its data conversion capabilities in the GSE and airborne fields. Complete information and specifications are available upon request. Simply write, wire or telephone today.

Engineers: If your field is data conversion, an exceptional opportunity is available at DSE. Write to personnel director. An equal opportunity employer.

**Power divider**

HHP-50 power hybrids handle up to 500 W cw and up to 1500 W peak envelope power. Four ports with type N connectors exhibit 25-dB min isolation between the two input arms and between the sum and difference arms. Insertion loss is 0.5 db max, phase balance between input arms is 5° max, amplitude balance is 0.2 dB max and vswr is 1.3.


**Time-delay switch**

A 2-oz, hermetically sealed time-delay switch provides 150 ms delay at 22 to 36 Vdc and up to 0.5 A. The model M-501-1-2 functions on the first event at the specified time interval.

Time interval may be modified from 0.01 to 10 s. The 2-pole device features line transient protection and built-in suppression for inductive loads.

Planautics Corp., 410 S. Cedros Ave., Solano Beach, Calif. Phone: (714) 755-1181.
Mix these signal, power and coax leads in any combination.

Burndy Trim Trio Connectors—available in many shapes—accept three contact styles, all crimp-removable, for signal and power leads #16 thru #24, twisted pair #24 and #26, and subminiature coaxial cables.

Changing conductors is fast and simple, whether for lower voltage drop or better shielding or mechanical reasons. This makes Trim Trio Connectors ideal for breadboard and prototype work as well as production. For large production runs you can take advantage of the economies offered by the automatic Burndy Hyfematic™ with a crimp rate of up to 3000 contacts per hour.

Get more details on how you can take advantage of the Burndy Trim Trio System—THE ACCEPTED METHOD OF INTER-MIXING CONTACTS.

INTERNATIONAL SALES HEADQUARTERS AND MANUFACTURING FACILITIES:

BURNDY
Norwalk, Connecticut
Digital readout

Series TNR-50 digital readouts consist of 8 models handling 8-wire BCD input in 1, 2, 4, 8 code with input signals of 3.5 V. Supply voltage of 180 ± 10 Vdc at 2 to 12 mA is confined to the panel areas. The transistorized decoder-driven circuitry eliminates diode decoders. Nixie tubes have a 100,000- to 200,000-hour life expectancy. Other input codes and a variety of signal voltages are available.

Transistor Electronics Corp., P. O. Box 6191, Minneapolis. Phone: (612) 941-1100. Circle No. 264

Pentode

The PL-8582/267 ceramic beam pentode has a third-order intermodulation distortion level of at least −40 dB at 350 W useful power output. Plate dissipation rating is 300 W. The external-anode, forced-air cooled tube is 2.16-in. long by 1.75-in. in diameter and uses a vane-type suppressor grid and an oxide-coated unipotential cathode. Required filament voltage is 26.5 V at 1.0. Plate voltage is 200 V max and plate current is 350 mA max. Transconductance is 0.04 mhos.

Price: $87 (1 to 5). Machlett Labs., 312 N. Nopal St., Santa Barbara, Calif. Phone: (805) 965-481. Circle No. 265

Operational amplifier

Model 115D differential op-amp is designed as an amplifying black box for PC cards with 0.1-in. hole spacing. Output is ±10 V at 4 mA and unity gain bandwidth is 1 MHz. Voltage drift is 25 µV/°C and current drift is 5 nA/°C. Narrow band input noise is 10 µV peak. Package is 0.8-in.3 molded epoxy.

P&A: $19 (1 to 9); stock. Zeltex, Inc., 1000 Chalmar Blvd., Concord, Calif. Phone: (415) 686-6660. Circle No. 266
IF YOU CAN’T AFFORD TO DESIGN WITH FAIRCHILD DTµL, YOU CAN’T AFFORD TO DESIGN.

Industrial IC's at about $1.00 per Function: Fairchild Diode-Transistor Micrologic® Integrated Circuits are now available at prices you can afford. Take your pick of dual in-line packaging for production line use (at about $1.00 per function at distributor quantities) or Mil spec rated 1/4” x 1/4” Cerpaks. Either way you get high performance monolithic logic elements for a wide variety of applications.

Complete, versatile line: Fairchild DTµL circuits offer unique design features: A complete line which includes both binary and gate elements, completely compatible with one another. Gates may be wire OR'ed at the output. Expander inputs can be used with 930, 932, and 944 elements. Low input current leakage. High noise immunity.

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MIIROLOGIC: A FAIRCHILD TRADEMARK
MOL Resistors

... is just one reason why all major TV manufacturers use Mallory MOL film resistors. On 10,000-hour load-life test, resistance changed less than 5%. Other reasons: temperature coefficient of 250 PPM/°C; proved flame resistance, stability in humidity. Plus prompt delivery from expanded production capacity. Write for data and quotation. Mallory Controls Company, a division of P. R. Mallory & Co. Inc., Frankfort, Indiana 46041.

COMPONENTS

Accumulator

A lifetime of 10⁶ cycles is claimed for this 28-position numerical accumulator. Series 130-2532 adds and subtracts by single counts from 0 to 28. Count may be stopped at any number and reset one step at a time or completely reset to zero. Pulsing rate is 350 to 400 counts/minute. Solenoid operating voltages are 12 to 115 Vac or dc.

P&A: $7.50 (1 to 9); stock to 3 wks. Chicago Dynamic Industries, 1725 Diversey Blvd., Chicago. Phone: (312) 935-4600.

Circle No. 267

Heat sinks

Two series of low-profile heat dissipators accommodate press-fit diodes, rectifiers and SCRs, and transistor cans. Both feature a 10-sided, formed-fin configuration for max heat transfer. Series 606 for press-fit devices has a controlled press-fit aluminum expansion jacket. Series 600 is available with hole patterns for TO-3, TO-6, TO-8 and TO-36 cans.

P&A: From $0.42; stock. Alpha Components Corp., 4222 Glencoe, Venice, Calif. Phone: (213) 398-7773.

Circle No. 268

STABILITY

50th ANNIVERSARY

MALLORY

ON READER-SERVICE CARD CIRCLE 29

ON READER-SERVICE CARD CIRCLE 223
Designing and fabricating cathode ray tubes for out-of-the-ordinary requirements is a specialty at ETC. We thrive on challenges.
Each of the different tube types that make up our "standard" line was designed to answer a specific customer need. They range from small electrostatic CRT’s to a twelve-gun multiple-display model, from fibre optic screens to distortionless rear windows. And they’re all deliverable, saving you the expense of prototype development and de-bugging.
The same design ingenuity that turned these custom-engineered tubes into standard items can be applied to the modification of an existing tube, or the development of an entirely new one, if a stock answer doesn’t fill the bill. Whatever it takes, we’ve got the know-how and skills to do it... including over three decades of experience in multi-layer phosphors, multi-gun structure, non-linear spiral bandings, hybrid deflection, shielding and potting.
Whether your needs involve data display, radar, mapping, or any other exacting application, you can be certain that an ETC tube will give you light output, deflection sensitivity and spot size criteria equaling or exceeding those available elsewhere. And you’ll find our service to be prompt, efficient, courteous—no matter how simple or complex the assignment.
We can’t promise you a scientific breakthrough. But we will provide you with the right tube for the job... even if we have to stretch the current art boundaries to do it. Find out why this kind of "total CRT capability" is your assurance of the greatest value in display. Write today for our latest catalog.

ETC® has them... already developed... already de-bugged

Designing and fabricating cathode ray tubes for out-of-the-ordinary requirements is a specialty at ETC. We thrive on challenges.
Each of the different tube types that make up our "standard" line was designed to answer a specific customer need. They range from small electrostatic CRT’s to a twelve-gun multiple-display model, from fibre optic screens to distortionless rear windows. And they’re all deliverable, saving you the expense of prototype development and de-bugging.
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Whether your needs involve data display, radar, mapping, or any other exacting application, you can be certain that an ETC tube will give you light output, deflection sensitivity and spot size criteria equaling or exceeding those available elsewhere. And you’ll find our service to be prompt, efficient, courteous—no matter how simple or complex the assignment.
We can’t promise you a scientific breakthrough. But we will provide you with the right tube for the job... even if we have to stretch the current art boundaries to do it. Find out why this kind of "total CRT capability" is your assurance of the greatest value in display. Write today for our latest catalog.

General Atronics
Electronic Tube Division
Philadelphia - Pennsylvania 19118
When you demand maximum temperature stability in a miniature size...

**PAKTRON®**
**FILM/FOIL**
**POLYCARBONATE**
capacitors
are your answer!

When your design calls for stability in a capacitor operating temperature range of 
-65° C to +105° C, then PAKTRON polycarbonate film/foil capacitors are your answer. Hot or cold, the inherent stability of polycarbonate will provide you with consistent reliability over the entire temperature range. PAKTRON polycarbonate capacitors are tough, too. You'll find them highly resistant to moisture, shock, vibration and contamination. Available in two lines. The epoxy dipped (shown above) line for values up to 0.5 mfd and 400 WVDC and the molded line (not shown) for values to 0.1 mfd and 200 WVDC.

**A hybrid solid-state/nuvistor preamplifier, LRA 045, has a dc to 100-kHz bandwidth. The driven common-mode circuitry in the input stage provides independence of common mode rejection from source impedance variations.** Common mode rejection at 1 kHz is 140 dB and noise is less than 8 µV p-p. Input impedance is 20 MΩ and gain is 10,000.


*Circle No. 269*

Model 15 servo/differential relay incorporates a high-gain silicon amplifier requiring a primary power source and a derived phase sensing signal source. The relay output consists of dry reeds with 15-W ac or dc switching capacity. Current amplification factor is 10⁸. Signal input impedance is 20 kΩ.

Price: $31.95. Sensitak Instrument Corp., 531 Front St., Manchester, N. H. Phone: (603) 627-1432.

*Circle No. 270*
ROUGH WITH A REASON

Unique GVB finish cuts core winding costs

GVB encased cores mean fewer production delays because GVB does much more than seal the core box against potting material. Its matte finish provides a resilient, non-slip base for winding, and the tough epoxy skin prevents the wire from cutting through to the core box. Guaranteed not to fail, even when wound with heavy #6 wire, GVB surface also eliminates abraded wire problems. No prior taping of the core is required, so another winding operation is wiped out.

Magnetics doubles the normal guarantee on core box finishes by expressing it in this unique way: The guaranteed voltage breakdown (GVB) finish seals the box and is capable of withstanding at least 1,000 volts at 60 cycles between a bare winding and the aluminum case. Quality control monitors the application and curing of GVB to assure dimensional and voltage breakdown fidelity. Performance characteristics are maintained between -65 and 200 degrees C.

To reduce production costs on your winding operations, try Magnetics' tape wound cores with GVB. Eight material types, in a wide range of sizes from 0.375" to 4.0" inside diameter, are stocked for immediate delivery. More information? Write Magnetics Inc., Dept. ED-27, Butler, Pa.
When your specifications call for quality performance at a modest price...

PAKTRON
HI-WHITE-50 capacitors are your answer!

Do your design specifications call for low voltage capacitors that are really miniature in size, perform with consistent reliability under rugged application conditions, and are modest in price? PAKTRON HI-WHITE-50 capacitors are your answer. Demanding industrial applications have proven PAKTRON HI-WHITE-50 capacitors are the perfect subminiature, economy capacitors for circuits requiring up to 50 WVDC. Standard values through .5 mfd. available. Special PAKTRON construction techniques assure smallest size, superior moisture resistance and exacting tolerances. See for yourself, ask for full details and free samples —no obligation.

"Remember, you're never more than a few feet away from a product of ITW®"

COMPONENTS

Control transformers

[Image of control transformers]

These "Versatile" control transformers are designed for multivoltage and/or current requirements. Transformers have 117 V 50/60 Hz primary and 3 isolated 12-V secondaries, one with center tap. Four sizes are available with secondaries of 100, 150, 250 and 500 mA. Secondaries of 6, 12, 18, 20, 24 and 36 V may be obtained.

Essex Wire Corp., 3501 W. Addison St., Chicago. Phone: (312) 463-7400.

Circle No. 271

Plastic pot

[Image of plastic pot]

A conductive plastic track is integrally molded with the Model CP-36's slip rings, terminal and case for improved reliability over wire-wound pots. Linearity is better than 1% with essentially infinite resolution. Standard resistance range is 500 Ω to 100 Ω. Multi-gang cups and servo mounts can be provided.

Keltron Corp., 223 Crescent St., Waltham, Mass. Phone: (617) 894-0525.

Circle No. 272

THUMBWHEEL SWITCHES

[Image of thumbwheel switches]

Send for further information

TECH LABORATORIES, INC.
Palisades Park, New Jersey
Phone: 201 — 944-2221  ·  TWX: 201 — 947-4730
Infinite resistance at null to 1100 volts is one feature of our new solid state differential voltmeter. Accuracy to ±0.0025% of dial setting is another. Price of $1095 doesn’t hurt our cause either. Ratio measurement lets you compare two external sources with a resolution of 0.0001%. In the TVM mode, input resistance is 100 megohms. Reference regulation is 0.0002%. Stability is 13 ppm peak-to-peak per 60 days.

Of course, that’s not the whole story. Other features include 6 digit inline readout with a dial resolution of 1 ppm. The recorder output is well isolated and linear to within 0.5% of end scale. Ten percent overranging minimizes range changing and offers increased resolution on the lower end of each range. A continuously adjustable last digit allows exact null and eliminates meter interpolation for resolution.

The half-rack Model 895A weighs 16 pounds.

FLUKE • Box 7428, Seattle, Washington 98133 • Phone: (206) 776-1171 • TWX: (910) 449-2850
When you need a wide capacitance range and high performance in a small size...

PAKTRON®
MOLDED AND DIPPED
CAPACITORS OF
MYLAR
are your answer!

PAKTRON
Division Illinois Tool Works Inc.
125 Leslie Avenue - Allendale, Virginia 22502

When your capacitor needs require you to think broadly in relation to capacitance and voltage range, then PAKTRON epoxy molded (shown above) and dipped capacitors of MYLAR® film/foil are your answer. They are designed to provide the widest possible capacitance range and most useful voltage ratings in the smallest practical sizes.

The PAKTRON molded line (shown) offers values through 0.1 mfd. and voltage ratings through 200 WVDC. The dipped line (not shown) provides values through 0.5 mfd. and voltage ratings through 600 WVDC.

Unique PAKTRON construction techniques and high grade epoxy coatings insure stability under severe environmental conditions including shock and vibration.

See for yourself, ask for full details and free samples—no obligation.

*Remember, you're never more than a few feet away from a product of ITW®

MICROWAVES

Dual TR-limiter

The MA-3813Z combines a gas TR tube and a solid-state limiter to provide protection from spike leakage energy for crystal receivers and tunnel diode amplifiers. The device operates up to 16-to-17 GHz with a peak power of 40 kW max. Vswr is 1.5 max and insertion loss is 1.0 dB. Recovery time is 2 µs and spike leakage is less than 0.05 ergs/pulse.

Microwave Associates, Inc., South St., Burlington, Mass. Phone: (617) 272-3000.

Circle No. 273

Rotary joints

A new family of circular waveguide rotary joints is designed for L, S, C, X, Xb, Ku and Ka band operation.

The joints handle full waveguide power over a 7% band with a max VSWR less than 1.15 and an insertion loss less than 0.1 dB. Typical waveguide sizes used are in the category of WR770 through RG-96/U.

Diamond Antenna & Microwave Corp., 35 River St., Winchester, Mass. Phone: (617) 729-5500.

Circle No. 274

low-cost bi-directional variable speed device

Globe a.c. and d.c. differentials offer torques up to 1000 oz. in. with top speeds of from 10 to 100 rpm in either direction. Smooth transitions from top speed in one direction through 0 rpm to top speed in the opposite direction at full torque are possible. Motor options: to 115 v.d.c.; to 230 v.a.c., 50/60 and 400 cps. Antenna drives, tracking devices, positioning or servocore systems are typical applications. For further information, request Bulletin DI.

Globe Industries, Inc., 2275 Stanley Ave., Dayton, Ohio 45404. Tel.: 513 225-3741

ON READER-SERVICE CARD CIRCLE 35
Vswr bridges

Two bridges enable vswr and return loss measurement in fixed or swept set-ups. Model RB-1 covers 0.5 to 50 MHz and RB-3 covers 3 to 1500 MHz. Accuracy of the RB-1 is 1% for vswr to 1.5 and 2% at vswr of 2. Max vswr error of the RB-3 is 1.5% below 1 GHz, and 2.5% at 1.5 GHz for vswr up to 1.5. Both have a short-circuit termination and a table converting return loss to vswr.

P&A: $360; stock to 4 wks. Anzac Electronics Inc., Moody's Lane, Norwalk, Conn. Phone: (203) 838 8451.

Circle No. 275

Top wall coupler

DBH-S-879 dual top wall wave-guide coupler handles 30 kW cw with a 0.05-dB insertion loss. It is designed for water-cooled operation and pressurization to 60 psig. Forward coupling is 65 dB and reverse coupling is 35 dB. Directivity is typically 35 dB across the band. Vswr is 1.05 max in the main arm and 1.03 max in the coupling arms.


Circle No. 276

June 7, 1966
STANCOR
STANDARD TRANSFORMERS

- Rectifier
- Power
- Step-down
- Toroidal
- Filters
- Chokes
- Plate
- Transistor
- Control
- Isolation
- Filament
- Audio

STANCOR STANDARD FILAMENT TRANSFORMERS

Over 75,000 in stock ranging from 100 to 10,000 of over 100 types.

Specification Ranges
- Primary Voltage 115-230 volts 50/60 cycle and 400 cycles
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* Write for 1966 catalog and information on variations from standard transformers.

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ARNOLD/TOROIDAL COIL WINDER

sets up quickly...easy to operate...
takes wide range of wire sizes

SPECIFICATIONS:
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- Max. finished toroid O.D.: 4.0 in.
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- Core range: 9/32" I.D. to 4" O.D. to 1 1/2" high

$890.00 includes all rings, counters and accessories

LABORATORY USE
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PRODUCTION USE
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ON READER-SERVICE CARD CIRCLE 39

ARNOLD MAGNETICS CORP.
6050 W. Jefferson Blvd., Los Angeles, Calif. 90016
(213) 870-6284
ON READER-SERVICE CARD CIRCLE 38
Waveguide circulators

Waveguide circulators covering a 20.0 to 36.0 GHz range are offered in 3 models in RG-53/U and RG-96/U waveguide. Featured are 2.0 GHz bandwidths with 20 dB min isolation, 0.5 dB max insertion loss and 1.2 max vswr. The millimeter-region units are designed for duplexing and paramp systems.

They can be modified for use as low-loss terminated circulators and switches.

E & M Labs., 7419 Greenbush Ave., North Hollywood, Calif. Phone: (213) 875-1484.

Circle No. 277

X-band amplifier

Series SAX-4700 X-band cw amplifier tunes over 500 MHz and has a 1-dB bandwidth of 40 MHz. The bellows-type tuners allow remote-gang tuning. The 8-kW cw unit covers 7.9 to 8.4 GHz with a gain of 45 dB. Beam voltage is 14 kV and beam current is 1.7 A. Heater voltage is 10 V and heater current is 5 A. Solenoid power required is 1.1 kW.

Electronic Tube Div. of Sperry Rand Corp., Gainsville, Fla. Phone: (305) 372-0411.

Circle No. 278

ON READER-SERVICE CARD CIRCLE 40
Digital plotting system

The "Delta" digital incremental plotter is designed to self-program up to 100 steps from a single input command via punched cards. One command from computer or tape will also produce up to 127 incremental plotting steps. The control unit is available with IBM 360 software. Either a magnetic tape or a card reader is included.

Benson-Lehner Corp., 14761 Califa, Van Nuys, Calif. Phone: (213) 781-7100.

Circle No. 279

GENTAPE - where great ideas are generated

When Moving Miniature Parts is a Particular Problem . . . or Driving Fine Instruments Demands Dimensional Stability,

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CONVEYOR and DRIVE BELTS DELIVER REMARKABLE QUALITY and FLEXIBILITY

Highly sophisticated electronic components seemingly become more minuscule by the minute — while precision standards soar. And assembly line methods and materials must keep pace.

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ON READER-SERVICE CARD CIRCLE 41

Vhf receivers

Series 500 vhf receivers cover AM, FM and cw in the 54- to 260-MHz range and are tunable in a single band. A tape dial, a 115/230-Vac power supply and built-in BFO are standard. IF bandwidth of 10 or 300 kHz is front-panel selectable. Video bandwidth is adjustable to 1, 3, 10, 30 or 150 kHz.

P&A: $1600 to $1750; stock to 30 days. Communication Electronics Inc., 6006 Executive Blvd., Rockville, Md. Phone: (301) 933-2800.

Circle No. 280

Communications buffer

Series MCDL sequential access buffer, designed as a magnetic core delay line, has a 1386-bit capacity. The basic bit serial in/bit serial out unit contains the 1386-bit array selected on 4 coordinates by mutually prime ring counters. Each stage provides the shift and core drive function in each circuit. A bit serial in/bit parallel out model is obtained by adding a magnetic core rope card which will accommodate up to 16 parallel bits.

Di/An Controls Inc., 944 Dorchester Ave., Boston. Phone: (617) 288-7700.

Circle No. 281

ELECTRONIC DESIGN
New value-priced sweeper...

with a dc offset plug-in for your
Hewlett-Packard 3300A Function Generator

Here’s a low cost ($210) plug-in, 3304A, for the Hewlett-Packard 3300A Function Generator that provides internal sweeping, up to ±16 v of dc offset on all functions, plus sawtooth and offset square-wave outputs.

This is an instrument package you need to use on your bench...it provides more flexibility than words can describe. The new plug-in sweeps internally over a decade. The sweep starts at the main frame frequency setting and is controlled at the top end by the “Sweep Width” control. Any function can be offset with stability of ±50 mv over 24 hours. The 3304A plug-in sawtooth is independent of the 3300A main frame frequency and can be used for driving external systems. The offset square wave clamps the top or bottom of the waveform either to the dc offset voltage or to ground potential.

The plug-in provides the offset with coarse and vernier control, a 15 v sawtooth (adjustable) and the square wave over the same frequency range as the main frame. The main frame, the 3300A itself, is the first plug-in function generator to offer variable phase, phase-lock capability with accurate sine, square, triangular output, amplitude controlled, 0.01 Hz to 100 kHz. It offers linear voltage programmability, single-cycle, multi-cycle, free-run operation, external trigger capability. It provides isolated dual output amplifiers, plus a sync pulse output for oscilloscope or recorder.

All this for just $570, plus the cost of one of these plug-ins: 3301A Operational Plug-in, $20; 3302A Trigger/Phase-Lock Plug-in, $190; 3304A Offset Plug-in, $210.

Ask for a demonstration. Have your Hewlett-Packard field engineer turn it on and show you what this remarkably versatile instrument can give you. Or write for a special chart showing all the outputs available from this instrument and its plug-ins: Hewlett-Packard, Palo Alto, California 94304, Tel. (415) 326-7000; Europe: 54 Route des Acacias, Geneva.

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Cut to any size or outline with ordinary scissors
Co-Netic and Netic foils are ideal for initial laboratory or experimental evaluation... also for production applications and automated operations. Dramatically enhance component performance by stopping degradation from unpredictable magnetic fields. When grounded, foils also shield electrostatically. They are not significantly affected by dropping, vibration or shock, and do not require periodic annealing. Available in thicknesses from .002" in rolls 4", 15", and 19-3/8" wide. High attenuation to weight ratio possibilities. Every satellite and virtually all guidance devices increase reliability with Netic and Co-Netic alloys, saving valuable space, weight, time, and money.

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Consult SYNTRONIC YOKE SPECIALISTS
Syntronic's team of experts knows more about yoke design, engineering and quality control than anyone else. A solid 10-year record of leadership—acknowledged throughout the industry. Benefit from it.

SEMICONDUCTORS

Capacitance diodes
Ratings of 80 and 100 Vdc are combined with a 50-MHz Q as high as 300 in these voltage-variable capacitance diodes. The SV-4500 to SV-4800 and SV-5500 to SV-5700 series utilize an abrupt junction epitaxial structure to achieve minimum capacitance ratios of 5.6 and 6.4 for 80 and 100 V. Capacitance ranges from 10 to 47 pF at 4-V bias with 10% tolerance.
P&A: From $5.00; stock to 2 wks. Somerset Electronics Corp., P.O. Box 115, Manville, N. J. Phone: (201) 722-2340.

Pnp transistor
Typical switching time of the TW-300 pnp silicon planar transistor is better than 20 µs at a collector current of 1 µA. Switching speed stems from the small geometry epitaxial base. Collector-to-base breakdown voltage of the single emitter device is 45 V and emitter-to-base breakdown voltage is 30 V. Beta is greater than 30 at 1 µA and Vce of 0.5 V.

ystromatic INSTRUMENTS, INC.
100 Industrial Road, Addison, Illinois
Phone: Kingswood 3-6444

ON READER-SERVICE CARD CIRCLE 43

ON READER-SERVICE CARD CIRCLE 44
Fast 4-way relief for inventory headaches...mix your own!

Ordering and stocking a different connector configuration for each new product design requirement can be a real headache. If this is your problem, aspirin won’t help you. But, our pin and socket connector housings will—four ways!

These housings, available with or without die-cast aluminum shells, now accept four types of size 16 contacts—including coaxial. Which means you can bring both power and shielded circuits through the same connector in any desired combination, up to 160 positions.

All four contacts are crimp snap-in types for fast, dependable termination with AMP's matched application tools. Three types of pin and socket contacts are available; type II is a precision screw-machined contact meeting applicable requirements of MIL-C-8384, types III and III(+) are continuous strip formed contacts for high production at lowest installed cost.

For your low-level signal lines, our new subminiature COAXICON* contact is ideal. It's applied by AMP's exclusive single-stroke crimping technique which simultaneously crimps center conductor, braid and cable support. And, it snaps into any connector cavity just like the pin and socket contacts.

So take a tip, not a pill. Minimize your inventory problems by selecting A-MP* Series M, D, DD, W or WW connector housings in the materials and sizes you'll need. Then, mix your own contacts in whatever configuration the application calls for. Sound good? Write today and get the complete story.

*Trademark of AMP INCORPORATED
Silicon rectifiers

Series R plug-in silicon rectifiers are available in full- and half-wave, doubler, center-tap, open-bridge and three-phase types.

The octal socket devices contain double diffused passivated controlled avalanche junctions in a cold case configuration. Voltage ratings range from 50 to 5,000 V PIV and currents from 100 mA to 6 A.

Edal Industries, Inc., 4 Short Beach Rd., East Haven, Conn.

Phone: (203) 467-2591.

Circle No. 284

Another First by Weston-Rotek: Wideband True RMS To DC Converter

True RMS to 0.05% from your DVM

For use with existing DVM’s and other precision dc measuring instruments, this unit provides accurate, reliable and automatic true rms measurements of sinusoidals, square, triangular and other complex wave forms. It eliminates errors caused by distortion in "average computing" precision voltmeters.

Features: .05% Accuracy .01% Repeatability 30 mv to 1000 volts full scale 10 Hz to 100 KHz

10 volts dc full scale output 7.1 Crest Factor All Silicon Guarded and Isolated

Ask for Bulletin 840. Weston Instruments, Inc., Rotek Division, 11 Galen Street, Watertown, Mass. 02172

WESTON® prime source for precision...since 1888

High-gain amplifier

The TQN-0030 transistor amplifier operates at 30 MHz with a 2-MHz bandwidth and provides a gain of 100 dB min. Actual noise figure is 2.5 dB max and quiescent current drain is 6 V at 1.5 mA. The gain characteristic has a stability of ±3 dB at an operating range of -25 to +75°C. Input and output both have 50-Ω connectors.

Micro State Electronics Corp., 152 Floral Ave., Murray Hill, N. J.

Phone: (201) 464-3000.

Circle No. 285

Switching diode

A subminiature high-speed germanium gold alloy junction switching diode is 1/3 the volume of comparable units. The hermetically sealed diode is 0.07-in. in diameter and 0.16-in. in length. Characteristics include a 100-ps reverse recovery time with resistance load of 120 Ω. PIV is 1 V to -25 V at 50 μA. Forward current is over 200 mA at 1 V.

P&A: $0.85; 2 wks. International Diode Corp., 90 Forrest St., Jersey City, N. J. Phone: (201) 432-7151.

Circle No. 286

SCRs

Series 22RC of SCRs has PRV ratings from 25 to 700 V (800 V non-repetitive transient rated). Maximum current rating is 22.3 A average, 25 A rms at up to 70°C case temperature. Junction temperature capability is 150°C. The SCRs are packaged in TO-48 cans.

P&A: $10.80 to $51 (1 to 9); 2 to 4 wks. International Rectifier, 238 Kansas St., El Segundo, Calif.

Phone: (213) 678-6281.

Circle No. 287

ON READER-SERVICE CARD CIRCLE 46
RFI/EMI TEST, MEASUREMENT, ANALYSIS, & CONTROL SERVICES

F&M SYSTEMS CO. can dispatch an RFI/EMI Test Crew to your job-site within 24 hours to provide...

- MILITARY SPECIFICATION TESTING
- INTEGRITY MEASUREMENTS OF SHIELDED ENCLOSURES
- RFI/EMI SOURCE LOCATION
- INTEGRITY MEASUREMENTS OF SECURE COMMUNICATIONS SYSTEMS
- GENERAL RFI/EMI CONSULTATION SERVICES

Measurements and analyses over the RF spectrum of 30 cps to 15 gc are accomplished by F&M... even under the most undesirable field conditions. Write for F&M Systems' RFI/EMI Services Brochure.

F&M SYSTEMS CO.

Impulse noise counter

Noise pulses on voice frequency circuits are counted by the 74258-A impulse noise counter. An internal register records all pulses exceeding a pre-set amplitude between 0 and -60 dB. Counting time can be adjusted in steps up to 60 minutes with max count of 9999. All pulses separated by 125 ms or more will record. The instrument operates from dry cells or a dc supply.


Circle No. 288

Resistance test set

Model UTS-4 takes low-range dc resistance measurements from 2-terminal or complex multi-position devices. A constant 10 or 100 mA passes through the test resistance and the drop is measured with a high-resistance volt-meter. Constant currents are regulated at ±1% over an ambient temperature range of 60° to 90°F.

Sparton Corp., P.O. Box 1784, Albuquerque, N. M. Phone: (505) 898-1150.

Circle No. 289
Need a small 1, 5 or 10 mc crystal oscillator for use in synthesizers, timing systems, counters, communication systems, time-code generators, tape systems, or some other small black box? Think TRACOR®

Shown half actual size

Volume 19.1 in³
That’s about the size of it.

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TRACOR, Inc.
General Sales Office
6500 Tracor Lane
Austin, Texas 78721
Phone: 512-926-2800

TEST EQUIPMENT

Coulometer

The VT-1176 coulometer incorporates a 6-digit counter with manual reset and a front-panel switch. Any of four scales may be dialed with the last counter digit set to read in 0.01, 0.1, 1.0 or 10 coulombs. Accuracy of 1% ± one digit is not affected by over-ranging up to 250%. The coulometer can be adapted with appropriate transducer to measure any physical parameter which can be converted to a varying voltage.


Circle No. 290

Resistance system

A guarded Wheatstone resistance measuring system, model 232, has ±0.01% accuracy. The 5-dial bridge has 6-place resolution on 11 ranges from 1.2 Ω to 12,000 MΩ full scale. The null-detector has 5-μV sensitivity, 70-dB ac rejection and 5-s overload recovery. The completely guarded generator provides up to 200 V at a variable 0 to 1 W.

P&A: $1045; stock to 30 days. Electro Scientific Industries, 13900 N.W. Science Park Dr., Portland, Ore. Phone: (503) 646-4141.

Circle No. 291

Substrate Backed Thinistors are available with a standard film thickness of 5 microns on substrates of nickel foil, silicon, barium titanate, beryllium oxide, quartz and commercial alumina.

Free Standing Thinistors have a standard thickness of 20 microns for lead mounting.

Low heat capacity
Fast thermal response
Large surface-to-mass ratio
Sensitivity plus accuracy
Standard negative temperature coefficients of resistance

Thinistor® Applications

- Microcircuity — Micromodule, integrated circuitry and circuit chip techniques. Temperature compensation of semiconductor devices, microminiature temperature control, monitoring and telemetry.
- Pulse Circuitry — Fast response and high thermal sensitivity make Thinistors suitable for pulse circuitry and in computer elements such as multipliers, amplifiers, oscillators, flip-flops, triggers, counters and shift registers.
- Gas Chromatography — Low heat capacity of Thinistors permits small volume samples. Rapid response provides detector possibilities previously unobtainable.
- Micro Calorimetry — Thermal energy determinations where minute amounts of heat are involved and low heat capacity and fast, thermal response are essential.
- Vacuum Manometry — High surface-to-mass ratio, low heat capacity and high sensitivity permit extension of thermal conductivity techniques for low pressure gas measurements.
- Medical Electronics — Low heat capacity, rapid response, high dissipation constant and extreme sensitivity for temperature and flow measurements. Clinical thermometers, thermal dilution studies and area skin temperature measurements.
- Infrared Energy Detection—Thinistors permit an intimate bond by sintering the thermistor film directly to its substrate eliminating distortion and attenuation properties of cement normally used in bonding.

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We are looking for EE's, ME's and Physicists for design consultation or systems development in ASW and underwater problems—experience in penetrations aids—design studies, tests, analysis and reports on electronic and mechanical systems. Your reply strictly confidential.

ON READER-SERVICE CARD CIRCLE 48
New standard of performance

- Contact resistance 0.0015 ohm—change less than 0.001 after life, rotational and salt spray tests
- Low thermal emf
- Space-age performance...designed to MIL specs

The low contact resistance means you can even switch microvolts without signal loss. So if you have a problem of switching at low signal levels, this is the answer. There's no other switch in this class.

Low stable contact resistance and long life go side by side in the design. Switch body constructed of diallyl phthalate. Gold-plated terminals. Solid silver alloy brush and contact design. Stainless steel hardware. Unique hex shaft for multiple positioning of brushes.

The switches have long life under difficult conditions of temperature, vibration and shock—100,000 rotations minimum at 125°C. Exceptionally low thermal emf—less than 0.01 microvolt per degree C change in temperature.

They're a smooth-acting series of switches, available in a large number of configurations to meet your exact needs. We build them with 1 to 6 poles, each pole with 2 or 3 terminals (11 active, 1 collector). Price: moderate for a switch of this quality.

To learn more about the switches and how to order, write or call Components Division, Leeds & Northrup, North Wales, Pa. 19454. (215) 699-5353.

Application Notes

Current integration

"Using the Electrometer Voltmeter as a Current Integrating or Charge Measuring Instrument" is a 6-page application note discussing circuit considerations for these measurements. This technique is used in measurements of capacitors, random current pulses, laser energy, ion and electron beams, current integration as a timing device and dosimetry. Circuits and equations involved in these applications are given. Two techniques are described. Keithley Instruments.

Circle No. 292

AF amplifier design

A 12-page application note details design of a 4-output AF amplifier. The circuit uses germanium transistors in the push-pull output stage and silicon transistors in the input and driver stages. Complete with schematics, charts and graphs, the bulletin gives power supply data and a modified battery-operated circuit. American Elite Inc.

Circle No. 293

Generator calibration

A 12-page application note 19 describes the IF substitution technique for attenuation measurement as applied to calibration of signal generator controls. Procedures for calibration of cw and pulse-modulated signal generators to accuracies of ±1 dBm are given. The method of measurement, operation of the manufacturer's substitution receiver, error summation and equipment necessary to perform the measurement in a 10 to -110 dBm range are discussed. PRD Electronics.

Circle No. 294

SCR motor controls

Open-chassis SCR motor controls for OEM applications are described in a 4-page bulletin. Complete specifications, configurations and dimensions are given. Also included are typical performance curves and a list of standard and optional accessories. Gerald K. Heller Co.

Circle No. 295

this is not a CAPACITOR!

It is available with all these standard specs:

- 0.01% Tolerance
- 0 ± 1 ppm/°C TC (0°C to +60°C)
- Guaranteed 25 ppm shelf life stability for one year
- Non-measurable noise
- Non-measurable inductance

With these standard specs, it could only be a Vishay Precision Resistor...not wire-wound...not conventional evaporated metal film...but a totally new kind of resistor, with a totally new order of precision!

How it got its unique small size...how it delivers its unequalled performance without parameter trade-offs...and where it can solve problems for you, are explained in this new, complete DATA CATALOG. Send for your free copy today.
What is really important when evaluating crystal frequency standards?

How much can you find out from aging-rate data?

New Literature

Stator winder

A two-page, two-color bulletin describes the Link-Possis automatic stator winder, model ST-W-4B. Model ST-W-4B winds one to five coils per pole for 2, 4, or 6 pole stators. It winds 2, 3, or 4 poles simultaneously. Possis Machine Corp.

Circle No. 296

Spark gaps

A 20-page brochure discusses almost 300 types of 2-electrode and triggered spark gaps. Drawings, curves and application notes aid in presenting operation and performance data on both types. Catalog information and mounting methods are given. Signalite Inc.

Circle No. 297

Force transducer

Major features, specifications and typical applications of the “micro-probe” are detailed in a new data sheet. The transducer is fully described as a force probe comprising a rigid mechanical system acting on a pair of semiconductor strain gages. Endevco Corp.

Circle No. 298

Infrared detector

A 2-page bulletin covers the manufacturer’s infrared detector. Test conditions, electrical characteristics, application notes, dimensions and a general description are included. Borg-Warner.

Circle No. 299

HIGH-VOLTAGE CABLE INSULATED WITH G-E SILICONE RUBBER SURVIVES 3 TOUGH CONDITIONS

One, it carries high currents at 7,000 volts rms, in an airborne power supply.

Two, it withstands temperatures that continuously fluctuate between —55°C and 125°C.

Three, it operates at altitudes as high as 70,000 feet.

Of all the cable insulating materials checked by the manufacturer, G-E silicone rubber proved reliable under every condition. Resisting ozone and corona, it extended cable life to at least 1,000 hours.

Saves $2.00 per unit

By using silicone rubber as both a dielectric and a potting sealant for a standard connector, the manufacturer got a bonus — a void-free, all silicone system with a minimum of labor.

Compared to installation of individual insulating sleeves for connector contacts, cost savings amounted to $2.00 a unit. And reliability was improved.

FREE NEW DATA BOOK

For more ways on how G-E silicone rubber can save money, get Technical Data Book CDS-592, a comprehensive 36-page guide to high performance wire and cable.

Test equipment

A review of scientific and test instruments is the subject of catalog PD-602. The 20-page brochure concentrates on strain gage devices and high-speed strip chart recorders. The catalog encompasses photographs of each unit, as well as drawings, specifications, dimensions and graphs. Baldwin-Lima-Hamilton Corp.  

Circle No. 300

Photo-sensitive FET

A 12-page brochure explains the operation of the photo-sensitive FET. Examples of circuits using this FET as a light amplifier and as a switch are provided. Applications described include chopper, modulator, multiplexer, servo, relay, logical and computer uses. Performance data for these and other applications are supplied. Teledyne Inc, Crystalonics Div.  

Circle No. 301

Photo choppers

Bulletin PC-118 provides information on ac and dc drive photo choppers. Internal schematics and a typical application of a photo chopper in a modulator circuit are provided. The bulletin includes scope traces of output waveforms and dimensional drawings. Airpax Electronics Inc.  

Circle No. 302

Tunnel diode amplifiers

Low noise figure tunnel-diode amplifiers are listed with principal specifications in a short form catalog folder. The two-color brochure lists 68 standard models ranging from 250 MHz to 20 GHz in the uhf, L, S, C, X and K bands. Micro State Electronics.  

Circle No. 303

Potentiometer catalog

The 60-page potentiometer catalog P-3 provides detailed engineering information and includes 15 different resistance curves, nomenclature, test methods, attenuator circuits and attenuator calculations. Quick reference charts list seven basic pots from 0.1 to 4 W. Centra-lab, Div. of Globe-Union Inc.  

Circle No. 304

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AUGAT INC.

31 PERRY AVE., ATTLEBORO, MASS. 02703

Low Cost • High Performance • Wide Application

14 CONTACT DUAL-IN-LINE SOCKET

for testing and packaging Monolithic Circuits

Accepts packages with flat or round leads. Has large contoured entry holes for easy insertion. Gentle wiping action of spring contacts prevent lead damage.

Can be used in panel mount or printed circuit applications. Wire-wrap® termination also available.

Fast, easy, damage-free release of circuit. Unique ejector key, as integral part, speeds up testing programs. Simple extractor tool facilitates handling in close packaging production.

Unit has molded diallyl pythalate body with gold-plated, beryllium copper contacts. Dimensions: .8L x .5W x .37H. 16 and 20 DIL sockets under development.

Write for Data Sheet 166.

© Trade Mark Gardner-Denver Co.

ON READER-SERVICE CARD CIRCLE 54

---

Unregulated—Series "S"  
Output: 1 KV to 50 KV up to 10 MA DC  
Input: 115 V, AC—60/400 cycles  
From Stock

Regulated—Series "TRHV"  
Output: 1 KV to 30 KV up to 5 MA DC  
Input: 115 V, AC—60/400 cycles  
From Stock

Both Del models represent the ultimate in quality. Their design and manufacture incorporate only the finest materials and workmanship to assure reliable and dependable service.

Applications

- Missile Ground Support Equipment  
- Electron Optical Devices  
- Radar Displays  
- Nuclear Applications  
- Image Intensifiers  
- CRT Applications  
- Electrostatic Precipitation Equipment

Other instrumented power supplies available up to 250 KV ratings.

WRITE FOR DATA

DELELECTRONICS CORPORATION

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ON READER-SERVICE CARD CIRCLE 55

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America's Foremost COMPACT HIGH VOLTAGE POWER SUPPLIES

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NEW LITERATURE

Fluidics

An explanation of fluidics technology is given in a new 12-page folder. In an applications section, the illustrated pamphlet cites such typical fluidic functions as binary logic, computing, counting, timing, digital switching, amplification, information handling, sensing and other control techniques. A page of symbols for use by designers is also contained.

Devices covered include flip-flops, an OR/NOR gate, an AND gate, single and staged binary counters, proportional amplifiers, a pneumatic timer and fluid resistors. A language section includes 39 definitions of terms, including functions, uses and operating principles of various types of fluid amplifiers. Corning Glass Works.

Circle No. 305

Vibratory systems

"How to Analyze Vibratory Systems" is the title of the 4-page, 2-color design monograph No. 5. Essential information to classify and analyze linear single-degree-of-freedom system vibrations is given. Formulas with definitions, charts and graphs are used. Lord Corp.

Circle No. 306

Design Data from

New Continuous Plating Saves 40%

New continuous reel-to-reel precious metal plating (gold, silver, Rhodium, etc.) on strip reduces materials cost up to 40%. Provides extremely accurate depositing to specifications, allows selection of plated area (i.e.: 20 microinches one side, 100 microinches on opposite side).

Process also permits plating of pre-die cut integrated circuits for semiconductors, etc., and allows forming after plating.

Burton Research Laboratories, Inc.
Division of Burton Silverplating Co.

circle No. 171

Reliable Fastener Seals Described

Parker Seal Company's Fastener Seals are well-known for their superior reliability in mechanical sealing. These famed "seal-for-sure" designs are described in a new catalog-handbook containing sizes, dimensions, engineering data, etc. It includes their new Thredseal, an extremely reliable, easy-to-use seal for sealing directly against threads as well as information on Stat-O-Seals, Lock-O-Seals, etc.

Parker Seal Company
10567 Jefferson Blvd.
Culver City, California 90230

Circle No. 173

Rotary Power Tap Switches

Bulletin DS-158 describes the Model 10 Rotary Power Tap Switches, which are available in three types to meet a variety of switching requirements. The Model 10 Tap Switches incorporate a break before make, non-shorting switching movement, with switching positions indexed at 30°. The switches are "Underwriters' Laboratories, Inc. Listed." The bulletin fully describes these three different types and furnishes all specifications, dimensions, ratings, and prices.

Curtis Development & Mfg. Co.
3236 North 33rd Street
Milwaukee, Wisconsin 53216

Circle No. 175
White Noise Loading Tests Systems

A new paper entitled "White Noise Loading of multi-channel Communications Systems" by W. Oliver is available from Marconi Instruments. Under a number of general headings, the paper deals with Noise Power Ratio measurement, measurement of intermodulation distortion and crosstalk together with other noise and distortion criteria of multi-channel systems.

MARCONI INSTRUMENTS
111 Cedar Lane
Englewood, New Jersey

The Multireed Relay

This highly explicit pamphlet provides full technical data on Thermosen's Multireed Relay. The Multireed Type A has 4 to 8 contact groups controlled by a single coil; Type B has up to 3 independent DPDT relays; and Type C has up to 5 SPDT relays. True Form C switching and individual contact adjustment make this unit adaptable in a wide variety of applications. For further information write:

Thermosen, Inc.
375 Fairfield Avenue
Stamford, Conn.

Complete Line of Non-Linear Resistors

This 110-page engineering guide covers the most complete line of heat-, light-, and voltage-sensitive resistors available today. Prepared by the Components Division of Amperex, the book describes physical principles, measurement techniques and the electrical and mechanical properties of the non-linear resistors in the Amperex line. These fall into four broad categories: negative temperature-coefficient thermistors, positive temperature-coefficient thermistors, voltage-dependent resistors, and light-dependent resistors. Applications data, including circuits and selection criteria, are given for each type.

Amperex Electronic Corporation
Components Division
Hicksville, Long Island, N. Y. 11802

June 7, 1966
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DIALCO Switches and Indicator Lights provide almost limitless applications—are flexible in arrangement—economical in price—and feature high reliability. Switches are the silent, momentary type—requiring 24 oz. (approx.) operating force. Contact arrangements are: S.P.S.T., normally open or normally closed; S.P.D.T., two circuit (one normally open, one normally closed). Ratings: 3 amps, 125V A.C.; 3 amps, 30V D.C. (non-inductive). The switch is completely enclosed and independent of the lamp circuit. The light source is the T-1 3/4 incandescent lamp, available in voltages from 1.35 to 28V. Units are made for single hole (keyed) mounting in panels up to 3/16" thick and mount from back of panel in 1/2" clearance hole. Switch forms for dry circuits are also available. Other features include: 1/2" or 3/4" interchangeable caps, round or square, rotatable or non-rotatable, in a choice of 7 color combinations.

For complete data, request current Catalog.

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- For bonding when a thermally conductive interface is required.

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139 FOUNDry ST. / WAKEFIELD, MASS. (617) 245 5900 - TWX 617 245 9213

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<td>0.5</td>
<td>±1%</td>
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**CONVENIENT TABULAR FORMAT PROVIDES QUICK AND EASY MODEL-TO-MODEL COMPARISONS**

One look at the specimen pages will show you—better than words—the extent of the information furnished by the DIRECTORY OF TECHNICAL SPECIFICATIONS and the comparative arrangement of the data. These convenient tables are designed for rapid and accurate point-by-point comparison of instruments having similar functional capabilities. By providing a thorough across-the-market analysis, all alternatives can be considered in selecting the right instrument for any application.

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Designer's Datebook

June 13-14

June 15-17

June 15-17
1966 IEEE Solid-State Device Research Conference (Evanston, Ill.) Sponsor: IEEE; Dr. B. J. Rothlein, National Semiconductor Corp., P. O. Box 443, Danbury, Conn.

June 15-20
XIIIth International Scientific Congress on Electronics (Rome, Italy); Secretariat of the Congress, Via Crescenzio 9, Rome, Italy.

June 21-23
Conference on Precision Electromagnetic Measurements (Boulder, Colo.) Sponsor: Institute for Basic Standards of the National Bureau of Standards; John Cronland, University of Colorado, 328 University Memorial Center, Boulder, Colo.

June 27-29
2nd Annual Conference & Exhibit on "Exploiting the Ocean" (Wash., D. C.) Sponsor: Dr. James H. Wakelin Jr., Scientific Engineering Institute, Waltham, Mass.

July 6-9
Annual Meeting of the National Society of Professional Engineers (Minneapolis, Minnesota) Sponsor: National Society of Professional Engineers; Kenneth E. Trombley, 2029 K Street, N.W., Washington, D.C.
PROBLEM-SHRINKERS - THE APPLICATION-ASSISTANCE PEOPLE AT RCA - WENT ON TO DEVELOP AND PROVE NEW VERTICAL DEFLECTION CIRCUITS, MADE POSSIBLE BY THE A40607C, FOR BOTH LOW B+ AND HIGH B+ OPERATION.

THE CASE OF VERTICAL DEFLECTION. IT WAS A MAJOR PROBLEM - UNTIL THE PROBLEM-SHRINKERS AT RCA DESIGNED THE A40607C. THIS NEW DEVELOPMENTAL BEAM POWER TUBE HAS AN INTEGRAL DIODE, AND WITH ITS RECOMMENDED CIRCUITRY PROVIDES A MINIMUM OF INTERACTION BETWEEN THE HEIGHT AND LINEARITY CONTROLS IN THE VERTICAL DEFLECTION CIRCUIT.

THE PROBLEM VIRTUALLY EVAPORATED WITH THE A40607C IN A FEEDBACK-STABILIZED VERTICAL-OUTPUT CIRCUIT DEVELOPED BY RCA APPLICATION ENGINEERS. THIS CIRCUIT CAN DELIVER PERFORMANCE FEATURES NOT FOUND IN CONVENTIONAL CIRCUITS USING MORE COMPONENTS:

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- OUTPUT STAGE DEVELOPS ITS OWN BIAS
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Now—a complete line of heat handlers...

Dale thru-chassis PH wirewounds

When you see design advantages in mounting through the chassis—specify Dale PH Housed Precision Wirewounds. They handle more power—up to 100 watts—and give you exceptional stability plus other important design features.

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Dale's expanded PH line includes the new PH-10-5 and PH-25-8, both non-inductively wound and designed to ground the housing to the chassis. This combination provides both reduced assembly time and faster cycle time. It makes these PH models extremely valuable in high speed core memory systems where they are used as drive line resistors in modules of up to 256 units.

NAME YOUR SPECIAL

Built to the electrical and environmental requirements of MIL-R-18546, PH Wirewounds can be quickly adapted to your special requirements. Special terminals, resistances, tolerances and matching are only a few of the modifications readily available.

For complete information on thru-chassis and chassis-mounted housed resistors, write for Catalog A or call Dale: 564-3131, Area Code 402.

DALE ELECTRONICS, INC.
1328 28th Avenue, Columbus, Nebraska
In Canada: Dale Electronics, Canada, Ltd.

PH RESISTOR SPECIFICATIONS

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<td>10</td>
<td>1 to 12.7K .1 to 47.1K</td>
<td>4x6x2x.040 aluminum chassis</td>
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<tr>
<td>PH-10-5</td>
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<td>5 to 6.3K .1 to 23.5K</td>
<td>5x7x2x.040 aluminum chassis</td>
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<td>PH-25</td>
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<td>5 to 25.7K .1 to 95.2K</td>
<td>aluminum panel</td>
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<td>PH-25-8</td>
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<td>50</td>
<td>3 to 52K .1 to 75K</td>
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<tr>
<td>PH-100</td>
<td>100</td>
<td>5 to 35K .1 to 50K</td>
<td></td>
</tr>
</tbody>
</table>

Temperature Coefficient: ±50 PPM, ±30 PPM, ±20 PPM, depending on value and tolerance
Operating Temperature: -55°C to +275°C
Construction: Extruded aluminum housing with steatite or alumina core and tinned copperweld or 180 alloy terminals, depending on physical size. Covered by U.S. Patents.

*Power Rating based on 275°C max. internal hotspot temperature with resistor mounted on standard heat sink.