

## XILINX ALLIANCE-EDA COMPANIES &amp; PRODUCTS - FEBRUARY 1997 - 1 OF 2

	COMPANY NAME	PRODUCT NAME	VERSION	FUNCTION	DESIGNKIT	3K/	XC	CPLD	UNI	PLATFORMS			
						4K	5200	7K9K	LIB	PC	SUN	RS6000	HP7
DIAMOND	Aldec	Active-CAD	2.2	Schematic Entry, State Machine & HDL Editor, FPGA Synthesis & Simulation	Included	✓	✓	✓	✓	✓			
	Cadence	Verilog	2.4	Simulation	Xilinx Front End	✓	✓	7k	✓		✓	✓	✓
		Concept	2.1	Schematic Entry	Xilinx Front End	✓		7k	✓		✓	✓	✓
		FPGA Designer	9504	Topdown FPGA Synthesis	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		Synergy	2.3	FPGA Synthesis	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		Composer	4.4	Schematic Entry	Xilinx Front End	✓		7k	✓		✓	✓	✓
	Mentor Graphics	Autologic	A.3-B.1	Synthesis	Xilinx Synthesis Lib.	✓	✓	7k	✓		✓	✓	✓
		Design Architect	B.x	Schematic Entry	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		QuickSim II	B.x	Simulation	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		QuickHDL	B.x	Simulation	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		Galileo	3.2.5	Synthesis	Call Mentor	✓	✓	✓	✓		✓	✓	✓
		Leonardo	4.0.1	Synthesis	Call Mentor	✓	✓	✓	✓		✓	✓	✓
	OrCAD	Capture (Win)	7.0	Schematic Entry	Call OrCAD	✓	✓	✓	✓	✓			
		Simulate (Win)	6.10	Simulation	Call OrCAD	✓	✓	✓	✓	✓			
		VST 386+ (DOS)	1.2	Simulation	Call OrCAD	✓			✓	✓			
		SDT 386+ (DOS)	1.2	Schematic Entry	Call OrCAD	✓			✓	✓			
		PLD 386+ (DOS)	2.0	Synthesis	Call OrCAD	✓			✓	✓			
	Synario Design Automation	ABEL	6.3	Synthesis, Simulation	ABEL-XCPLD			✓	✓	✓			
		Synario	2.3	Schematic Entry, Synthesis & Simulation	SYRO-LCA, SYRO-XCPLD	✓	✓	✓	✓	✓			
	Synopsys	FPGA Express	1.0	Synthesis	Call Synopsis	✓	✓	TBD		✓			
		FPGA Compiler	3.5	Synthesis	Call Xilinx	✓	✓	✓	✓		✓	✓	✓
		VSS	3.5	Simulation	Call Xilinx	✓	✓	✓	✓		✓	✓	✓
		Design Compiler	3.5	Synthesis	Call Xilinx	✓	✓	✓	✓		✓	✓	✓
	Viewlogic	WorkView Office	7.1.2/7.2	Schem/Sim/Synth	Call Xilinx	✓	✓	✓	✓	✓	✓	✓	✓
		ProSynthesis	5.02	Synthesis	Call Xilinx	✓	✓	7k	✓	✓	✓	✓	✓
		ProSim	6.1	Simulation, Timing Analysis	Call Xilinx	✓	✓	7k	✓	✓	✓	✓	✓
		ProCapture	6.1	Schematic Entry	Call Xilinx	✓	✓	7k	✓	✓	✓	✓	✓
		PowerView	6.0	Schem/Sim/Synth/Timing Analysis	Call Xilinx	✓	✓	✓	✓	✓	✓	✓	✓
RUBY	Capilano Computing	Design Works	3.1	Schematic Entry/Sim	XD-1	✓			✓	✓			
	Compass Design Automation	ASIC Navigator		Schematic Entry	Xilinx Design Kit	✓	✓	7k			✓		✓
		X-Syn		Synthesis		✓					✓		✓
		QSim		Simulation		✓	✓	7k			✓		✓
	Escalade	DesignBook	2.0	Design Entry		✓			✓	✓	✓		
	Exemplar Logic	Galileo	3.2	Synthesis/Timing Analysis Simulation	Included	✓	✓	✓	✓	✓	✓		✓
		Leonardo	4.0.1WS / 4.02PC	Synthesis	Included	✓	✓	✓	✓	✓	✓		✓
	IK Technology Co.	ISHIZUE PROFESSIONALS	1.06	Schematic Entry/Simulation	Xilinx Design Kit	✓	4Q			✓	✓		✓
	IKOS Systems	Voyager	2.31	Simulation	Xilinx Tool Kit	✓	✓				✓		✓
		Gemini	1.21	Simulation	Xilinx Tool Kit	✓	✓				✓		✓
	INCASES Engineering GmbH	Theda	4.1	Design Entry	Xilinx Kit	✓							✓
	ISDATA	LOG/iC Classic	4.2	Synthesis	LCA-PP	✓		7k	✓	✓	✓		✓
		LOG/iC2	4.2	Synthesis Simulation	Xilinx Mapper	✓	✓	7k	✓	✓	✓		✓
	Logic Modeling Corp. (Synopsis Division)	Smart Model		Simulation Models	In Smart Model Lib.	✓	✓	7k,9k		✓	✓	✓	✓
		LM1200		Hardware Modeler	Xilinx Logic Module	✓		7k,9k		✓	✓	✓	✓
	Model Technology	V-System/VHDL	4.4j (PC) / 4.6a (WS)	Simulation		✓			✓	✓	✓	✓	✓
	Protel Technology	Advanced Schematic	3.2	Schematic Entry	Included	✓	✓	7k	✓	✓			
		Advanced PLD	3	PLD/FPGA Design & Simulation	Included	✓	✓	7k		✓			
	Quad Design Technology	Motive	4.3	Timing Analysis	XNF2MTV	✓				✓	✓	✓	✓
	SimuCad	Silos III	96.1	Schematic Entry & Simulation	Included	✓	✓		✓	✓			
	Sophia Sys & Tech	Vanguard	5.31	Schematic Entry	Xilinx I/F Kit	✓		✓		✓	✓		✓
	Summit Design Corp.	Visual HDL	3.0	Graphical Design Entry/Simulation/Debug	EDIF Interface	✓	✓	✓	✓	✓	✓	✓	✓
	Synplicity, Inc.	Synplify-Lite	2.6b	Synthesis	Xilinx Mapper	✓	✓	9k	✓	✓	✓		✓
		Synplify	2.6b	Synthesis	included	✓	✓	9k	✓	✓	✓		✓
	TopDown Design Solutions	V-BAK	1.1	XNF to VHDL translator	XNF interface	✓	✓		✓	✓	✓	✓	

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	COMPANY NAME	PRODUCT NAME	VERSION	FUNCTION	DESIGNKIT	2k/3k/	XC	CPLD	UNI	PLATFORMS			
						4k	5200	7k9k	LIB	PC	SUN	RS6000	HP7
RUBY	TopDown Design Solutions	V-BAK	1.1	XNF to VHDL translator	XNF interface	✓	✓		✓	✓	✓	✓	
	VEDA DESIGN AUTOMATION INC	Vulcan	4.5	Simulation	XILINX Tool Kit	✓					✓		✓
	Veribest	Veribest VHDL	14.0	Schematic Entry	Xilinx FPGA Design Kit	3k,4k			✓	✓	✓		✓
		Veribest Verilog	14.0	Simulation	Xilinx FPGA Design Kit	3k,4k			✓	✓	✓		✓
		VeriBest Simulator	14.0	Simulation	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
		DMM	14.x	Design Management	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
		VeriBest Synthesis	14.0	Synthesis	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
		Synovation	12.2	Synthesis	Xilinx FPGA Design Kit	3k,4k			✓	✓	✓		✓
		PLDSyn	12.0	Design Entry Synthesis		✓		7k		✓	✓		✓
		VerBest Design Capture	14.x	Design Capture	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
	Accolade Design Automation	Peak VHDL	2.21	Simulation	Xilinx Plus	✓	✓			✓			
		Peak FPGA	2.20	Synthesis	Included	✓	✓	✓		✓			
EMERALD	ACEO Technology, Inc.	Asyn	4.1	Synthesis	Included	✓	✓		✓	✓	✓	✓	✓
		Softwire	3.3	Multi-FPGA Partitioning	Included	✓	✓		✓	✓	✓	✓	✓
		Gatean	3.3	ASIC to FPGA Netlist Mapping	Included	✓	✓		✓	✓	✓	✓	✓
	Acugen Software, Inc.	Sharpeye	2.60	Testability Analysis	AALCA interface	✓	✓	7k		✓	✓		✓
		ATGEN	2.60	Automatic Test Generation	AALCA interface	✓	✓	7k		✓	✓		✓
		AAF-SIM	2.60	Fault Simulation	AALCA interface	✓	✓	7k		✓	✓		✓
		PROGBSDL	2.63	BSDL Customization	AALCA interface	✓	✓	7k		✓	✓		✓
		TESTBSDL	2.63	Boundary Scan ATG	AALCA interface	✓	✓	7k		✓	✓		✓
	ALPS LSI Technologies	Edway Design Systems		Synthesis/Simulation		✓		✓		✓			
	Aptix Corporation	System Explorer	3.1	System Emulation	Axess 3.1	✓	✓	✓			✓		✓
		ASIC Explorer	2.3	ASIC Emulation	Axess 2.3	4K	✓		✓				
	Aster Ingenierie S.A.	XILLAS	4.2	LASAR model generation	Worst Case Simulation	✓		7k		✓	✓		✓
	Auspy Development Co.	APS	1.3.3	Multi-FPGA Partitioning	Included	✓	✓			✓	✓		
	Chronology Corporation	TimingDesigner	3.0	Timing Specification and Analysis	Included	✓	✓	7k,9k	✓	✓	✓		✓
		QuickBench	1.0	Visual Test Bench Generator	Included	✓	✓	7k,9k	✓	✓	✓		✓
	CINA-Computer Integrated Network Analysis	SmartViewer	1.0e	Schematic Generation	XNF Interface	✓		7k		✓			
	Epsilon Design Systems	Logic Compressor		Synthesis optimization		✓	✓			✓	✓	✓	✓
	Flynn Systems	Probe	3.0	Testability Analysis	Xilinx Kit	✓	✓	7k		✓			
		FS-ATG	3.0	Test Vector Generation	Xilinx Kit	✓	✓	7k		✓			
		CKTSIM	3.0	Logic Analysis	Xilinx Kit	✓	✓	7k		✓			
		FS-SIM	3.0	Simulation	Xilinx Kit	✓	✓	7k		✓			
	Fujitsu LSI	PROVERD		Top-Down Design System	Included	3k,4k				✓			
	Harmonix Corporation	PARTHENON	2.3	Synthesis		4k		7k		✓	✓		
	Logical Devices	Total Designer	4.7	Simulation & Synthesis	Call Xilinx	✓	✓	✓	✓	✓			
		Ulysa	1.0	VHDL Synthesis	Call Xilinx	✓	✓	✓	✓	✓			
	MINC	PLDesigner-XL/PL-Synthesizer	3.3/3.2.2	Synthesis	Xilinx Design Module	✓				✓	✓		✓
	Teradyne	Lasar	6	Simulation	Xilinx I/F Kit	✓					✓		✓
	Tokyo Electron Limited	ViewCAD	1.2	FLDL to XNF translator	XNFGEN	✓							
	Trans EDA Limited	TransPRO	1.2	Synthesis	Xilinx Library	✓					✓		✓
	Visual Software Solutions	Statecad	3.0	Grph. Design Entry, Sim., Debug		✓	✓	✓	✓	✓	✓		✓
	Zuken	Tsutsuji		Synthesis/Simulation	XNF Interface	3k,4k					✓	✓	✓
	Zycad	Paradigm RP		Rapid Prototyping		✓					✓		✓
		Paradigm XP		Gate-level Sim		✓					✓		✓

Items that have changed since the last issue (XCell 23) are in color.  
The following entry was removed: MEMEC

**Diamond:** These partners have strong strategic relationships with Xilinx and have a direct impact on our releases. Typically, Xilinx is directly involved in the development and testing of the interface to XACTstep software for these products.

**Ruby:** These partners have a high degree of compatibility and have repeatedly shown themselves to be significant contributors to our users' development solutions.

**Emerald:** Proven Xilinx compatibility

## XILINX ALLIANCE-EDA CONTACTS - FEBRUARY 1997

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*Changes since last issue (XCell 23) printed in color.*