

XILINX ALLIANCE-EDA COMPANIES & PRODUCTS-NOVEMBER 1996-1 OF 2

	COMPANY NAME	PRODUCT NAME	VERSION	FUNCTION	DESIGNKIT	3k/	XC	CPLD	UNI	PC	PLATFORMS		
						4k	5200	7k9k	LIB		SUN	RS6000	HP7
DIAMOND	Aldec	Active-CAD	2.2	Schematic Entry, State Machine & HDL Editor, FPGA Synthesis & Simulation	Included	✓	✓	✓	✓	✓			
	Cadence	Verilog	2.4	Simulation	Xilinx Front End	✓	✓	7k	✓		✓	✓	✓
		Concept	2.1	Schematic Entry	Xilinx Front End	✓		7k	✓		✓	✓	✓
		FPGA Designer	9504	Topdown FPGA Synthesis	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		Synergy	2.3	FPGA Synthesis	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		Composer	4.4	Schematic Entry	Xilinx Front End	✓		7k	✓		✓	✓	✓
	Mentor Graphics	Autologic	B.x	Synthesis	Xilinx Synthesis Lib.	✓	✓	7k	✓		✓	✓	✓
		Design Architect	B.x	Schematic Entry	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		QuickSim II	B.x	Simulation	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
		QuickHDL	B.x	Simulation	Call Xilinx	✓	✓	7k	✓		✓	✓	✓
	OrCAD	Simulate (Win)	6.10	Simulation	Call OrCAD	✓	✓	7k		✓			
		VST 386+ (DOS)	1.2	Simulation	Call OrCAD	✓		7k	✓	✓			
		Capture (Win)	7.0	Schematic Entry	Call OrCAD	✓	✓	7k	✓	✓			
		SDT 386+ (DOS)	1.2	Schematic Entry	Call OrCAD	✓		7k	✓	✓			
		PLD 386+ (DOS)	2.0	Synthesis	Call OrCAD	✓		7k		✓			
	Synario Design Automation	ABEL	6.3	Synthesis, Simulation	ABEL-XCPLD			✓	✓	✓			
		Synario	2.3	Schematic Entry, Synthesis & Simulation	SYRO-LCA, SYRO-XEPLD	✓	✓	✓	✓	✓			
	Synopsys	FPGA Express	1.0	Synthesis	Call Synopsis	✓	✓	TBD		✓			
		FPGA Compiler	3.5	Synthesis	Call Xilinx	✓	✓	✓	✓		✓	✓	✓
		VSS	3.5	Simulation	Call Xilinx	✓	✓	✓	✓		✓	✓	✓
		Design Compiler	3.5	Synthesis	Call Xilinx	✓	✓	✓	✓		✓	✓	✓
	Viewlogic	WorkView Office	7.1.2/7.2	Schem/Sim/Synth	Call Xilinx	✓	XACT6	✓	✓	✓	✓	✓	✓
		ProSynthesis	5.02	Synthesis	Call Xilinx	✓	XACT6	7k	✓	✓	✓	✓	✓
		ProSim	6.1	Simulation, Timing Analysis	Call Xilinx	✓	XACT6	7k	✓	✓	✓	✓	✓
		ProCapture	6.1	Schematic Entry	Call Xilinx	✓	XACT6	7k	✓	✓	✓	✓	✓
		PowerView	6.0	Schem/Sim/Synth/Timing Analysis	Call Xilinx	✓	✓	✓	✓	✓	✓	✓	✓
RUBY	Capilano Computing	Design Works	3.1	Schematic Entry/Sim	XD-1	✓			✓	✓			
	Compass Design Automation	ASIC Navigator		Schematic Entry	Xilinx Design Kit	✓	✓	7k			✓		✓
		X-Syn		Synthesis		✓					✓		✓
		QSim		Simulation		✓	✓	7k			✓		✓
	Escalade	DesignBook	2.0	Design Entry		✓			✓	✓	✓		
	Exemplar Logic	Galileo	3.2	Synthesis/Timing Analysis Simulation	Included	✓	✓	✓	✓	✓	✓		✓
	IK Technology Co.	ISHIZUE PROFESSIONALS	1.06	Schematic Entry/Simulation	Xilinx Design Kit	✓	4Q			✓	✓		✓
	IKOS Systems	Voyager	2.31	Simulation	Xilinx Tool Kit	✓	✓				✓		✓
		Gemini	1.21	Simulation	Xilinx Tool Kit	✓	✓				✓		✓
	INCASES Engineering GmbH	Theda	4.1	Design Entry	Xilinx Kit	✓							✓
	ISDATA	LOG/iC Classic	4.2	Synthesis	LCA-PP	✓		7k	✓	✓	✓		✓
		LOG/iC2	4.2	Synthesis Simulation	Xilinx Mapper	✓	✓	7k	✓	✓	✓		✓
	Logic Modeling Corp. (Synopsis Division)	Smart Model		Simulation Models	In Smart Model Lib.	✓	✓	7k,9k		✓	✓	✓	✓
		LM1200		Hardware Modeler	Xilinx Logic Module	✓		7k,9k		✓	✓	✓	✓
	Model Technology	V-System/VHDL	4.4j (PC) 4.6a (WS)	Simulation		✓			✓	✓	✓	✓	✓
	Protel Technology	Advanced Schematic	3.2	Schematic Entry	Included	✓	✓	7k	✓	✓			
		Advanced PLD	3	PLD/FPGA Design & Simulation	Included	✓	✓	7k		✓			
	Quad Design Technology	Motive	4.3	Timing Analysis	XNF2MTV	✓				✓	✓	✓	✓
	SimuCad	Silos III	96.1	Schematic Entry & Simulation	Included	✓	✓		✓	✓			
	Sophia Sys & Tech	Vanguard	5.31	Schematic Entry	Xilinx I/F Kit	✓		✓		✓	✓		✓
	Summit Design Corp.	Visual HDL	3.0	Graphical Design Entry/Simulation/Debug	EDIF Interface	✓	✓	✓	✓	✓	✓	✓	✓
	Synplicity, Inc.	Synplify-Lite	2.6b	Synthesis	Xilinx Mapper	✓	✓	9k	✓	✓	✓		✓
		Synplify	2.6b	Synthesis		✓	✓	9k	✓	✓	✓		✓
	TopDown Design Solutions	V-BAK	1.1	XNF to VHDL translator	XNF interface	✓	✓		✓	✓	✓	✓	
	VEDA DESIGN AUTOMATION INC	Vulcan	4.5	Simulation	XILINX Tool Kit	✓					✓		✓

XILINX ALLIANCE-EDA COMPANIES & PRODUCTS-AUGUST 1996-2 OF 2

	COMPANY NAME	PRODUCT NAME	VERSION	FUNCTION	DESIGNKIT	2k/3k/	XC	CPLD	UNI	PLATFORMS			
						4k	5200	7k9k	LIB	PC	SUN	RS6000	HP7
RUBY	Veribest	Veribest VHDL	14.0	Schematic Entry	Xilinx FPGA Design Kit	3k,4k			✓	✓	✓		✓
		Veribest Verilog	14.0	Simulation	Xilinx FPGA Design Kit	3k,4k			✓	✓	✓		✓
		VeriBest Simulator	14.0	Simulation	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
		DMM	14.x	Design Management	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
		VeriBest Synthesis	14.0	Synthesis	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
		Synovation	12.2	Synthesis	Xilinx FPGA Design Kit	3k,4k			✓	✓	✓		✓
		PLDSyn	12.0	Design Entry Synthesis		✓		7k		✓	✓		✓
		VerBestDesign Capture	14.x	Design Capture	Xilinx FPGA Design Kit	3k,4k	✓		✓	✓	✓		✓
Accolade Design Automation	Peak VHDL	2.21	Simulation	Xilinx Plus	✓	✓			✓				
	Peak FPGA	2.20	Synthesis	Included	✓	✓	✓		✓				
ACEO Technology, Inc.	Asyn	4.1	Synthesis	Included	✓	✓		✓	✓	✓	✓	✓	
	Softwire	3.3	Multi-FPGA Partitioning	Included	✓	✓		✓	✓	✓	✓	✓	
	Gatran	3.3	ASIC to FPGA Netlist Mapping	Included	✓	✓		✓	✓	✓	✓	✓	
	Acugen Software, Inc.	Sharpeye	2.60	Testability Analysis	AALCA interface	✓	✓	7k		✓	✓		✓
ATGEN		2.60	Automatic Test Generation	AALCA interface	✓	✓	7k		✓	✓		✓	
AAF-SIM		2.60	Fault Simulation	AALCA interface	✓	✓	7k		✓	✓		✓	
PROGBSDL		2.63	BSDL Customization	AALCA interface	✓	✓	7k		✓	✓		✓	
TESTBSDL		2.63	Boundary Scan ATG	AALCA interface	✓	✓	7k		✓	✓		✓	
ALPS LSI Technologies	Edway Design Systems		Synthesis/Simulation		✓		✓		✓				
Aptix Corporation	System Explorer	3.1	System Emulation	Axess 3.1	✓	✓	✓			✓		✓	
	ASIC Explorer	2.3	ASIC Emulation	Axess 2.3	4K	✓		✓					
Aster Ingenierie S.A.	XILLAS	4.2	LASAR model generation	Worst Case Simulation	✓		7k		✓	✓		✓	
Auspy Development Co.	APS	1.2.3	Multi-FPGA Partitioning	Included	✓				✓	✓			
Chronology Corporation	TimingDesigner	3.0	Timing Specification and Analysis	Included	✓	✓	7k,9k	✓	✓	✓		✓	
	QuickBench	1.0	Visual Test Bench Generator	Included	✓	✓	7k,9k	✓	✓	✓		✓	
CINA-Computer Integrated Network Analysis	SmartViewer	1.0e	Schematic Generation	XNF Interface	✓		7k		✓				
Epsilon Design Systems	Logic Compressor		Synthesis optimization		✓	✓			✓	✓	✓	✓	
Flynn Systems	Probe	3.0	Testability Analysis	Xilinx Kit	✓	✓	7k		✓				
	FS-ATG	3.0	Test Vector Generation	Xilinx Kit	✓	✓	7k		✓				
	CKTSIM	3.0	Logic Analysis	Xilinx Kit	✓	✓	7k		✓				
	FS-SIM	3.0	Simulation	Xilinx Kit	✓	✓	7k		✓				
Fujitsu LSI	PROVERD		Top-Down Design System	Included	3k,4k				✓				
Harmonix Corporation	PARTHENON	2.3	Synthesis		4k		7k		✓	✓			
Logical Devices	Total Designer	4.7	Simulation & Synthesis	Call Xilinx	✓	✓	✓	✓	✓				
	Ulysa	1.0	VHDL Synthesis	Call Xilinx	✓	✓	✓	✓	✓				
MEMEC Design Services	One-Hot State Mach. Lib.		Grph. Design for One-Hot S.Mach	Xilinx Kit	✓	✓		✓	✓	✓	✓	✓	
MINC	PLDesigner-XL/PL-Synthesizer	3.3/3.2.2	Synthesis	Xilinx Design Module	✓				✓	✓		✓	
Teradyne	Lasar	6	Simulation	Xilinx I/F Kit	✓					✓		✓	
Tokyo Electron Limited	ViewCAD	1.2	FLDL to XNF translator	XNFGEN	✓								
Trans EDA Limited	TransPRO	1.2	Synthesis	Xilinx Library	✓					✓		✓	
Visual Software Solutions	Statedcad	3.0	Grph. Design Entry, Sim., Debug		✓	✓	✓	✓	✓	✓		✓	
Zuken	Tsutsuji		Synthesis/Simulation	XNF Interface	3k,4k					✓	✓	✓	
Zycad	Paradigm RP		Rapid Prototyping		✓					✓		✓	
	Paradigm XP		Gate-level Sim		✓					✓		✓	

Items that have changed since the last issue (XCell 22) are in color.
The following entry was removed: The Rockland Group.

Diamond: These partners have strong strategic relationships with Xilinx and have a direct impact on our releases. Typically, Xilinx is directly involved in the development and testing of the interface to XACTstep software for these products.

Ruby: These partners have a high degree of compatibility and have repeatedly shown themselves to be significant contributors to our users' development solutions.

Emerald: Proven Xilinx compatibility

XILINX ALLIANCE-EDA CONTACTS-NOVEMBER 1996

COMPANY NAME	CONTACT NAME	PHONE NUMBER	E-MAIL ADDRESS
Accolade Design Automation	Dave Pellerin	(800) 470-2686	pellerin@seanet.com
ACEO Technology, Inc.	Ray Wei	(510) 656-2189	ray@aceo.com
Acugen Software, Inc.	Nancy Hudson	(603) 881-8821	
Aldec	David Rinehart	(702) 456-1222 x12	dave@aldec.com
ALPS LSI Technologies, Inc.	David Blagden	441489571562	
Alta Group	Paul Ekas	(408) 523-4135	ekas@altagroup.com
Aptix Corporation	Henry Verheyen	(408) 428-6209	henry@aptix.com
Aster Ingenierie S.A.	Christopher Lotz	+33-99537171	
Cadence	Ann Heilmann	(408) 944-7016	annh@cadence.com
Capilano Computing	Chris Dewhurst	(604) 522-6200	info@capilano.com
Chronology Corporation	Mike McClure	(206) 869-4227 x116	sales@chronology.com
CINA-Computer Integrated Network Analysis	Brad Ashmore	(415) 940-1723	bashmore@cina.com
Compass Design Automation	Marcia Murray	(408) 474-5002	
Epsilon Design Systems, Inc.	Cuong Do	(408) 934-1536	CuongEDS@aol.com
Escalade	Rod Dudzinski	(408) 654-1651	
Exemplar Logic	Shubha Shukla	(510) 337-3741	shukla@exemplar.com
Flynn Systems	Matt Van Wagner	(603) 598-4444	matt@flynn.com
Fujitsu LSI	Masato Tsuru	+81-4-4812-8043	
Harmonix Corporation	Shigeaki Hikusui	(617) 935-8335	
IK Technology Co.	Tsutomu Someya	+81-3-3839-0606	someya@ikt.co.jp
IKOS Systems	Brad Roberts	(408) 366-8509	brad@ikos.com
INCASES Engineering GmbH	Christian Kerscher	+49-89-839910	ckerscher@muc.incases.com
ISDATA	Ralph Remme	+49-72-1751087	ralph.remme@isdata.de
Logic Modeling Corp. (Synopsis Division)	Marnie McCollow	(503) 531-2412	marniem@synopsys.com
Logical Devices	Chip Willman	(303) 279-6868	logdev@logicaldevices.com
Memec Design Services	Maria Agular	(602) 491-4311	info@mds.memec.com
Mentor Graphics	Sam Picken	(503) 685-1298	sam_picken@mentorg.com
MINC	Kevin Bush	(719) 590-1155	
Minelec	Marketing Department	+32-02-4603175	
Model Technology	Greg Seltzer	(503) 526-5465	greg_seltzer@model.com
OrCAD	Mike Jingozian	(503) 671-9500	mikej@orcad.com
Protel Technology	Luisse Markham	(408) 243-8143	
Quad Design Technology, Inc.	Britta Sullivan	(805) 988-8250	
SimuCad	Richard Jones	(510) 487-9700	richard@simucad.com
Sophia Sys & Tech	Tom Tilbon	(408) 232-4764	
Summit Design Corporation	Ed Sinclair	(503) 643-9281	
Synario Design Automation	Jacquelin Taylor	(206) 867-6257	taylorja@data-io.com
Synopsys	Lynn Fiance	(415) 694-4289	lynnf@synopsys.com
Synplicity, Inc.	Alisa Yaffa	(415) 961-4962	alisa@synplicity.com
Teradyne	Mike Jew	(617) 422-3753	jew@teradyne.com
Tokyo Electron Limited	Shige Ohtani	+81-3-334-08198	shige@xilinx.tel.com.jp
TopDown Design Solutions	Art Pisani	(603) 888-8811	
Trans EDA Limited	James Douglas	+44-703-255118	
VEDA DESIGN AUTOMATION INC	Kathie O'Toole	(408) 496-4515	
Veribest	Mike O'Donohue	(303) 581-2330	mikeo@veribest.com
Viewlogic	John Dube	(508) 480-0881	dube@viewlogic.com
Visual Software Solutions, Inc.	Riky Escoto	(800) 208-1051	rescoto@attmail.com
Zuken	Makato Ikeda	+81-4-594-27787	
Zycad	Charlene Locke	(510) 623-4451	char@zycad.com

Inquiries about the Xilinx Alliance Program can be e-mailed to alliance@xilinx.com

Changes since last issue (XCell 22) printed in color: • The following two entries deleted: ITS, The Rockland Group