

# New CPLD Software Updates

New device support and enhancements have been added to the Xilinx XC9500 CPLD software. Both the core fitter and EZTag™ download software have been enhanced to support XC9500 CPLD family designs.

## The latest updates feature:

1. JEDEC programming file generation for the newest member of the XC9500 family, the XC9572-7.
2. Refined fitter optimization algorithms for improved fitting efficiency
3. Netlist reader improvements to accept Exemplar™ generated netlists
4. ISP algorithm enhancements
5. Faster download time from a PC

## The names of the enhanced fitter and EZTag files:

Platform	Fitter File	EZTag File
PC	FITTERPC.ZIP	EZTAG_PC.ZIP
Sun	FITTERSN.TAR.Z	EZTAG_SN.TAR.Z
HP	FITTERHP.TAR.Z	EZTAG_HP.TAR.Z

## The PC files are accessible from the Xilinx BBS and FTP sites:

**BBS:** Software Help ➔ CPLD Software

**FTP:** <ftp://ftp.xilinx.com/pub/swhelp/cpld>

Since the UNIX versions of these files are compressed for the convenience of our workstation users, they are available only on the FTP site. A detailed description of each file is contained in CPLD.LST, also located at these sites. Download these files NOW to take advantage of the industry's best software for the industry's best pin-locking CPLD. ♦

# HW-130 Device Programmer Update

We work closely with third-party programmer manufacturers to support our products with standard programmer solutions. Our own HW-130 programmer for Xilinx SPROMs and CPLDs supports system development and low-volume programming requirements.

Updated HW-130 interface software is now available for both PC and workstation platforms (DOS, Windows 3.1, Windows 95, Windows NT, Sun OS, Solaris, HP9000/700 and IBM RS6000 environments). Users can download the files from the bulletin board system, or request the upgrade by contacting a local Xilinx sales representative. The upgrade is free-of-charge to current HW-130 owners.

Programming support for the XC9500 family is available now on the HW-130 for the XC9536 and the XC95108 CPLD devices. The XC9500 family also is supported by BP Microelectronics' and Data I/O's latest updates, available from their bulletin

board systems. Several other programmer manufacturers have scheduled XC9500 family support in late 1996 and early 1997. The XC9500 family has been added to the charts located near the back of this newsletter that list programmers supporting Xilinx products. ♦



## VITAL Model Support for XC9500 CPLDs

VITAL model support for the XC9500 CPLD family is now available from Topdown Design Solutions. These models allow the designer to generate timing-accurate, gate-level VHDL from Xilinx XNF files. VBAK/VITAL models are designed to work with the Model Technology V-System simulator. These models are designed, implemented, sold and supported by Topdown Systems.

**For additional information, contact Topdown Systems by telephone at 800-438-8435 or by electronic mail at [info@topdown.com](mailto:info@topdown.com). Topdown Systems' web site is located at [www.topdown.com](http://www.topdown.com). ♦**