

New XC9500 ISP Products in Volume Production

Four new XC9500 ISP (In-System Programmable) CPLD devices are now in volume production. The XC9536, XC9572, XC95108 and XC95216 CPLDs are ready for designers looking for the best, most-flexible ISP solution. These 36-, 72-, 108- and 216-macrocell devices, respectively, feature fast, guaranteed performance, the industry's best pin-locking architecture, a true JTAG-compliant interface, and advanced flash memory technology with up to 10,000 program/erase cycles. The resulting benefits include reduced overall costs, faster time-to-market and the flexibility needed to make design changes anytime during the product life cycle.

Please contact your local Xilinx sales representative for further technical information, price and availability information, or requests for samples of these new ISP CPLD products. ♦

Feature	XC9536	XC9572	XC95108	XC95216
Macrocells	36	72	108	216
Usable Gates	800	1600	2400	4800
t_{PD} (ns)	5/7.5/10/15	7.5/10/15	7.5/10/15	10/15/20
Registers	36	72	108	216
Pin-locking	✓	✓	✓	✓
JTAG (1149.1)	✓	✓	✓	✓
Max. User I/Os	34	72	108	166
Packages	PC44/VQ44	PC84 PQ100/TQ100	PC84 PQ100/TQ100 PQ160	PQ160 HQ208



THE XC4000 SERIES OF FPGAS:

Five Million and Counting

XC4000 device #5,000,000 shipped in September. The recipient, Acuson Corporation, a manufacturer of medical electronics systems, was featured at a commemoration ceremony.

Since its introduction in the early 1990s, the XC4000 family has quickly grown to become the world's most successful FPGA product line. The addition of XC4000E devices (and now the XC4000EX versions) to the product family have accelerated XC4000 unit shipments since mid-1994, when the family reached the million-unit milestone.

"As medical technology continues to advance at a phenomenal rate, the success of Acuson more and more depends on our ability to design and deliver the best products possible in the shortest amount of time," stated Robert Gallagher, Acuson president. "The XC4000 has greatly contributed to our technological vision and has helped us provide cost-effective solutions for the needs of our dynamic marketplace. We are delighted to share in the XC4000's success." ♦

XC4000EX Family Enters Production

The world's most popular FPGA series just got bigger! Xilinx began shipments of production-qualified XC4036EX devices in November. The XC4036EX offers 36,000 maximum logic gates and a typical gate range (logic and memory) of 22,000 to 65,000 gates. Planned devices in the XC4000EX family will range from 28,000 to 128,000 logic gates.

The XC4000EX FPGAs contain all the advanced features of the popular XC4000 and XC4000E architectures. However, several significant enhancements to the architecture address the routing, clock distribution, and I/O needs of higher density devices (See XCell #20, page 21).

Development system software for the XC4000EX family will start shipping in late December. The initial software offering will support Synopsys, Mentor Graphics and Viewlogic EDA environments, as well as workstations from Sun (both SunOS and Solaris) and HP. Please contact your local Xilinx sales representative for the latest availability status. ♦