

Xilinx Hosts University Workshops

If you are a university professor who would like to incorporate programmable logic technology into your engineering curriculum, get started by attending a Xilinx University Workshop. Xilinx technology has been used in many engineering courses, including beginning and advanced digital design, processor architecture, digital signal processing, VLSI design, data communications and various project-oriented laboratory courses.

This summer, Xilinx will be hosting four workshops in the United States and two in Asia. These workshops provide a thorough introduction to programmable logic technology, and discuss how to integrate the technology into first-year through fifth-year university courses. The workshops are taught by a team of Xilinx training professionals and local professors with experience at using programmable logic in their own coursework. Technolo-

gies covered at the workshops include field programmable gate arrays (FPGAs), complex programmable logic devices (CPLDs), and dynamically reconfigurable logic. Although most workshops will cover the same basic material, some will have special themes, as described below. Hands-on labs using PC-based development tools are included in each workshop.

These three-day workshops are available free of charge to professors and instructors. However, due to the popularity of the workshops, attendance is limited to two people from each university or college. Seating and hotel rooms at each workshop are limited, so early registration is advised.

To register, contact Jason Feinsmith, Xilinx University Program Manager, at 408-879-4961 or e-mail xup@xilinx.com or visit www.xilinx.com/programs/univ.htm for information. ♦

WORKSHOP SCHEDULE

UNITED STATES

Xilinx/Washington State University Workshop

Richland, WA ♦ June 24 - 26

In addition to the basic course material, Dr. Donald Hung will discuss his first-hand experiences in developing courses that use programmable logic.

Xilinx/Cornell University Reconfigurable Computing Workshop

Ithaca, NY ♦ July 10 -12

This will be our first hands-on workshop devoted to the topic of reconfigurable computing with FPGAs. Targeted at educators who are familiar with reprogrammable logic and are very interested in the concept of dynamically reconfigurable computing, this workshop will condense the basic material and focus on this new and promising

area of study. Several researchers will be present to discuss their work.

Xilinx/Massachusetts Institute of Technology Workshop

Boston, MA ♦ July 15 - 17

For those particularly interested in computer architectures, this workshop will include a look at MIT's newly-created computer structures curriculum based on Xilinx devices and the "electric legos" concept.

Xilinx/Oakland University Workshop

Detroit, MI ♦ July 24 - 26

Dr. Subra Ganesan will discuss his work in using FPGAs in digital signal processing applications at this workshop.

ASIAN SCHEDULE

Workshops are planned for the week of August 19 in China, and the week of August 26 in Taiwan. Further details were not available at the time of this printing.

For up-to-date information, visit www.xilinx.com/programs/univ.htm on the World Wide Web. ♦