

XACT-CPLD Supports Xilinx CPLD Design

Xilinx has expanded software support of our CPLD product lines with the introduction of the DS-560™ XACT-CPLD package. XACT-CPLD provides a complete low-cost, user-friendly environment for schematic, behavioral and VHDL design on the PC, Sun 4 and HP-700 platforms. XACT-CPLD supports both the XC7000 family, the industry's fastest and lowest-cost CPLDs, and the XC9500 family, the best in-system programmable CPLDs.

Features of the

XACT-CPLD package include:

- **Automatic Device Selection:** Automatically implements the design in the smallest CPLD possible
- **Automatic Optimization, Partitioning and Mapping:** Designs are automatically optimized, partitioned and mapped into the device for optimal efficiency and design performance
- **XACT-Performance™:** Timing-driven optimization collapses logic to meet user-specified critical timing requirements.
- **Static Timing Analyzer:** Provides a complete pin-to-pin timing report of the design, including detailed internal path analysis
- **In-System Programming Support:** EZTag download software supports programming of Xilinx CPLDs any-

where in a JTAG chain. The download cable is included with the software.

- **XC9500 CAE Interfaces:** Includes Viewlogic and OrCAD interfaces and libraries for the PC platform and Viewlogic, Synopsys VHDL and Verilog interfaces and libraries for the Sun and HP platforms.

Schematic and VHDL Design Entry

Xilinx provides an open design environment allowing designers to choose from a variety of schematic entry, VHDL synthesizer and simulation tools such as those from OrCAD, Viewlogic, Mentor Graphics, Exemplar and Synopsys. When combined with the appropriate library and interface software, XACT-CPLD version 6 provides a complete environment for the processing of Xilinx CPLD designs.

ABEL-HDL Entry

XABEL owners can embed macros in their schematics or enter complete chip designs for Xilinx CPLDs, then use XACT-CPLD v6 to complete the design implementation. The new Xilinx XABEL-CPLD package also includes this core software. XABEL-CPLD is a complete ABEL-HDL based design tool designed specifically for text-only CPLD design.

Embedded Third-Party Compilers

The Xilinx fitter technology is licensed to many third-party development tool vendors, providing the flexibility and versatility of industry-standard design software environments with the speed, density and routability of Xilinx CPLDs. The fitter is fully integrated into the Data I/O Synario, Logical Devices CUPL, and Isdata LOGiC environments. For price and availability of the XACT-CPLD v6 based fitter, contact the development tool manufacturer. ♦

Product Availability and Pricing

The XACT-CPLD v6 core software is available now for immediate delivery.

PART NUMBER	VERSION	PLATFORM	PRICING
DS-560-PC1-C	v6	PC	\$295
DS-560-SN2-C	v6	Sun	\$495
DS-560-HP7-C	v6	HP	\$495