

General

Q. What are the FXC Libraries included with XACT 5.1.1?

- A. In addition to the XC2000, XC3000, XC4000, and XC7000 libraries, the 5.1.1 release also includes libraries called FXC2000, FXC3000, FXC4000, and FXC7000 that can be used in place of the Unified libraries to reduce simulation runtime. Simulation of designs with the 5.0.0 Unified libraries suffered significant increases in simulation runtime due to the addition of more comprehensive violation checking. The FXC simulation libraries do not contain the additional violation checks but are identical in every other way. NOTE: The XC libraries should be

used to perform a final timing simulation so that the new violation checking will be performed.

Q. How do I turn off the internal oscillator in an XC5200 family FPGA?

- A. Set the Makebits tag for the oscillator clock to the user clock setting (“OscClk:UserClk”). This means that the internal oscillator clock is replaced by the user clock. The default is “OscClk:Cclk”. Doing this may produce a DRC error since XACT expects a net to be connected to the osc.CK pin in the top right corner of the XACT Design Editor. You may either ignore the DRC error, or, to eliminate the DRC error, connect a grounded net to the osc.CK pin. ♦

Viewlogic

Q. I just installed XACT 5.1.1, and ProSeries reports:

```
SYSCMD.PIF or SYSBLOCK.PIF
Cannot find file.
Check to insure the path and
filename are correct.
```

How is this corrected?

- A. The system window opened by a sys command or a print command requires the files syscmd.pif and sysblock.pif. These two files are missing from the DS-VLS-STD-PC1 package. You can retrieve these two files either by installing “PROSYN” from the DS-VLS-EXT-PC1 package, by unzipping the prosyn1.zip file from the wn1\prosyn\group2 directory, or by downloading 511FIX.ZIP from the BBS. Make sure these two .pif files are in either the PROSER\ or Windows\ directory.

Q. Why do I get the message “Type Mismatch” from ProSeries?

- A. PROvhdl, PROgen, PROlib, PROsynth, and PROgen return this error message when no project has been defined. Defining a project via PROJman or PROcapture will cause the message to go away.

Q. I can run all of the ProSeries software except ProSim. What could be wrong?

- A. PROsim is the only program in the PRO Series 6.0 release that uses the WIN32S 32-bit extender. If all the other PROseries software works, the problem may be that WIN32S is not installed or the version installed is not compatible. Try installing WIN32S from the 5.1.1 CD. The WIN32S setup.exe program is located in the WIN32S directory.

Q. Why do I get the error message, “The 386 chip is currently executing in virtual mode under the control of another program” from PROseries tools?

- A. The versions prior to XACT 5.1.1 contained executables that could not be run in Windows. Attempting to run these executables in an MS-DOS box would result in this error message. Typically, this occurs when an older, non-Windows compatible executable appears in the path before the new PROSER directory. Remove all references to WORKVIEW in the following system variables: PATH, WDIR, and SYSPLT, and the message should go away. ♦

Synopsys

Q. How can I tell what version of XSI I have?

- A. At your command prompt, type
syn2xnf <CR>. A help screen will come
up with a version number of syn2xnf:
syn2xnf v3.5.0=XSI 3.2
syn2xnf v3.6.0=XSI 3.3

Q. I was using Synopsys 3.2a and XSI 3.2. Now, I've upgraded Synopsys to v3.3a. When I try to compile my design, I get an error message from the Synopsys Compiler:

Error: The entity add_sub_ub'
depends on the package
'std_logic_arith' which has
been analyzed more
recently.

Please re-analyze the source
file for 'add_sub_ub' and try
again.

Information: Compile
terminated abnormally.

- A. If you have an existing setup of Synopsys and XSI, and you upgrade your version of Synopsys as above, then you must reanalyze your designware and simulation libraries. Please see the release notes section entitled 'Analyzing the Designware and Simulation Libraries.'

Q. Can Synopsys 3.2a work with XSI 3.3?

- A. No. XSI is only compatible with versions of Synopsys equal to or greater than the XSI version. In other words:
XSI 3.2 can work with Synopsys 3.2a and greater.
XSI 3.3 can work with Synopsys 3.3a and greater.
Synopsys 3.2a will not work with XSI 3.3.

Q. What are some of the new features in XSI 3.3?

- A. Among the major improvements: XSI 3.3 supports XC5200 family designs, and XBLOX optimization has been improved. ♦

OVERVIEW OF TECHNICAL SUPPORT FACILITIES

Automated Support Systems

An incorrect number for the telephone hotline service in Germany was published on page 29 of *XCELL* #17. Our apologies for any inconvenience that may have caused. The correct number is given below. ♦

Xilinx home page - available at <http://www.xilinx.com>.

XDOCS E-mail document server - send an E-mail to xdocs@xilinx.com with 'help' as the only item in the subject header. You will automatically receive full instructions via E-mail.

XFACTS fax document server - available by calling 1-408-879-4400.

In order to provide timely support for new, high-growth markets, Xilinx Applications has established a series of E-mail addresses to direct technical inquiries. These addresses may be used to request information packets or to ask in-depth technical questions:

Digital Signal Processing applications dsp@xilinx.com
PCI-bus applications pci@xilinx.com
Plug and Play ISA applications pnf@xilinx.com
Asynchronous Transfer Mode applications atm@xilinx.com
Reconfigurable logic/computing applications ... reconfig@xilinx.com

Hotline Support, North America

Customer Support Hotline: 800-255-7778

Hrs: 8:00 a.m. - 5:00 p.m. Pacific time

Customer Support Fax Number: 408-879-4442

Avail: 24 hrs/day-7 days/week

Electronic Technical Bulletin Board: 408-559-9327

Avail: 24 hrs/day-7 days/week

Customer Service: 408-559-7778, ask for customer service

For software updates, authorization codes, documentation updates, etc.

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