

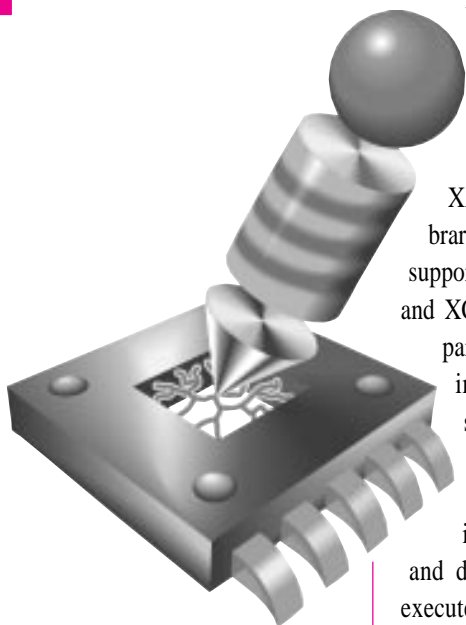
# New $X_S A_T C_E T_P$ Advanced Design System

As reported in *XCELL* #17, Xilinx has merged with NeoCAD, Inc. of Boulder, Colorado, a leading supplier of FPGA design software. As a result of the merger, Xilinx recently brought to market a new FPGA design system called the XACTstep™ Advanced package — the first new product offering that leverages the NeoCAD software technology.

The Advanced package features XACTstep Foundry, NeoCAD's core design system for FPGA implementation (formerly called NeoCAD FPGA Foundry), bundled with the XACTstep system from Xilinx (formerly called XACT).

XACTstep Foundry, featuring advanced timing driven placement and routing technology, has a proven track record for helping users achieve maximum utilization and performance for Xilinx FPGA designs.

XACTstep Foundry is netlist and library compatible with XACTstep, and supports the Xilinx XC4000, XC3100/A, and XC3000/A FPGA families. It adds particularly significant value to the implementation of high-density designs, and includes support for the XC4025. XACTstep Foundry includes optional high-level synthesis integration, multi-device partitioning and distributive processing capabilities. It executes on PCs and workstations, including Sun Solaris.



The XACTstep Advanced package is available now. Easy upgrade paths for existing XACT and NeoCAD FPGA Foundry users also are available. **Contact your local Xilinx sales representative for ordering information.** ♦

## Update News

The latest revision of XACTstep Foundry (version 7) was shipped this summer to NeoCAD FPGA Foundry owners with a Xilinx configuration and active maintenance contract.

XACTstep version 6 updates are scheduled for first customer ship in October. Most users will receive their updates in the fourth quarter. XACTstep version 6 supports the XC2000, XC3000, XC3100, XC4000, XC5000, and XC7000 component families.

Xilinx is hard at work finalizing a new software technology strategy and release plan leveraging the best software technologies of both Xilinx and NeoCAD. New products integrating the best of XACTstep and XACTstep Foundry, scheduled for introduction in 1996, will result in unparalleled power, functionality and ease-of-use for FPGA designers. Details on these products will become available in the coming months. ♦

## Synthesis Support

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Tools for both VHDL and Verilog HDL design entry are available. VITAL-compliant simulation models are available for a variety of third-party simulation tools including Cadence Verilog and Model Technologies. In addition, the EPLD physical device models are available from Logic Modeling, a division of Synopsys.

So, no matter what tools are used, Xilinx provides everything needed to produce high-performance EPLD designs with minimal effort.

**For product availability or other information, please contact your local Xilinx representative.** ♦