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Documentation related to cp1d design methodology.

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<a href="#">epldref.pdf</a>	1,568KB	XEPLD Reference Guide
<a href="#">epldrefw.pdf</a>	1,374KB	XEPLD (For Windows) Reference Guide
<a href="#">epldsch.pdf</a>	686KB	XEPLD Schematic Design Guide
<a href="#">epldschw.pdf</a>	691KB	XEPLD Schematic (For Windows) Design Guide
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<a href="#">xnf61_7k.pdf</a>	21KB	XNF XC7000 Specification Addendum to Xilinx XNF

## Specification

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