



File Download: app_xapp

Contents of /ftp/apps/xapp

Application design files for XAPP Application Notes
See ../../../../apps/xapp.htm for more information.

Filename	Size	File Description
xapp007o.zip	27KB	Boundary Scan Emulator for XC3000 v2.0 Implemented in OrCAD 386+ V1.10 Unified Libraries
xapp007v.zip	33KB	Boundary Scan Emulator for XC3000 v2.0 Implemented in Viewdraw-LCA Unified Li
xapp014o.zip	70KB	Ultra-Fast Synchronous Counters v1.02, Implemented in OrCAD 386+ V1.10 Unified Libraries
xapp014v.zip	61KB	Ultra-Fast Synchronous Counters V.1.01, Implemented in Viewdraw-LCA Unified Libraries
xapp044o.zip	39KB	High-Performance RAM based FIFO V.1.00, Implemented in Orcad 386+ V1.1
xapp002v.zip	197KB	Loadable Up/Down Counters, 8 to 24 bit V.2.0.1, Implemented in Viewdraw-LCA Unified Libraries
xapp050o.zip	417KB	Crossbar Switch in XC4000 V.1.00, Implemented in Orcad 386+ V1.1
xapp050v.zip	362KB	Crossbar Switch in XC4000 V.1.00, Implemented in Viewdraw-LCA Unified Libraries
xapp001o.zip	100KB	60 MHz Counters, 8 to 24 bits long V.2.0.1, Implemented in OrCAD 386+ V1. Unified Libraries
xapp005o.zip	68KB	Register-Based FIFO for XC3000 V.2.0.1, Implemented in OrCAD 386+ V1. Unified Libraries
xapp005v.zip	87KB	Register-Based FIFO for XC3000 V.2.0.1, Implemented in Viewdraw-LCA Unified Libraries
xapp009o.zip	26KB	Frequency Synthesizer, FSK Modulator V.2.0.1, Implemented in OrCAD 386+ V1.

xapp021v.zip	107KB	Unified Libraries 150 MHz Dual-Prescaler Presettable Cou for XC3000. V.2.01, Implemented in Viewdraw-LCA
xapp023o.zip	64KB	Unified Libraries Accelerating Loadable Counters in XC40 V.1.01, Implemented in OrCAD 386+ V1.1
xapp003o.zip	311KB	Unified Libraries Synchronous Presettable Up and Down Co 8, 12, and 16 bits long V.2.0.1, Implemented in OrCAD 386+ V1.
xapp004o.zip	225KB	Unified Libraries Loadable Binary Counters, 16, 32 bits V.2.0.1, Implemented in OrCAD 386+ V1.
xapp009v.zip	31KB	Unified Libraries Frequency Synthesizer, FSK Modulator V.2.0.1, Implemented in Viewdraw-LCA
xapp021o.zip	90KB	Unified Libraries 150 MHz Dual-Prescaler Presettable Cou for XC3000. V.2.01, Implemented in Or 386+ V1.10.
xapp022o.zip	12KB	Unified Libraries Adders, Subtractors, and Accumulators XC3000. V.1.01, Implemented in OrCAD V1.10.
xapp022v.zip	17KB	Unified Libraries Adders, Subtractors, and Accumulators XC3000. V.1.01, Implemented in Viewdr
xapp023v.zip	78KB	Unified Libraries Accelerating Loadable Counters in XC40 V.1.01, Implemented in Viewdraw-LCA
xapp026o.zip	23KB	Unified Libraries Multiplexers and Barrel Shifters in XC V.1.01, Implemented in OrCAD 386+ V1.1
xapp026v.zip	32KB	Unified Libraries Multiplexers and Barrel Shifters in XC V.1.01, Implemented in Viewdraw-LCA
xapp028o.zip	5KB	Unified Libraries Frequency/Phase Comparator for Phase-L Loops. V.1.01, Implemented in OrCAD 3
xapp028v.zip	6KB	Unified Libraries Frequency/Phase Comparator for Phase-L Loops. V.1.01, Implemented in Viewdra
xapp029o.zip	6KB	Unified Libraries Serial Code Conversion Between BCD and V.1.01, Implemented in OrCAD 386+ V1.1
xapp029v.zip	8KB	Unified Libraries Serial Code Conversion Between BCD and V.1.01, Implemented in Viewdraw-LCA
xapp048o.zip	39KB	Unified Libraries Xilinx Guided Tour for XACT 5.0 V.1.00, Implemented in OrCAD 386+ V1.1
xapp048v.zip	37KB	Unified Libraries Xilinx Guided Tour for XACT 5.0 V.1.00, Implemented in Viewdraw-LCA

xapp002o.zip	187KB	Unified Libraries Loadable Up/Down Counters, 8 to 24 bit V.2.0.1, Implemented in OrCAD 386+ V1.
xapp003v.zip	375KB	Unified Libraries Synchronous Presettable Up and Down Co 8, 12, and 16 bits long V.2.0.1, Implemented in Viewdraw-LCA
xapp004v.zip	282KB	Unified Libraries Loadable Binary Counters, 16, 32 bits V.2.0.1, Implemented in Viewdraw-LCA
xapp036a.zip	6KB	Unified Libraries DRAM Controller for XC7200 ABEL and .pld Behavioral design files for a 4-port memory controller. Fits in an XC7236.
xapp049a.zip	18KB	Pentium/Synchronous DRAM Controller Behavioral Design Example for EPLDs ABEL files, V.1.00
xap4007v.zip	29KB	Boundary Scan Emulator for XC3000 Implemented in Viewdraw-LCA
xap4021v.zip	114KB	Pre-Unified Libraries 150 MHz Dual-Prescaler Presettable Cou for XC3000. V.1.00, Implemented in Viewdraw-LCA
xap4006v.zip	84KB	Pre-Unified Libraries RAM-Based FIFO for XC4000 V.1.10, Implemented in Viewdraw-LCA
xap4009v.zip	40KB	Pre-Unified Libraries Frequency Synthesizer, FSK Modulator V.1.10, Implemented in Viewdraw-LCA
xapp006v.zip	84KB	Pre-Unified Libraries RAM-Based FIFO for XC4000 V.1.10, Implemented in Viewdraw-LCA
xap4005v.zip	101KB	Unified Libraries Register-Based FIFO for XC3000 V.1.10, Implemented in Viewdraw-LCA
xap4004v.zip	350KB	Pre-Unified Libraries Loadable Binary Counters, 16, 32 bits V.1.10, Implemented in Viewdraw-LCA
xap4003v.zip	196KB	Pre-Unified Libraries Synchronous Presettable Up and Down Co 8, 12, and 16 bits long V.1.10, Implemented in Viewdraw-LCA
xap4002v.zip	273KB	Pre-Unified Libraries Loadable Up/Down Counters, 8 to 24 bit V.1.10, Implemented in Viewdraw-LCA
xap4001v.zip	149KB	Pre-Unified Libraries 60 MHz Counters, 8 to 24 bits long V.1.10, Implemented in Viewdraw-LCA