

**ANSWERS****Technical Support**

File Download: app_pci

Contents of /ftp/apps/pci

Applications Involving PCI

Filename	Size	File Description
revle.zip	503KB	Viewlogic version and Verilog source code for XC3164 designs in app "Designing Flexible PCI Interfaces wit Xilinx EPLDs."
pci_abel.zip	3KB	ABEL source code for XC73108 and XC735 designs in application note "Designing Flexible PCI Interfaces w/ Xilinx EPLD
pci_v.zip	16KB	Verilog/HDL source code for XC3164 des application not "Fully Compliant PCI Interface in XC3164A-2"
pci_vhdl.zip	3KB	VHDL source code for XC73144 Target In design in application note "Designing Flexible PCI Interfaces w/ Xilinx EPLD
7300ckl.zip	8KB	XC7300-10 PCI Component Electrical Che (Word 6.0)
3100ackl.zip	3KB	XC3100A-2 PCI Component Electrical Che (Word 6.0)
pciapp.zip	25KB	Notes on PCI applications