

**ANSWERS**

# Technical Support



## File Download: app\_misc

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### Contents of /ftp/apps/misc

Miscellaneous Programmable Logic Applications

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Filename	Size	File Description
=====		
rs_232.zip	213KB	Design files to accompany the applicat note: RS-232 Based Communications with FPGAs. Contains Viewlogic schematics Pre-Unified Libraries, post-script fil
pc_isa.zip	50KB	files to accompany "Configuring FPGAs over a Processor Bus" application note
bcdcnt.zip	59KB	ViewLogic Schematics for "Ten Digit Synchronous BCD Counter", targeted for
psmfiles.exe	44KB	Viewlogic schematic files for "Dynamic controller in an XC4000 FPGA" App note
counter.pld	1KB	Counter file mentioned on p.1-4 of XEP Design Guide, April 1994
hd16bitb.zip	9KB	High speed 16-bit counter (9 CLBs)
hd16bit.zip	17KB	High-density 16-bit counter and accumu hard macros (8 CLBs each)
pwm.zip	8KB	Viewlogic design files for "Pulse Widt Modulation in Xilinx Programmable Logi application note For All Platforms
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