

**ANSWERS****Technical Support**

File Download: app_arithmetic

Contents of /ftp/apps/arithmet

FPGA Applications Involving Arithmetic Functions

Filename	Size	File Description
divide4k.zip	83KB	16 x 16 Divide function in 4000 series Implemented with FutureNet/Silos
add16.zip	43KB	16-Bit Conditional Sum Adder Xilinx Data Book, "Conditional Sum Add
a16bit.zip	50KB	16-Bit Adder with Carry Lookahead Xilinx Data Book, "Adders and Comparat
a16jet.exe	404KB	Implemented in Dash-LCA Self-extracting archive. Contains HP Jet files to print out schematics for Adder with Carry Lookahead.