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XILINX RAMPS VOLUME ON XC5000 FAMILY

Two New XC5000 Devices Added to Low-Cost Family

SAN JOSE, Calif., May 8, 1995—Xilinx, Inc. (NASDAQ:XLNX) today announced it is in full production with two members of its XC5000 family of field programmable gate arrays (FPGAs). The XC5000 family architecture is optimized to dramatically reduce the cost of reprogrammable logic, enabling XC5000 devices to be used in high-volume applications traditionally served by masked gate arrays. The company also announced two new low-density devices that offer the same low cost and high performance benefits of the existing devices.

“Since the announcement of the XC5000 family in November 1994, customer interest in these highly flexible, low cost FPGAs has been so tremendous that we have added two new devices,” said Chuck Fox, vice president of product marketing. “The high demand for these devices is, in large part, the result of the top-tier gate array manufacturers shifting their focus to higher volume, higher density applications. And, with XC5000 FPGAs at less than 3X the cost of a gate array, programmable devices become a viable alternative for high volume production.”

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Xilinx is currently shipping in volume the 10,000-gate XC5210 and the 6,000-gate XC5206 logic devices. With the XC5210 device available for \$38.00 and the XC5206 device for \$25.00, in 10,000-unit quantities, the XC5000 devices currently represent the most cost-efficient FPGAs in the industry, according to Fox.

Commenting on the XC5000 family, Richard J. Glass, senior hardware design engineer for VTEL, a global leader in application solutions for the videoconferencing industry, commented, "Xilinx XC5000 FPGAs allow VTEL to greatly increase the integration density of specialized video processing and control circuits while maintaining the advantages of adaptive, on-board programming with significantly reduced costs."

Xilinx Announces Two New XC5000 Family Devices

In addition to the higher density devices, Xilinx is also announcing two new devices that will target low-density applications. "Many customers continue to require low-density devices that offer the speed and cost effectiveness of gate arrays, yet need FPGA flexibility," added Fox. "The XC5000 architecture has proven to be extremely efficient in silicon utilization, making the devices very cost-effective at all density levels. Indeed, the XC5000 family, spanning 2,000 to 18,000 gates and beyond, is clearly setting direction for cost and performance requirements in today's programmable logic devices."

Xilinx will offer the 2,000-gate XC5202 device for \$9.00 and the 4,000-gate XC5204 device for \$15.00, in 10,000 piece quantities, in the third quarter, further narrowing the gap between FPGA and masked gate array pricing. The fifth member

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of the family, the XC5215 device, a 15,000 gate device that offers up to 18,000 usable gates, will also be available in the third quarter. As with the XC5206 and XC5210, these devices will offer footprint compatibility with existing Xilinx FPGAs, eliminating the need for customers to re-design the layout of their printed circuit boards.

The following chart details the XC5000 family:

	XC5202	XC5204	XC5206	XC5210	XC5215
Usable Gates	2200-2700	3900-4800	6000-7500	10,000-12,000	14,000-18,000
Max I/O	84	124	148	196	244
Flip-Flops	256	480	784	1296	1936
*Pricing (10K quantity)	\$9	\$15	\$25	\$38	\$68
Availability	3Q95	3Q95	Now	Now	3Q95

*Prices for the XC5202, XC5204, XC5206 and XC5210 devices are in PC84 packages; the XC5215 device is in PQ208; all are in -6 speed grades

The XC5000 family is the first FPGA family specifically developed as a cost-effective, high-volume production alternative to gate arrays by utilizing a three-layer metal (TLM), 0.6 micron CMOS process. Additionally, its innovative SRAM-based architecture features the VersaBlock™ logic module and VersaRing™ I/O interface. The VersaBlock logic module is a unique combination of both logic and routing resources that yields maximum logic utilization while minimizing silicon area. The result is a substantially reduced die size that enables lower device prices. The VersaRing I/O interface decouples the input/output blocks from the core logic,

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like a gate array architecture, and adds incremental routing resources along the edge of the device, providing higher pin assignment flexibility. All XC5000 devices also feature JTAG circuitry, making the devices fully board testable for customers, an increasing requirement by today's FPGA designers.

In addition to the low cost, the XC5000 devices have also proven to be very high performance—up to 50 MHz in many applications, thereby serving a significant portion of the logic requirements in the sub-10,000 gate market, including applications such as local area networking and video processing.

The following chart details the XC5200 family performance:

Function	-6 Speed Grade	-5 Speed Grade
16-Bit Decoder	9 ns	8 ns
24-bit Accumulator	32 MHz	39 MHz
16-to-1 Multiplexor	16 ns	13 ns
16-bit Unidirectional Loadable Counter	40 MHz	50 MHz
16-bit Adder	24 ns	20 ns
24-bit Loadable Unidirectional counter	36 MHz	42 MHz

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Xilinx also offers customers a cost-reduction path for high volume with the Xilinx Hardwire™ family. This enables customers to easily convert FPGA designs into a mask-programmed version of the exact FPGA design—without the re-design typically required for masked gate arrays—further serving the time-to-market requirements of traditional gate array applications.

Company Background

Founded in 1984, Xilinx is the world's leading supplier in the billion dollar CMOS programmable logic industry. The company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer peripherals, telecommunications, industrial control, instrumentation and military markets. Headquartered in San Jose, Calif., the company produces innovative device architectures and associated development system software.

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