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FOR IMMEDIATE RELEASE

**XILINX ANNOUNCES PLANS TO SPUR DEVELOPMENT
OF RECONFIGURABLE COMPUTING MARKET**

**New Program Offers Special Incentives to
Reconfigurable Computing Developers Using Xilinx Devices**

SAN JOSE, Calif., June 12, 1995—Xilinx, Inc., (NASDAQ:XLNX) today announced that it has identified *reconfigurable computing* as another major market opportunity for FPGAs and has taken the first in a series of steps to spur market development. Over the next three years, Xilinx plans to invest more than \$20 million on developing the market and products for reconfigurable hardware, which is projected to grow to \$1 billion by the end of this decade, according to Xilinx estimates. The company will make an initial investment to support applications developers and consultants through a number of programs aimed at creating a market for reconfigurable computing.

Reconfigurable computing makes use of in-system configurable FPGAs as computing elements within general purpose computers. A wide range of distinct applications in image and audio processing, digital signal processing, scientific computing, database searching and EDA can benefit by

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the acceleration of software algorithms by up to 1000x.

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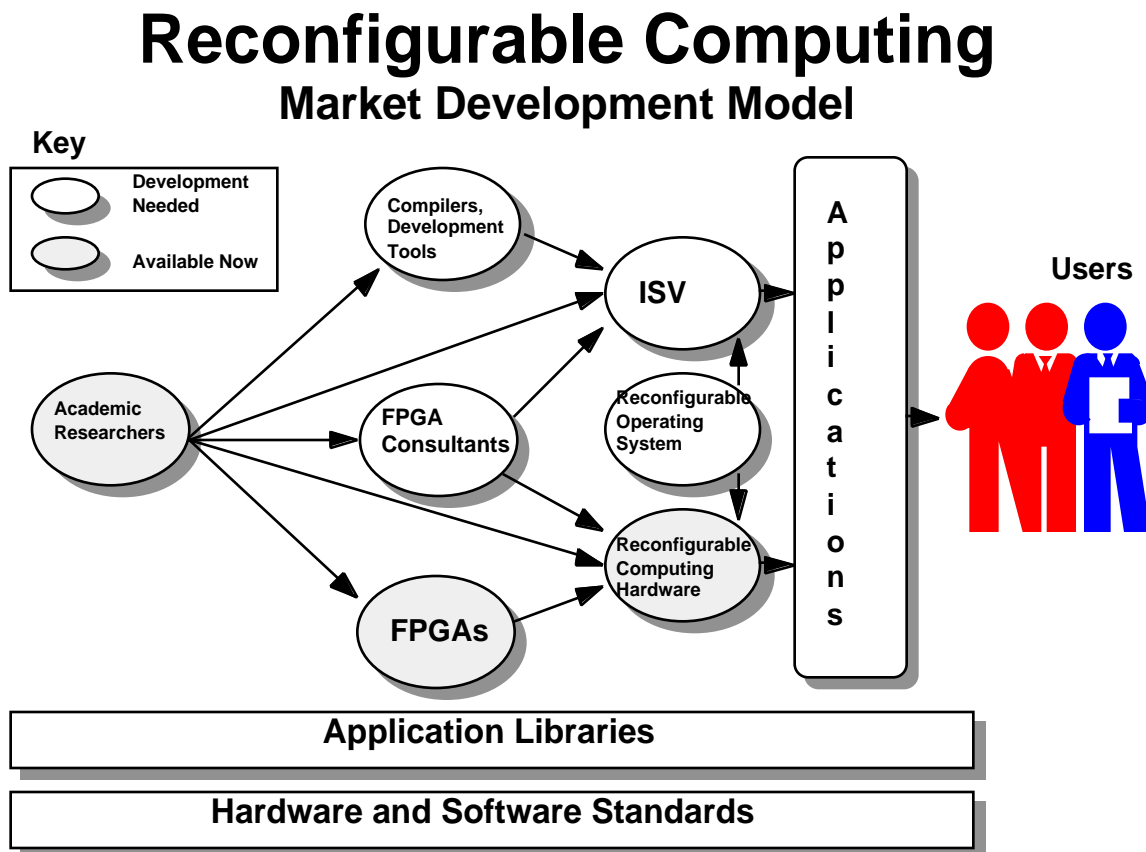
To date, reconfigurable computing has garnered support from the scientific and academic communities, whose combined efforts have demonstrated the viability of FPGA-based reconfigurable hardware and applications, but its commercial success has been very limited. Some application examples include the successful development of two generations of FPGA-based computers, called the *Splash* series, by the U.S. Government's SuperComputing Research Center (SRC). One version, the *Splash-2*, was used in a Virginia Polytechnic Institute project, which successfully demonstrated complex image processing tasks executing in real-time. In another project at Digital Equipment Corporation's Paris Research Center, a reconfigurable computer based on FPGAs was developed to accelerate scientific computing applications, also with great success.

"The market for reconfigurable computing is in critical need of a 'kick-start' and, as the leader in programmable logic, we intend to play that role," said Curt Wozniak, president of Xilinx. "For years, evangelists of reconfigurable computing have communicated to us, the primary FPGA supplier for these applications, directly and through our third-party software developers and FPGA consultants, the viability of such applications. With our development of very low-cost, extremely high-capacity devices, we have the enabling FPGA technology in place to create a significant market."

According to Wozniak, the reconfigurable computing market will require the collaboration of distinct industry components if commercial applications are to develop along a practical path for the end-user. These include the genesis of new FPGA architectures tuned to reconfigurable applications, the availability of software to implement and run those

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applications and, finally, the creation of standards to govern the process. (See nearby chart, *Reconfigurable Computing, Market Development Model*.)



Reconfigurable Computing Developers Program

To stimulate commercial applications, Xilinx today announced the Reconfigurable Computing Developers Program that is designed to offer special support for qualified companies using Xilinx programmable logic. As members of this program, developers can qualify to receive discounts on devices, development systems and training as well as support of promotional activities. Under the Reconfigurable Computing Developers Program, developers might also be eligible for cash development grants.

Specific benefits to members include:

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- Up to a 50 percent discount on Xilinx development systems.
- Special discount pricing on purchases of Xilinx devices up to \$100,000.
- Fifty percent discount on technical training.
- Eligibility for commercial development grants of up to \$20,000 upon approval of a reconfigurable computing hardware development proposal.
- Promotional support for activities through Xilinx-produced sales literature distributed to Xilinx sales channels and access to Xilinx customers through Xilinx mailing lists.
- Electronic access to reconfigurable computing libraries and files.

This initial program represents the first step in a comprehensive market-development plan to stimulate the commercialization of reconfigurable computing hardware. Xilinx will be announcing additional programs as well as FPGAs that are specifically architected for reconfigurable computing over the next year. To obtain an application for the program, developers may contact Xilinx at (800) 231-3386 or fax requests to (408) 879-4780. Additionally, both the application and a more detailed description of the program are available from the Xilinx World Wide Web site at <http://www.xilinx.com>.

Company Background

Founded in 1984, Xilinx is the world's largest supplier in the \$1.2 billion CMOS programmable logic industry. The company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer peripherals, telecommunications, industrial control, instrumentation, and military markets. Headquartered in San Jose,

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Calif., the company produces innovative device architectures and associated development system software.

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