



Ann Dennis
Xilinx, Inc.
(408) 879-4726

FOR IMMEDIATE RELEASE

XILINX CUTS HI-REL FPGA PRICES
Prices to Drop an Additional 20 Percent

SAN JOSE, Calif., April 24, 1995—Xilinx, Inc., (NASDAQ:XLNX) today announced up to 20 percent price cuts for its high-reliability (Hi-Rel) MIL-STD-883B-compliant field programmable gate arrays (FPGAs). The reductions have been achieved through higher volumes and lower package and test costs.

“Although, many ASIC vendors are leaving the Hi-Rel industry, Xilinx is increasing its penetration by lowering prices and introducing new products,” said Mitch Richman, Hi-Rel product marketing manager. “Xilinx FPGAs offer a more cost-effective logic solution for Hi-Rel designs by eliminating the high non-reoccurring engineering (NRE) costs and high design risk associated with the use of mask-programmed gate arrays.”

Xilinx's Commitment to the Hi-Rel Market

Xilinx is a staunch supporter of the Hi-Rel market, as evident by the introduction of nine new devices in the last 12 months. The lower priced Hi-Rel FPGAs and the narrowing cost gap between FPGAs and mask-programmed gate arrays are continuing to increase the use of FPGAs in high-reliability applications. According to In-Stat, military FPGA sales are expected to reach \$45.7 million by 1997, up from \$22.2 million in 1994. Annual growth rates are averaging 28 percent

— more —

2100 Logic Drive • San Jose, California 95124-3400
Telephone: 408-559-7778 • FAX: 408-559-7114

through the end of the decade. Xilinx continues as the leading supplier in developing new devices and meeting users' demands for high-density programmable logic.

FPGAs Answer Greater Needs

With high NRE costs, reduced government funding and long cycle times, fewer Hi-Rel customers desire mask-programmed gate arrays. In addition, the supplier base for Hi-Rel, mask-programmed gate arrays continues to shrink as vendors withdraw from the market. The quick time-to-market benefits and design flexibility of FPGAs make them the ideal logic solution for shortened design and development cycles, and constantly changing specifications.

The reprogrammability and off-the-shelf availability of FPGAs also offer advantages over mask-programmed gate arrays. Military, defense, and aerospace engineers are facing increased pressure to buy lower-cost, off-the-shelf products due to a declining defense budget and continually changing system requirements. In addition, in-system upgrades are possible with the Xilinx SRAM-based, in-system reconfigurable FPGAs, providing for improved field flexibility over traditional fixed logic implementation.

Pricing and Availability

The XC4005B, a 5,000-gate device, is available for \$455 in 100-piece quantities and is projected to be less than \$215 in volume by mid-1996. Prices are expected to continue to drop up to 30 percent per year as process technologies improve,

— more —

volumes increase, and costs decrease. Xilinx Hi-Rel FPGAs are available from 1,200 to 13,000 gates, and meet MIL-STD-883B requirements set by the Defense Electronics Supply Center (DESC). New pricing for the selected devices follows:

Xilinx Hi-Rel FPGA Price Decreases
100-piece Quantities

Device	# Gates	1994	1995	% Decrease	Volume mid-'96
XC4005	5,000	\$569	\$455	20%	< \$215
XC4010	10,000	\$1112	\$891	20%	< \$420

Xilinx Background

Founded in 1984, Xilinx is the world's leading supplier in the billion dollar CMOS programmable logic industry. The company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer peripherals, telecommunications, industrial control, instrumentation, and military markets. Headquartered in San Jose, Calif., the company produces innovative device architectures and associated development system software.

— 30 —

Xilinx, XC4005, XC4010, and Xilinx ASIC Estimator are trademarks of Xilinx, Inc.