



**FOR IMMEDIATE RELEASE**

Ann Dennis  
Xilinx, Inc.  
(408) 879-4726  
e-mail: amdennis@aol.com

Mary Jane Reiter  
Tsantes & Assoc.  
(408) 452-8700  
MCIMAIL: 6526090@mcimail.com

**XILINX EXPANDS ITS POPULAR XC7000 SERIES EPLDS  
FOR SMALL AREA, LOW POWER APPLICATIONS**

*Low Power Version of the XC7336 Device Provides  
Less Than One-Third Power and Cost of Equivalent PAL Solutions*

SAN JOSE, Calif., October 9, 1995—In a move to broaden its leading PAL consolidation offerings, Xilinx, Inc., (NASDAQ:XLNX), today announced the XC7336 “Q” device, a low power addition to its popular XC7300 complex programmable logic device (CPLD) family. Complementing the low cost, high performance XC7336 device, the new XC7336Q device’s low-power operation and packaging options provide customers with a high integration PAL-like solution that fits tight form factor constraints while enhancing overall system reliability.

“The introduction of the XC7336Q product squarely addresses the low power requirements of our expanding customer base and is a significant enhancement to our low-end product offering,” said Nick Kucharewski, general manager of the Xilinx EPLD division.

**Lower Power Applications**

The XC7336Q device with a low 50 mA supply current, integrates three to four PALs, thereby saving board space, reducing power consumption and heat generation. A device using lower power translates into cooler operation for applications such as computing, peripherals, and consumer electronics. For

example, add-in cards used for memory expansion, graphics, and fax modem cards require high performance in a very small area. Implementing the control logic on the board with multiple PALs wastes space, increases heat dissipation difficulties, and exacerbates power consumption problems. Similarly, in consumer markets, trends have been toward smaller electronic devices and entertainment accessories. The corresponding smaller and more crowded PC boards will be aided by the smaller and cooler operating XC7336Q devices.

“Increasingly, we’re seeing a demand for the benefits of freeing up board space with PAL consolidation,” said Scott Lewis, marketing manager of the CPLD division. “The XC7336Q device benefits customers by lowering total system power requirements, which reduces the burden on cooling and power systems.

“Additionally, the XC7336Q device offers high-performance, 10 nanosecond propagation delays, yet costs as little as one-third the equivalent PAL solution, i.e., three 22V10’s,” continued Lewis.

### **XC7336Q Device Unique Features**

In addition to its space and power advantages, the XC7336Q device also offers system “performance enablers” not currently offered on any other competitive 44-pin EPLD. For example, built-in 3.3V/5V level shifting capability on all I/Os which allow direct logic interfacing for today’s mixed voltage systems and the industry’s highest output drive at 24 mA which eliminates the need for external buffers.

### **Smaller, Thinner Packaging**

Lower supply currents on the XC7336Q device now make it an ideal candidate for the new smaller, thinner 44-pin PQ and VQ surface mount packages. Both JEDEC standard packages provide the optimum in performance versus board area.

## Xilinx Announces Newest Member of XC7300 Family

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<u>Package</u>	<u>length × width × height (mm)</u>	<u>Space saving vs. PLCC</u>
PLCC package	17.5 × 17.5 × 4.3	— —
PQFP package	13.2 × 13.2 × 2.0	43%
VQFP package	12.0 × 12.0 × 1.0	53%

### Supporting Software

The XC7336Q device will be supported in the next XACTstep Series 7000 V 8 software release. Customers wishing to design with the XC7336Q device immediately may use existing XEPLD v 5.1 software by specifying the standard XC7336 device as the target device. Programming support for the XC7336Q device on the new Xilinx HW-130 programmer is available now. Third party programmer support is expected by October 1995.

### Pricing and Availability

<u>Device/Speed</u>	<u>Package/Grade</u>	<u>Price (100 piece)</u>	<u>Production</u>
XC7336Q-15	PC44C	\$6.50	Now
XC7336Q-15	PQ44C	\$7.40	Now
XC7336Q-15	WC44C	\$22.80	Now
XC7336Q-10	PC44C	\$9.20	Now
XC7336Q-10	PQ44C	\$10.40	Now
XC7336Q-15	VQ44C	\$8.50	Q495
XC7336Q-10	VQ44C	\$12	Q495

## **Xilinx Announces Newest Member of XC7300 Family**

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Introduced in 1993, the XC7300 CPLD family is designed to address customer needs for high performance and high density in a single complex programmable logic device. The XC7300 family features an innovative dual block architecture consisting of two types of functions blocks. The XC7336 device consists of four PAL-like 24V9 fast function blocks interconnected by the 100% populated Universal Interconnect Matrix (UIM).

Founded in 1984, Xilinx is the world's largest supplier of programmable logic in the semiconductor industry. The company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripherals, telecommunications, networking, industrial control, instrumentation, and military markets. Headquartered in San Jose, Calif., the company produces innovative device architectures and enabling development system software.

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