



Evelyn Hart
Xilinx, Inc.
(408) 879-5047
evelyn.hart@xilinx.com

Mary Jane Reiter
Tsantes & Associates
(408) 452-8700
maryjane@tsantes.com

FOR IMMEDIATE RELEASE

XILINX EXTENDS FPGA INDUSTRY'S MOST SUCCESSFUL CORE PROGRAM

Faster FPGA Enables Delivery of New Xilinx LogiCore™ PCI "Initiator" Module

SAN JOSE, Calif., July 29, 1996 -- Xilinx, Inc., (NASDAQ:XLNX), the world leader in CMOS programmable logic devices, today announced it has extended the FPGA industry's most successful core program with the delivery of its second, 100 percent compliant PCI core, the Xilinx LogiCore "Initiator" module. Previously, with the LogiCore PCI "Target," the module could only perform slave functions. With the new module, a customer can now complete a master interface design in which the Initiator module acts as a controller for the PCI bus. As a result, designers can reduce design risk and cut development time by at least nine months.

Additionally, the company today announced a new speed grade for the XC4000E family. The -2 speed grade, which is now shipping across the entire product family, is up to 20 percent faster than the -3. The new XC4013-2 speed grade enables the delivery of the PCI Initiator module, which requires a high level of design complexity and performance.

The modules were closely developed with beta customers who have successfully implemented the design in an XC4013-2 FPGA. These devices operate at 33 Mhz and include high speed burst synchronous FIFOs. They can be customized to fit individual design requirements resulting in a cost effective single chip solution.

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2100 Logic Drive • San Jose, CA 95124-3400
Telephone: 408-559-7778 • FAX: 408-559-7114

Xilinx Expands Core Offerings

“The LogiCore PCI Initiator modules have been optimized for the XC4013-2 devices in order to ensure the highest performance possible for the modules in the 13,000 gate range, which we currently see as the sweet spot in the market,” said Chuck Fox, vice president of Marketing.

As a mark of the PCI LogiCore’s success, Xilinx has sold over 150 PCI Target modules since the first introduction less than six months ago.

To complement Xilinx’s product offerings, the LogiCore Alliance program provides designers with additional modules developed by leading third party intellectual property (IP) providers that are optimized and verified for Xilinx programmable logic devices. Xilinx ensures high quality by certifying third party cores. Xilinx provides LogiCore partners with training and development tools to ensure designers’ requirements are met with the highest quality drop-in modules available. Customers can choose from a broad range of complexity and application types to complete high performance, high density programmable logic designs.

Pricing and Availability

The combination Xilinx PCI Initiator/Target module for Xilinx FPGAs complete with a data sheet, comprehensive user guide and PCI Systems Architecture textbook is available for designing with Viewlogic schematics, VHDL and Verilog for \$8,995. The Target-only interface is available for \$4,995. All PCI Target customers will receive a free upgrade to the Target/Initiator modules.

The XC4000E-2 devices begin at \$30 today in 5,000 piece quantities. By 2H97 the high volume price will be \$11. A mask programmed HardWire™ gate array version will be available for \$6.50. The XC4013E-2 devices begin at \$185 today in 5,000 piece quantities and will be as low as \$55 in high-volume production in 2H97. A HardWire version will be available in high volume for \$25 in 2H97.

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Founded in 1984, Xilinx is the world's largest supplier of programmable logic solutions comprising industry leading device architectures and world class design software. Headquartered in San Jose, Calif., the company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high

integration and quick time-to-market for electronic equipment manufacturers in the computer, peripherals, telecommunications, networking, industrial control, instrumentation and high reliability/military markets.

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To the extent that matters covered in this release are forward looking statements, they involve risks and uncertainties, including timely availability and acceptance of new products, the ability of these products to meet performance specifications, and other risks that are described in the company's SEC filings.

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