



FOR IMMEDIATE RELEASE

Contacts:

Ann Dennis

Xilinx, Inc.

(408) 879-4726

INTERNET: amdennis@aol.com

Mary Jane Reiter

Tsantes & Associates

(408) 452-8700

INTERNET: 6526090@mcimail.com

**XILINX SETS NEW STANDARD FOR WORLD'S FASTEST FPGA WITH
POPULAR XC3100A DEVICE FAMILY**

SAN JOSE, Calif., October 16, 1995—Xilinx, Inc. (NASDAQ:XLNX), the world's largest supplier of programmable logic, today announced it has redesigned its leading high-performance XC3100A family to be the industry's fastest FPGA. The new XC3100A devices were designed with an optimized triple layer metal, 0.6 micron CMOS process, compared with the two layer metal and 0.8 micron process previously used. This change in process and relayout of the XC3100A-2 device has resulted in a 50 percent performance improvement in the new XC3100A-09 device. Additionally, the company has reduced prices by as much as 45 percent on its existing XC3100A speed versions.

The result of a joint development effort between Xilinx and its foundry partners, the new offerings are bitstream and pin-compatible with the leading XC3100A family and are therefore backward compatible. This allows current designs using XC3100A FPGAs to take immediate advantage of the increased speed by simply inserting the faster speed grades into existing XC3100A sockets,

— more —

**2100 Logic Drive • San Jose, CA 95124-3400
Telephone: 408-559-7778 • FAX: 408-559-7114**

providing instant upgrades. The two new versions of the family, the XC3100A-1 and XC3100A-09 devices, denote new speed grades and span 1,000 to 7,500 usable gates. The XC3100A family is comprised of six devices, each consisting of six speed grades, and are offered in 35 package-device combinations, including TQFP and VQFP packages especially for PCMCIA applications.

“The release of the new XC3100A devices is in keeping with our strategic commitment to offer customers the highest performance solutions for lower-end density requirements,” said Chuck Fox, vice president product marketing. “Based on published information for a comparable, competitive device family, we calculated that our XC3100A family offers performance increases ranging from 15 to 60 percent in functions including counters, accumulators, and datapath operations.”

Highest-Speed FPGAs

The improvements made over the XC3100A-2 device are yielding faster speed benchmarks in Xilinx's XC3100A-09 device in clock to out (pin-to-pin) (6 ns), logic block delay (1.4 ns), pin to logic to pin (6 ns), 16-bit Johnson counter (320 MHz), 16-bit prescaled loadable counters (230 MHz), 8-bit accumulator (133 MHz), and datapath (335 MHz).

Compared to the XC3100A-2 device, the XC3100A-09 device has up to 40 percent faster block delays; 50 percent faster chip-to-chip speed; and is 50 percent

— more —

faster than the XC3100A-2 device for decoding functions. In addition, the XC3100A-09 is nearly twice as fast in interconnect speed. Dramatically faster interconnect speeds enable a significant decrease in routing delays, which means shortened design cycles for users.

As with all XC3100A devices, the new speed versions are fully PCI compliant. Additionally, the increased speed makes the new devices ideal for high-speed serial to parallel conversion, fast ATM switches, video control functions, and 3D graphics and imaging.

XACTstep Software for XC3100A-1 and XC3100A-09 Design

XACTstep Version 6 software, the new full feature EDA development tool set, now supports the XC3100A-1 and XC3100A-09 devices. Through the combination of extremely fast silicon and Xilinx's timing driven placement solution, XACTstep Performance™ allows designers greater productivity while increasing the development capability and predictability for high-performance designs. XACTstep version 6.0 software includes six powerful tools, including Design Manager, Hardware Debugger, and the programmable logic industry's first floorplanning tool that enables customers to optimize performance and maximize density, in an easy-to-use environment. All are accessible through graphical tool bars, icons, templates and pop-up menus on Microsoft Windows™ and workstation versions.

— more —

XC3100A-09 Pricing and Availability

The XC3100A family has shipped over two million units since its introduction in 1993. Pricing for the XC3100A family, Xilinx's fastest growing FPGA family, starts at \$10. Among the newest device speed grade offering, the XC3120A-09 (PC68C package) begins at \$22 in volume quantities.

<u>PC84 packages</u>	<u>Usable gates</u>	<u>Sample</u>	<u>Production</u>
XC3190A-09	6,000	Now	Q495
XC3195A-09	7,500	Now	Q495
XC3120A-09	1,000	Q495	Q196
XC3130A-09	2,000	Q495	Q196
XC3142A-09	3,000	Q495	Q196
XC3164A-09	4,500	Q495	Q196

Founded in 1984, Xilinx is the world's largest supplier of programmable logic in the semiconductor industry. The company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripherals, telecommunications, networking, industrial control, instrumentation, and military markets. Headquartered in San Jose, Calif., the company produces innovative device architectures and enabling development system software.

— 30 —

For more information on Xilinx, access our World Wide Web site at "<http://www.Xilinx.com>". Note to editors: XACTstep, Floorplanner, Design Manager, Hardware Debugger, and all XC-prefix products referenced above are trademarks of Xilinx, Inc. Xilinx is a registered trademark of Xilinx, Inc. Other brand or product names are trademarks or registered trademarks of their respective owners.