

What's changed between version 1 and version 1a of the Documentation Update Pack

1. The **Compiling VHDL Designs Using Foundation XVHDL** and **Accessing Xilinx Device Features Using Foundation XVHDL** application notes have been updated.
2. The **What's New in Foundation 6.0.1** Help file has been replaced by **What's New in Foundation 6.0.2..** In addition, you can refer to the WHATNEWX.HLP Help file in the ACTIVE\VHDL directory for information on what has changed in the XVHDL compiler between version 2.3.6, which shipped with Foundation 6.0.1, and version 2.4.4, which is included in the Foundation Service Pack.
3. The **Simulator Command Macro File Compatibility Notes** applications note has been removed, as most of the issues covered in that document have been addressed in the Foundation Service Pack.

Notes on the Documents

1. The **What's New in Foundation 6.0.2** Help file provides detailed information on the software functionality issues that are addressed by the Foundation Service Pack. Enhancements, bug fixes, and known issues are covered.
2. The **Simulation Guide** is an Adobe® Acrobat® version of a slide presentation that has been enhanced with Acrobat hotlinks, which are outlined in red.
3. The **Improved Simulation Tutorial** uses the CALC design found at ACTIVE\PROJECTS\CALC and is intended to replace the FLASH design-based tutorial found in ACTIVE\DOC\TIMING.PDF.
4. The **Compiling VHDL Designs Using Foundation XVHDL** application note covers methodologies for multi-source file designs.
5. **Accessing Xilinx Device Features Using Foundation XVHDL** provides explanation and design examples on how to take advantage of Xilinx architectural features such as I/O flip-flops, global buffers, global reset, slew rate control, RAM/ROM, etc.
6. **Board Level Simulation with Xilinx Foundation Series** discusses the methodology for simulating top-level designs that contain one or more Xilinx devices.

[Return to Hot Links page](#)