

# Hot Sheet

## 1) Device Information:

During the course of developing this beta, many different XC4000XL device files were included in the software. Please realize that the list of XC4000XL devices that will *actually* be supported by Xilinx can and do change over time.

The directories that list the XC4000XL devices in this beta release do not indicate whether a particular device will be supported in the final release of the software. That is, the presence of a XC4000XL device type in this beta software does not guarantee that it is supported. Conversely, there may be XC4000XL devices that Xilinx will be supporting that are not included in this beta.

The following is a list of the XC4000XL devices that can be found in this beta software.

<b>Device</b>	<b>Device Package</b>
xc4005xl	PC84, PG156, PQ100, PQ160, PQ208, TQ144, CB164
xc4010xl	PQ160, PG191, PQ208, HQ208, BG225, CB196
xc4013xl	PQ160, PQ208, HQ208, PG223, BG225, PQ240, HQ240, CB228
xc4020xl	PG223, HQ208, HQ240, HQ208
xc4028xl	HQ240, PG299, HQ304, BG352
xc4036xl	HQ304, PG411, BG432
xc4044xl	BG432, PG411
xc4052xl	BG432, PG411
xc4062xl	BG560, PG475

### **Please review the following important changes to the above list:**

The following list represents the XC4000XL device package combinations *that are currently in* this version of the beta software but are NOT planned devices by Xilinx and will NOT be supported in the final version of the M1 software.

xc4005xl	CB164	PG156			
xc4010xl	BG225	CB196	PG191	HQ208*	
xc4013xl	HQ208	PG223	BG225	HQ240*	CB228
xc4020xl	HQ208*	HQ240*	PG223		

\* Will be available in PQ package.

Please do not use the HQ208 or PQ208 package as these versions have not been verified.

The following list of device package combinations *are* in the NT Beta and can be used to design with the equivalent PQ package. The HQ and PQ packages are pinout compatible.

xc4013xl	HQ240 for HQ240
xc4020xl	HQ240 for PQ240

## 2) Bitstream Generation

Bitstream Generation is not working for the following devices:

xc4005xl  
xc4010xl  
xc4013xl  
xc4020xl

## 3) Speed Files and Device Performance

The -3 and -4 speed grades for the XC4028EX and the XC4036EX are tagged "preliminary". The performance is very close to actual/final.

In general, the current -3 XL speed file for the XC4000XL family is "non-mature". It is based on the XC4000EX -3 speed data and still needs some further refinement before it can be tagged "preliminary". However, the performance is very close to actual/final.

Regarding the XC4000XL devices, this beta software has been tuned and tested for the following (2) devices:

XC4036XL  
XC4028XL

The following (7) XL devices are untuned and only lightly tested by Xilinx.

XC4044XL  
XC4052XL  
XC4062XL  
XC4020XL  
XC4013XL  
XC4010XL  
XC4005XL

- 4) The CPLD Software (for 7300 and 9500 designs) is still “work in progress” and is not fully functional at this time. Therefore, the 7300 and 9500 devices are NOT supported in this beta.
- 5) LogiBlox has been failing due to memory problems.
- 6) Running this software from the CD is not working at this time.
- 7) Lab Install is not working at this time.
- 8) De-Install is not working at this time.
- 9) Please do not create directory names that have spaces in them. Any directory name with spaces are not handled properly.
- 10) Before running the Design Manager / Flow Engine, open the Explorer and double click on \$XILINX\bin\nt\revengine.exe.
- 11) Regarding the interaction of the Design Manager and the Flow Engine: pop-up menus may appear behind the Flow Engine, but in front of the Design Manager. Work around this by dragging the Flow Engine out of the way.
- 12) Guided mapping in this release is not achieving a very high percentage of guided components. This observation is relative to the 4000EX pre-release, as well as to our own standards. The NT Beta 2 release will incorporate significant improvements to guided mapping.