

Manual Update Sheet

DATE: June 1, 1998

Document Being Updated: *TMS320C3x/C4x Assembly Language Tools User's Guide*

Literature Number Being Updated: SPRU035C

Manual Included in a Kit: No

This Manual Update Sheet (SPRZ131) ships with the *TMS320C3x/C4x Assembly Language Tools User's Guide*.

Updates within paragraphs appear in a **bold typeface**.

Page: Change or Add:

- | | |
|------|--|
| 3–6 | <p>Include two suboptions in the <code>–v</code> option description.</p> <p style="margin-left: 40px;"><code>–v</code> specifies a version. The version tells the assembler which of the following TMS320C3x/C4x devices it should produce code for (The default is <code>–v30</code>):</p> <p style="margin-left: 80px;"><code>–v30</code> selects the TMS320C30
 <code>–v31</code> selects the TMS320C31
 <code>–v32</code> selects the TMS320C32
 <code>–v40</code> selects the TMS320C40
 <code>–v44</code> selects the TMS320C44</p> <p style="margin-left: 40px;"><code>–msrevx</code> the <i>x</i> = 1,2,3, etc.
 specifies the silicon revision of the target chip to enable additional functionality for more recent silicon releases. Used in conjunction with <code>–v</code> flag.
 Example:
 <code>–msrev6 –v31</code> : enable new (1996) 'C31 parallel instructions
 <code>–msrev2 –v32</code> : enable new (1996) 'C32 parallel instructions</p> |
| B–10 | <p>Change the fifth bullet.</p> <p style="margin-left: 40px;"><input type="checkbox"/> Size is the number of bits of memory required to contain this variable.</p> |

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