

July 1996

## DESCRIPTION

The SSI 34R3430R is a BiCMOS monolithic integrated circuit designed for use with center-tapped ferrite or MIG recording heads. It provides a low noise read path with selectable gains of 85 and 250 V/V, write current control, and data protection circuitry for as many as 4 channels. Power supply fault protection is provided by disabling the write current generator during power sequencing. A power down mode (idle) is provided to reduce power consumption to 3 mW nominal.

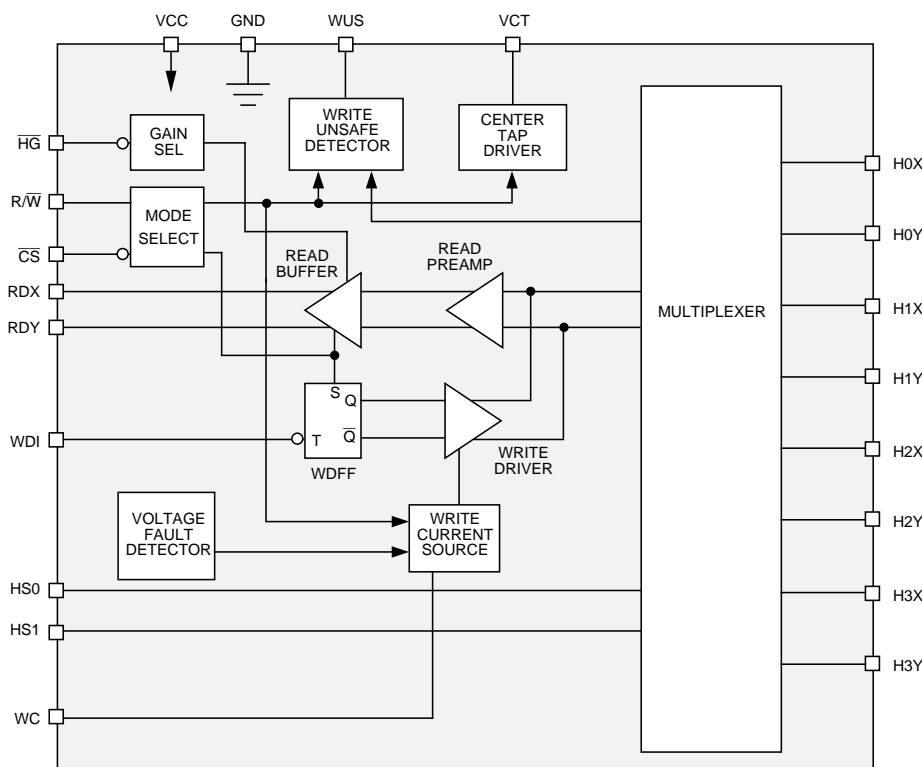
Internal 750  $\Omega$  damping resistors are provided. It requires only a +5 V power supply and is available in a surface mount package.

The SSI 34R3430R replaces the 32R1203R.

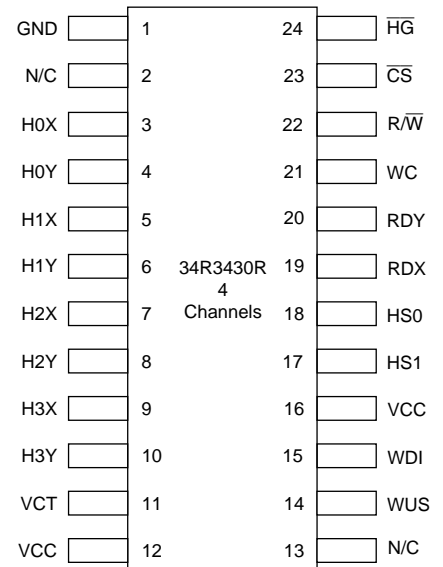
## FEATURES

- Pin selectable gain, 250 V/V and 85 V/V
- +5 V only power supply
- Low power
  - 125 mW nom read mode
  - 3 mW nom idle mode
- High Performance
  - Input noise = 1.2 nV/ $\sqrt{\text{Hz}}$  max
  - Input capacitance = 17 pF max
  - Write current range = 10 - 50 mA
  - Head voltage swing = 6 Vpk
- Designed for center-tapped ferrite or MIG heads
- Power supply fault protection
- Includes write unsafe detection
- Enhanced write to read recovery

## BLOCK DIAGRAM



## PIN DIAGRAM



24-Lead VSOP