

January 1997

DESCRIPTION

The SSI 34P3211 is a high performance BiCMOS single chip read channel IC that contains all the functions needed to implement zoned recording for high density tape storage applications. Functional blocks include the pulse detector, programmable filter, time base generator, and data synchronizer. VCO range of 0.6 MHz to 13 MHz is selectable through the serial port.

Programmable functions of the SSI 34P3211 device are controlled through a bi-directional serial port and banks of internal registers. This allows zoned recording applications to be supported without changing external component values from zone to zone.

The SSI 34P3211 utilizes an advanced BiCMOS process technology along with advanced circuit design techniques which result in a high performance device with low power consumption. The SSI 34P3211 supports a sleep mode for minimal power dissipation in non-operational periods.

The SSI 34P3211 is available in a 64-Lead TQFP package.

FEATURES

- Complete zoned recording application support
- VCO range: 0.6 MHz to 13 MHz selectable through the serial port
- Supports 1,7 RLL, MFM, FM, and GCR Encoding Format
- Fast attack/decay modes for rapid AGC recovery
- Dual rate charge pump for fast transient recovery
- Programmable cutoff frequency of 0.4 to 4 MHz
- Tracking Threshold for rapid LEVEL discharge
- Time base generator with better than 1% frequency resolution
- Fast acquisition phase lock loop with zero phase restart technique
- Fully integrated data synchronizer with programmable window shift control
- Programmable write precompensation
- Write equalization
- Write current DAC
- Bi-directional serial port for register access
- Register programmable power management (sleep mode < 5 mW)
- Low Operating Power (TBD mW typical @ 5 V)

SSI 34P3211

Read Channel for

Tape Storage

BLOCK DIAGRAM

