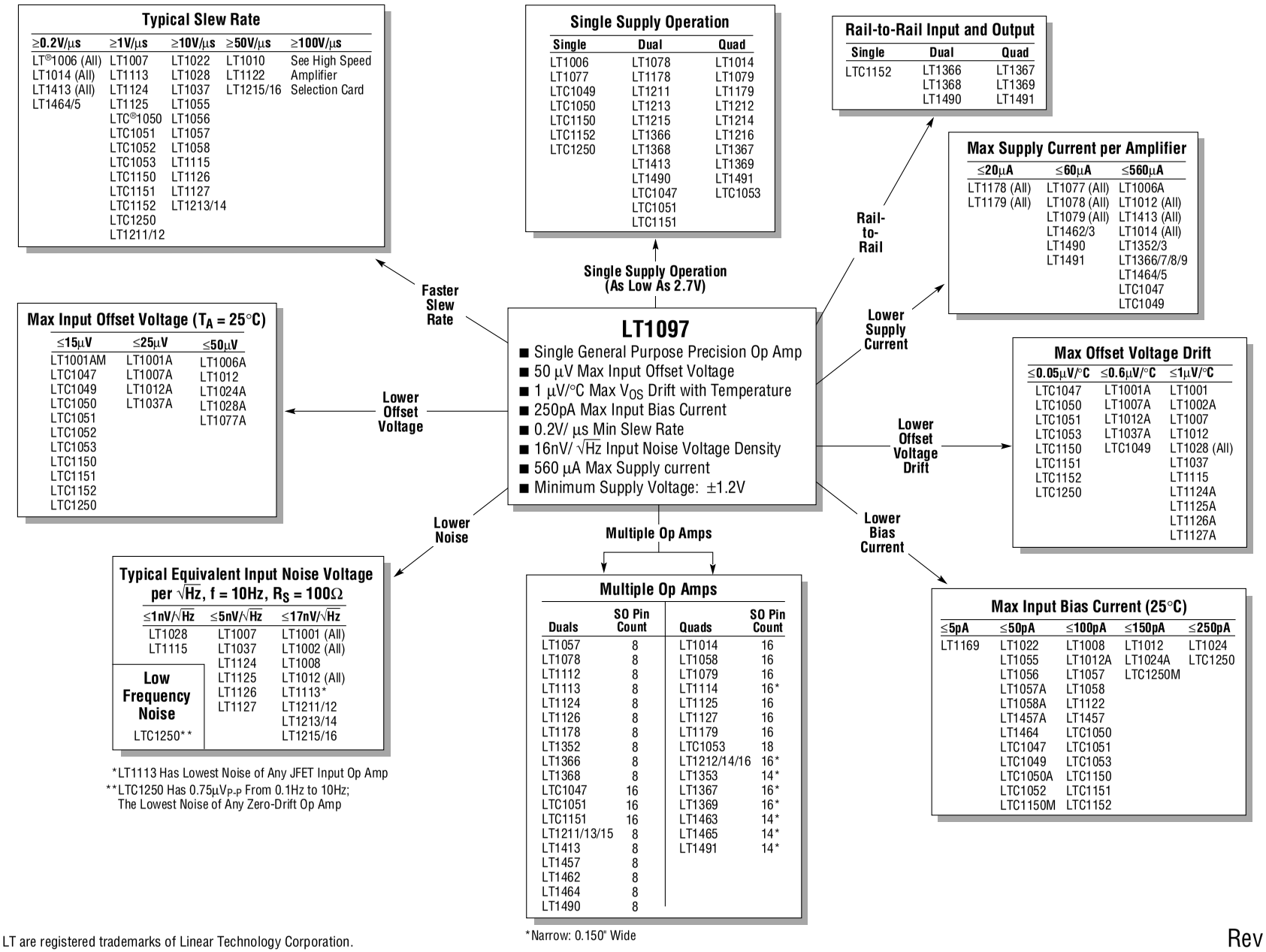


*LT1113 Has Lowest Noise of Any JFET Input Op Amp

**LTC1250 Has 0.75µVP-P From 0.1Hz to 10Hz; The Lowest Noise of Any Zero-Drift Op Amp

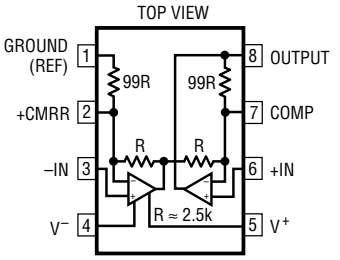
*Narrow: 0.150" Wide

Complete Instrumentation Amplifiers in 8-Pin Packages

- LTC1100: Zero Offset, Drift; Gain of 100
- LT1101: Micropower, Single Supply; Gain of 10 or 100
- LT1102: High Speed JFET Input; Gain of 10 or 100

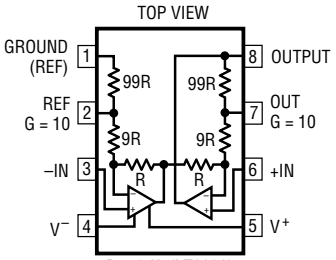
PARAMETER	LTC1100A $V_S = \pm5V$	LT1101A $V_S = 5V$	LT1102A $V_S = \pm15V$
Offset (Max)	10µV	160µV	600µV
Offset Drift (Max)	100nV/°C	2µV/°C	8µV/°C
Bias Current (Max)	50pA	8nA	40pA
Noise (0.1Hz to 10Hz)	1.9µV_{P-P} Typ	0.9µV_{P-P} Typ	2.8µV_{P-P} Typ
Gain	100/10 (SW PKG)	10/100	10/100
Gain Error (Max)	0.05%	0.05%	0.05%
Gain Drift	4ppm/°C Typ	4ppm/°C Max	18ppm/°C Max
Gain Nonlinearity (Max)	8ppm	8ppm	14ppm
CMRR (G = 100)(Min)	104dB	95dB	84dB
Power Supply (Max)	Single, Dual, 16V	Single, Dual, 44V	Dual, 44V
Supply Current (Max)	2.8mA	130µA	5mA
Slew Rate	1.5V/µs Typ	0.06V/µs Min	21V/µs Min (6:10)
Bandwidth (G = 10)	18kHz Typ	22kHz Min	2MHz Min

LTC1100



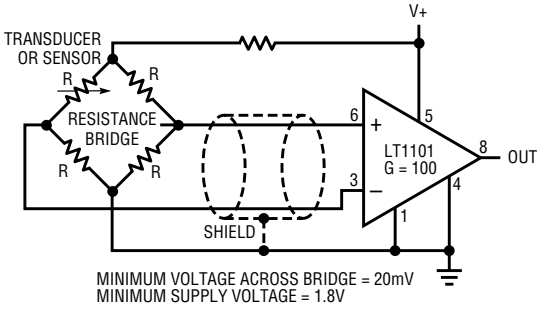
N8 PACKAGE 8-LEAD PLASTIC DIP
J8 PACKAGE 8-LEAD CERAMIC DIP

LT1101/LT1102



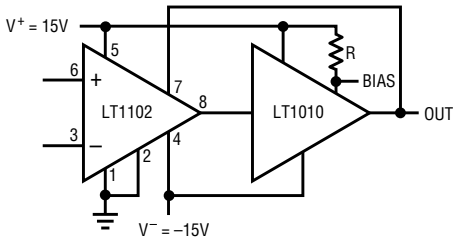
N8 PACKAGE 8-LEAD PLASTIC DIP
J8 PACKAGE 8-LEAD CERAMIC DIP

Differential Voltage Amplification from a Resistance Bridge (Single 5V Powered)



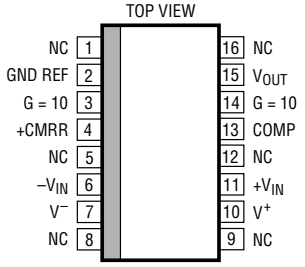
MINIMUM VOLTAGE ACROSS BRIDGE = 20mV
MINIMUM SUPPLY VOLTAGE = 1.8V

Wideband Instrumentation Amplifier with ±150mA Output Current



OUTPUT = ±10V INTO 75Ω TO 330kHz (R = 50Ω)
±10V INTO 200Ω TO 330kHz (R = 200Ω)
DRIVES 2.2nF CAP LOAD
GAIN = 10, DEGRADED 0.01% DUE TO LT1010

LTC1100CS



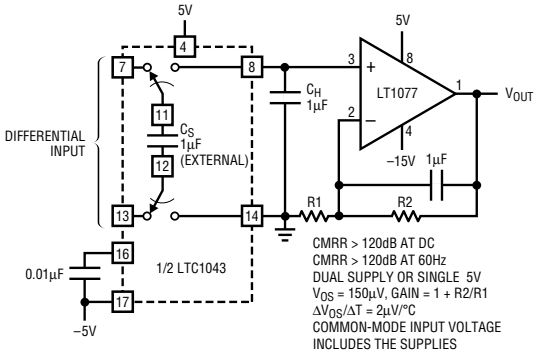
S PACKAGE
16-LEAD PLASTIC SW

Dual Precision Instrumentation Switched Capacitor Building Block: LTC1043

- Up to 120dB CMRR
- Adjustable Gain-Set by Output Op Amp
- Offset and Offset Drift as Low as Output Amp Specs
- Precise, Charge-Balanced Switching
- Up to 5MHz Clock Rate
- Internal or External Clock

PARAMETER	LTC1043 (USING LTC1050 AMPLIFIER)
Offset	0.5µV
Offset Drift	50nV/°C
Bias Current	10pA
Noise (0.1Hzk to 10Hz)	1.6µV
Gain	Resistor Programmable
Gain Error	Resistor Limited 0.001% Possible
Gain Drift	Resistor Limited <1ppm/°C Possible
Gain Nonlinearity	Resistor Limited 1ppm Possible
CMRR	120dB
Power Supply	Single, Dual (18V, ±9V Max)
Supply Current	2mA
Slew Rate	1mV/ms
Bandwidth	10Hz

Instrumentation Amplifier



CMRR > 120dB AT DC
CMRR > 120dB AT 60Hz
DUAL SUPPLY OR SINGLE 5V
V_{OS} = 150µV, GAIN = 1 + R2/R1
$\Delta V_{OS}/\Delta T$ = 2µV/°C
COMMON-MODE INPUT VOLTAGE INCLUDES THE SUPPLIES

CMRR vs Frequency