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Please note the following additions to page 1 of the LTC[®]1481 data sheet.

FEATURES

- ESD Protection Over $\pm 10\text{kV}$

DESCRIPTION

The driver and receiver feature three-state outputs, with the driver outputs maintaining high impedance over the entire common mode range. Excessive power dissipation caused by bus contention or faults is prevented by a thermal shut-down circuit which forces the driver outputs into a high impedance state. The receiver has a fail-safe feature which guarantees a high output state when the inputs are left open.

I/O pins are protected against multiple ESD strikes of over $\pm 10\text{kV}$.

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